

Tracking R&D Review Beijing

Monday, 5 February 2007

Open Session (09:30 - 21:00)

time	[id] title	presenter
09:30	[1] Welcome	
09:40	[2] LCTPC	
	- [1] Overview of the LCTPC Effort	SETTLES, Ron
	- [2] Results from Prototypes-I and Software Status	PETERSON, Dan
	- [3] Results from Prototypes-II and Electronics Developments	DIXIT, Madhu
	- [4] Coffee Break	
	- [5] Progress in the CMOS Pixel TPC Concept	TIMMERMANS, Jan
	- [6] Plans for Future R&D Measurements	MATSUDA, Takeshi
	- [7] Discussion	
12:30	Lunch	
13:30	[3] 4th Concept Tracking	
	- [1] Cluster Counting Drift Chamber for ILC (a viable alternative to TPC?)	GRANCAGNOLO, Francesco
	- [2] Discussion	
14:25	[4] SiLC	
	- [1] Motivations for Using Si Tracking and Main R&D Objectives	SAVOY-NAVARRO, Aurore
	- [2] R&D on Mechanics: main issues	VOS, Marcel
	- [3] R&D on Sensors	LOZANO, Manuel KIM, Hongjoo
	- [4] Coffee	
	- [5] R&D on Electronics and Elementary Modules	SCHUMM, Bruce SAVOY-NAVARRO, Aurore
	- [6] Simulations	SAVELIEV, Valeri
	- [7] Test benches and test beams	SAVOY-NAVARRO, Aurore
	- [8] Discussion	
19:00	Tracking Review Dinner	