

CALICE meeting

Tuesday, 13 February 2007

EUDET modules, electronics & DAQ - Sem 4a (09:00 - 10:30)

time	[id] title	presenter
09:00	[13] HCAL module structure	REINECKE, Mathias
09:20	[10] HCAL heat dissipation and power management	GOETTLICHER, Peter
09:40	[37] Study of 1.5 m data paths using a model slab	HOMMELS, Bart
10:00	[38] General work on the DAQ system from the slab backwards	WARREN, Matthew