# Status of R&D at DESY

Waldemar Koprek on behalf of LLRF team

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# **R&D** activities at DESY

- Automatization
- Klystron linearization
- Piezo control
- Controller of sc cavities
- RF-gun controller
- New IF for field detection
- Software for ATCA LLRF control system

#### **Automated Accelerator Operation**

High degree automation of accelerator operation





## **Klystron linearization**

- How close to saturation can we operate? (10%? 3%?)
- Klystron linearization implemented in Simcon 3.1
  - -measurements of nonlinearities in high power chain

06.

-0.2

-0.25

-0.2

-0.1

0.1

0.2

- first results of operation



0.05 0.1 0.15

-0.1

-0.15

-0.15 -0.1 -0.05



First preamplifier output. Circles corresponding 1-7mW





### Piezo control



### Controller of sc cavities

#### SIMCON 3.1 at:

- 1. FLASH-ACC1 8 cavities (permanently)
- 2. FLASH-ACC2/3 with 2 boards test of 16 cavities
- 3. MTS 8 cavities (permanently)
- 4. CHECHIA permament installation in April 07
- 5. FNAL at CC1
- 6. FNAL at Meson Hall





#### 17.02.2007 - FLASH - 800 bunches



## **RF-gun controller**

#### SIMCON 3.1 at:

- RF-Gun at FLASH permanently
- RF-Gun in Zeuthen permanently
- BESSY gun tested in Zeuthen with probe
- RF-gun in PSI plan for 07



#### Phase stability of beam in RF-gun at FLASH

rf control	phase change over time	phase r.m.s. bunch to bunch	phase r.m.s. pulse to pulse
rf drive only	<b>2.2°</b> /400 μs	0.73°	0.50°
PI control	<mark>0.7</mark> °/400 μs	0.18°	0.17°
AFF(1) PI(9)	$0.3^{\circ}/400\mu s$	0.10°	0.13°
AFF and P	as above	as above	as above
AFF and PI	unstable	unstable	unstable

#### New IF for field detection



- multi-channel IQ detector implemented in SIMCON controller
- IQ detector tested in ACC1 with IF=9MHz and Fs=54MHz
- quality measurement in MTS  $\rightarrow$

$$I = \frac{2}{M} \sum_{i=0}^{M-1} x_i \sin(i\alpha)$$
$$Q = \frac{2}{M} \sum_{i=0}^{M-1} x_i \cos(i\alpha)$$



## ATCA Software - Solution A



# **ATCA Software - Solution B**

Each board process all three kinds of signals



# ATCA Software – in progress

- IPMI controller
- Communication protocols
  - PCI Express
  - Gb Ethernet
  - Low Latency Communication Links
  - Communication between FPGA and DSP

### Thank You