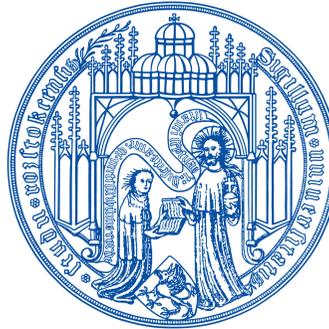


TDC electronics for the TPC

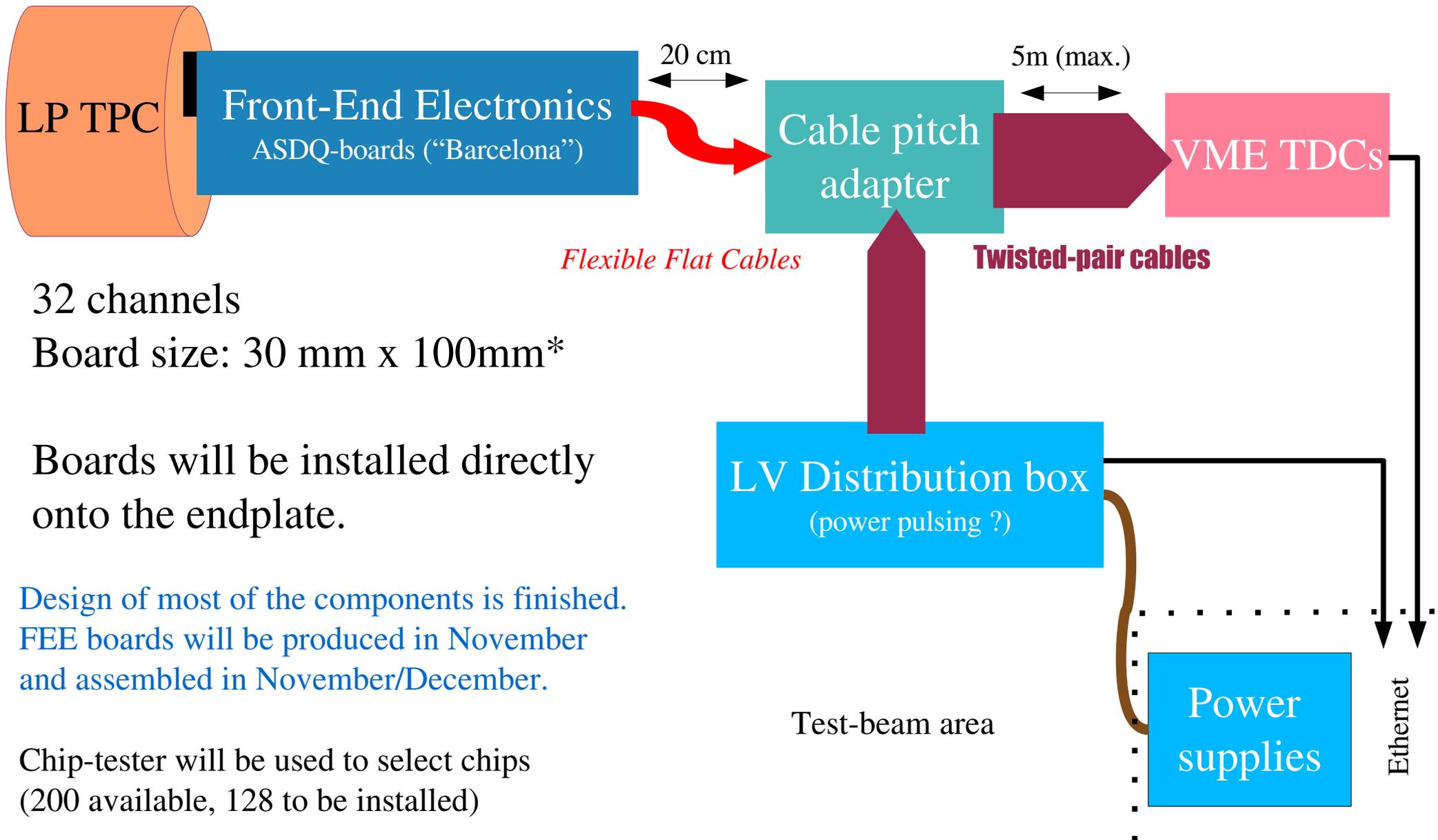


Universität Rostock

A.Kaukher O.Schäfer H.Schröder R.Wurth

EUDET Annual Meeting 2007
École Polytechnique, Paris

TDC-based readout electronics for LP TPC



32 channels

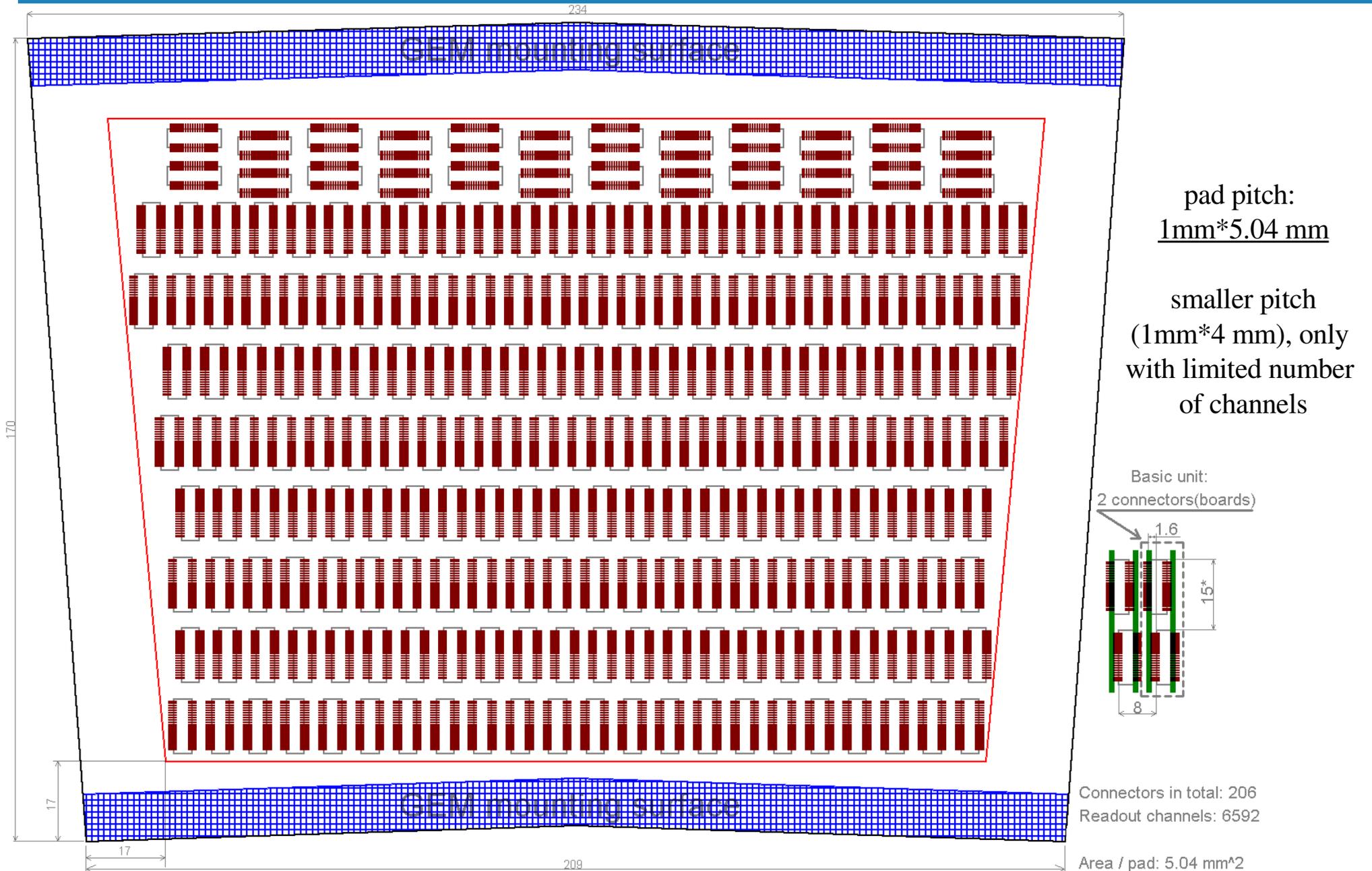
Board size: 30 mm x 100mm*

Boards will be installed directly onto the endplate.

Design of most of the components is finished.
FEE boards will be produced in November and assembled in November/December.

Chip-tester will be used to select chips
(200 available, 128 to be installed)

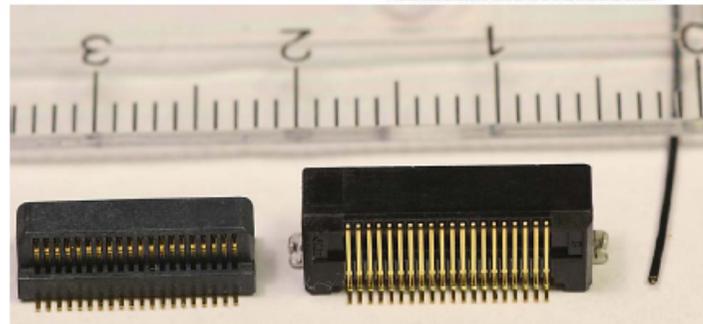
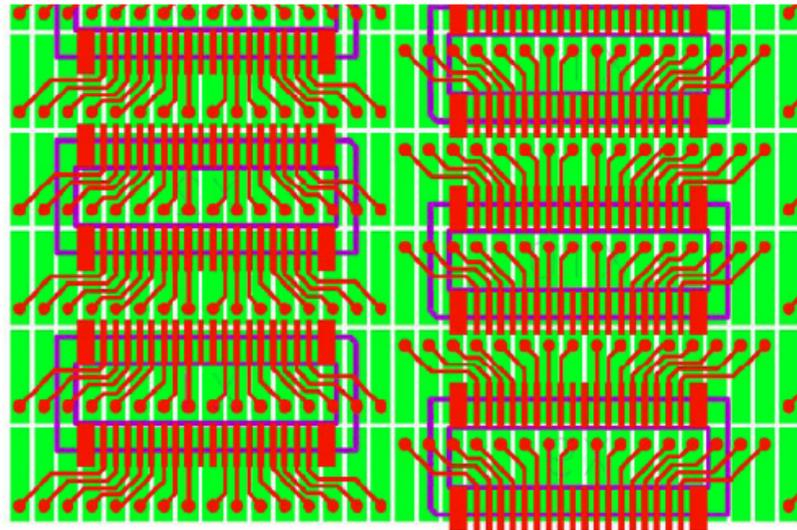
A Padplane: study for LP TPC



Readout electronics interface

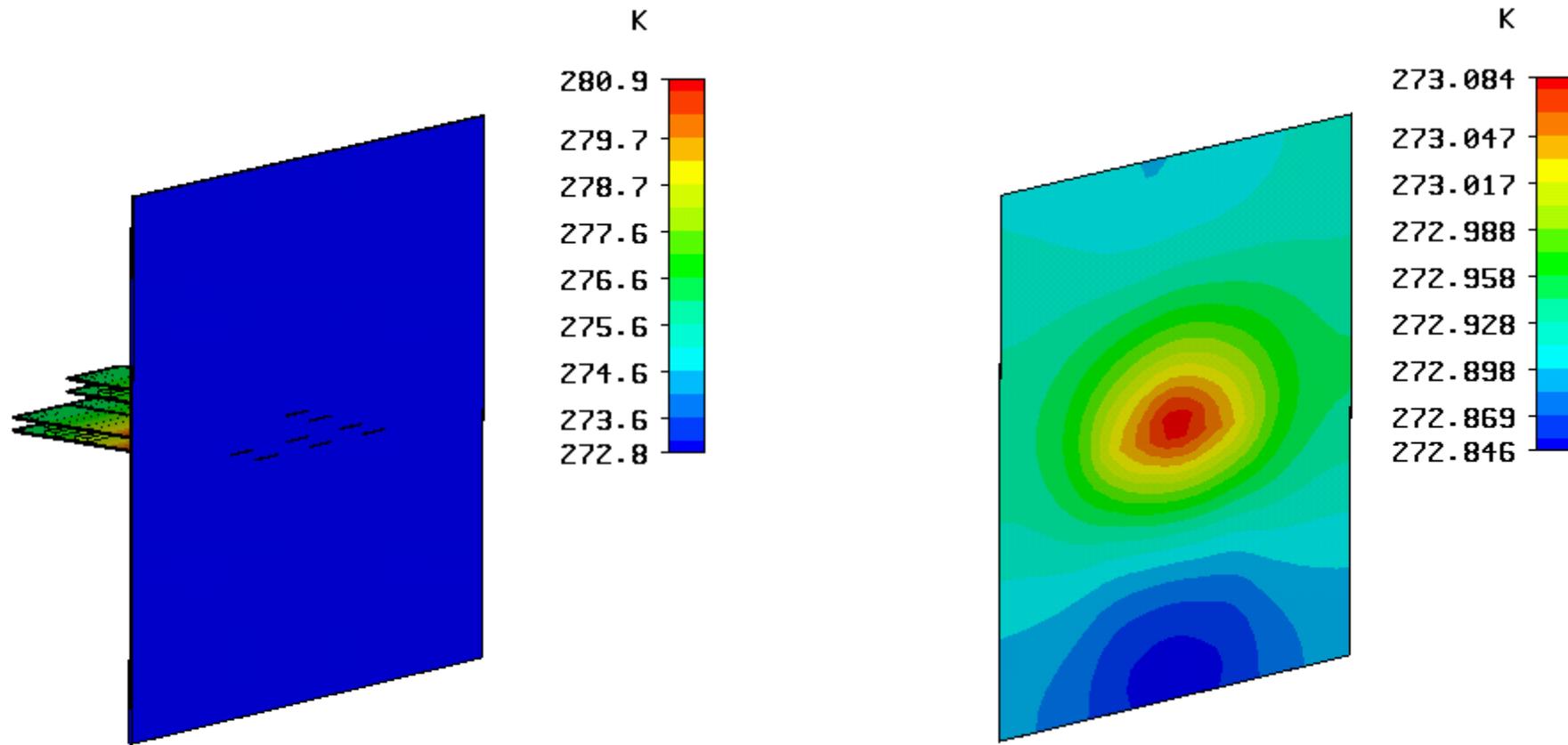
Example of signal routing from $1 \times 4 \text{ mm}^2$ pads to the WR-40S connector

- Smallest foreseeable pad size $1 \times 4 \text{ mm}^2$
- The connector should match this size
- Highest possible flexibility in pad geometry \Rightarrow small modules
- Japan Aviation Electronics offers a 40 pin connector with 0.5 mm pitch and dimensions $13.9 \times 4.7 \text{ mm}^2$
- 32 pins for signals and 8 pins for grounding



L. Jönsson, EUDET Annual meeting 2006

Thermal simulation



Preliminary

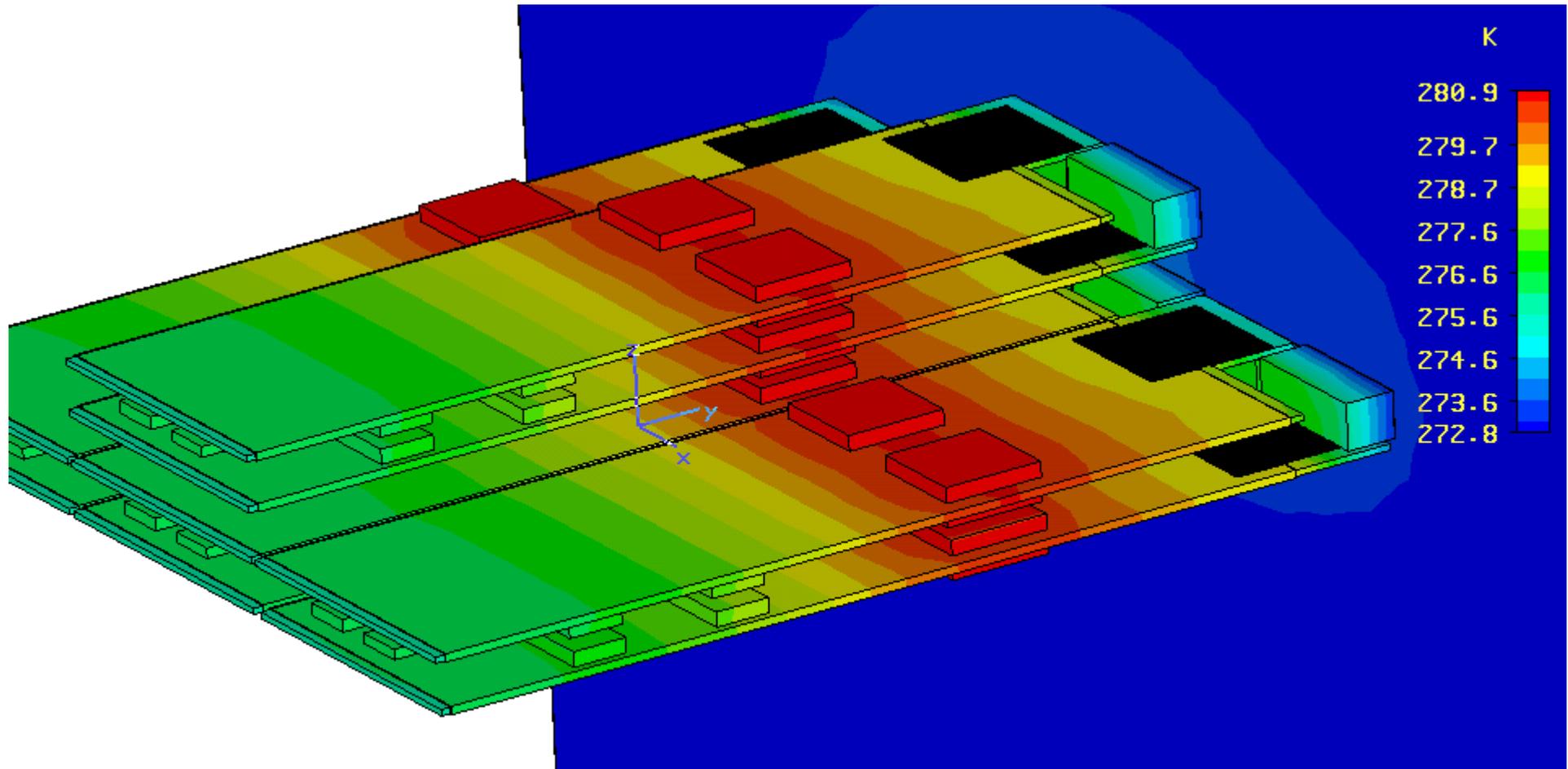
Static thermal simulation with *CST EM Studio*

ASDQ power dissipation: 40mW/channel

Ambient temperature: 273.1 degree Kelvin

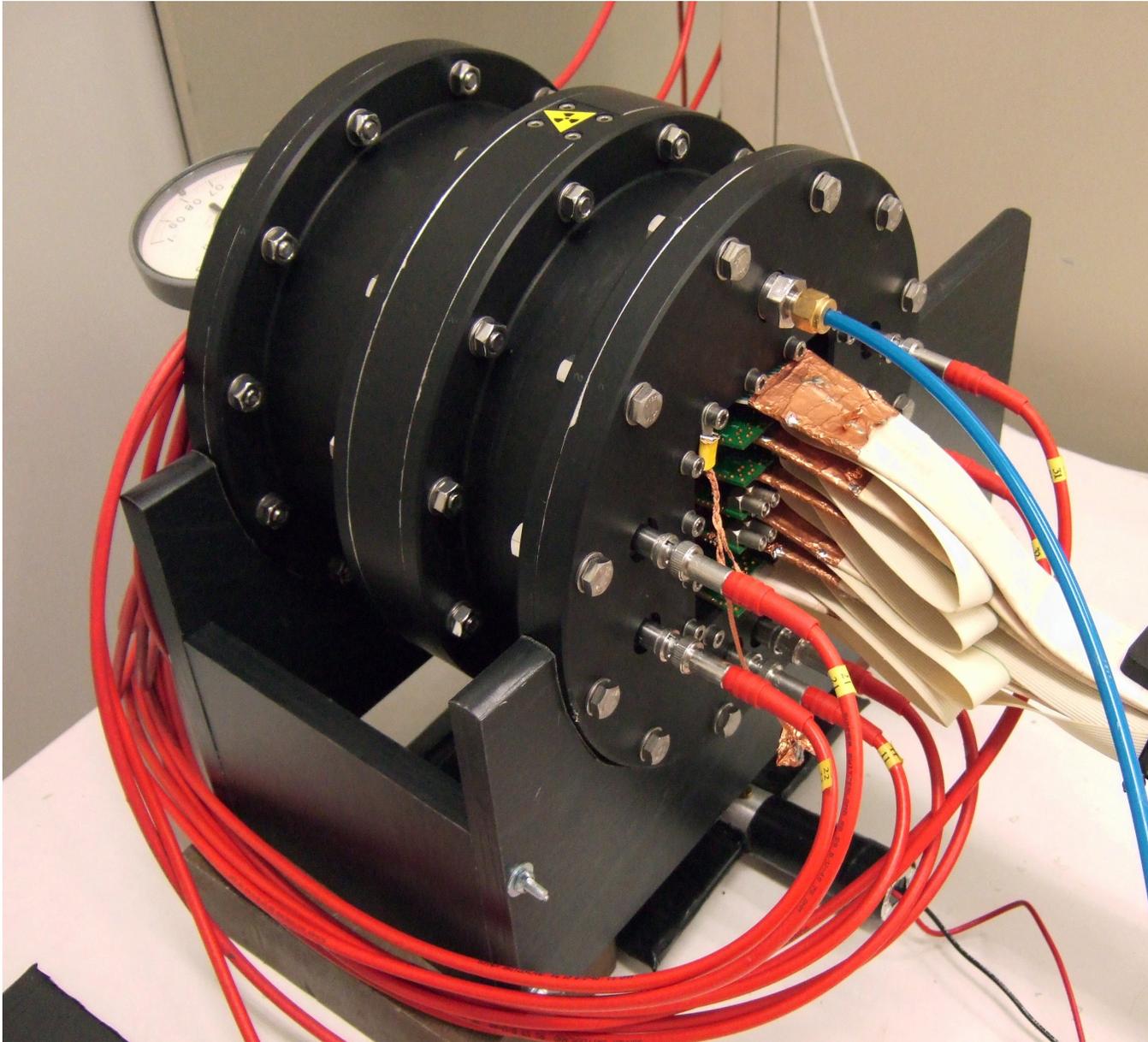
No straight forward implementation of the air flow

Thermal simulation



Preliminary

UNIMOCS detector



Features :

triple GEM setup

drift length 3 cm

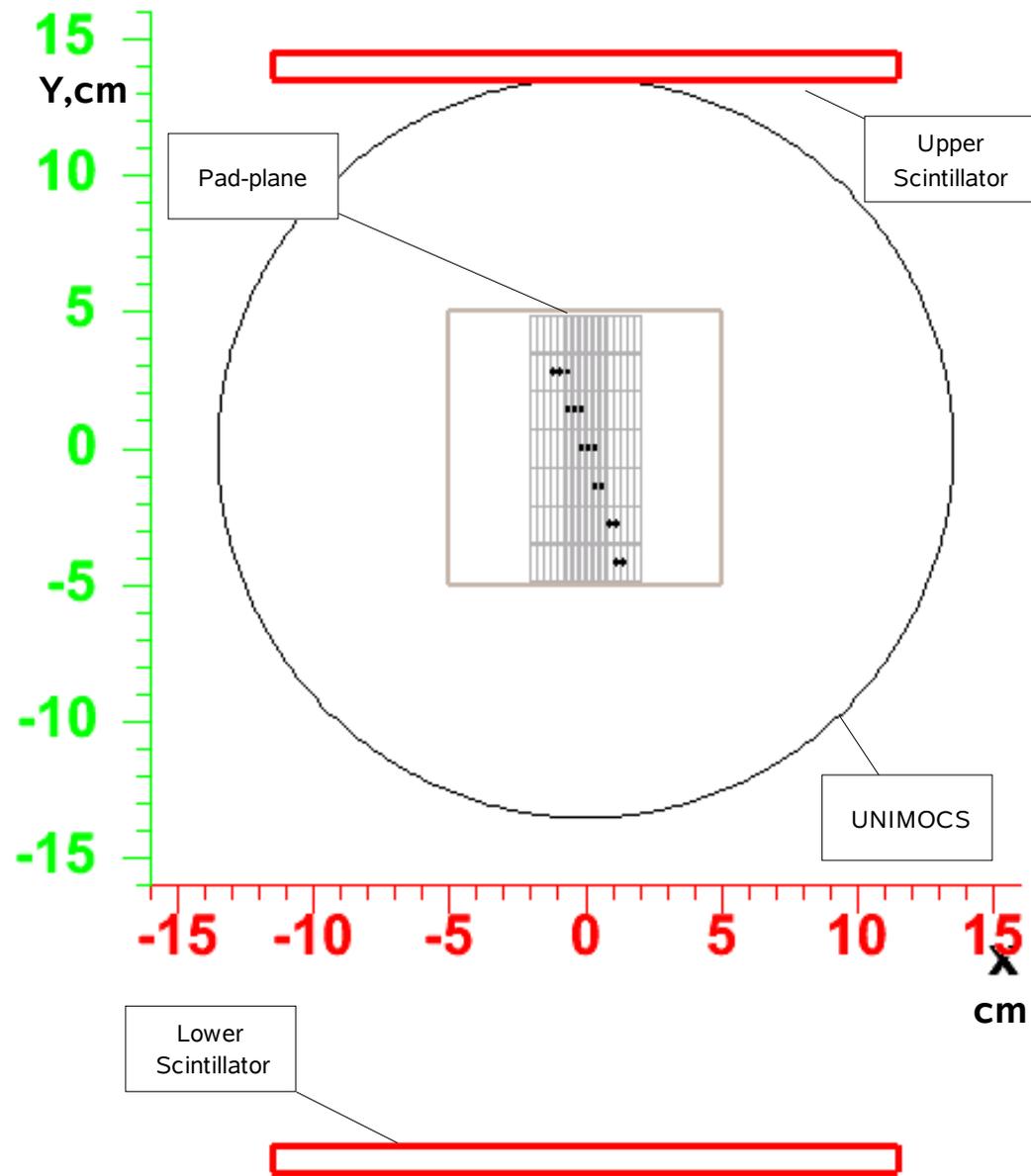
no field cage

7x16 pads

(2.5mmx14mm)

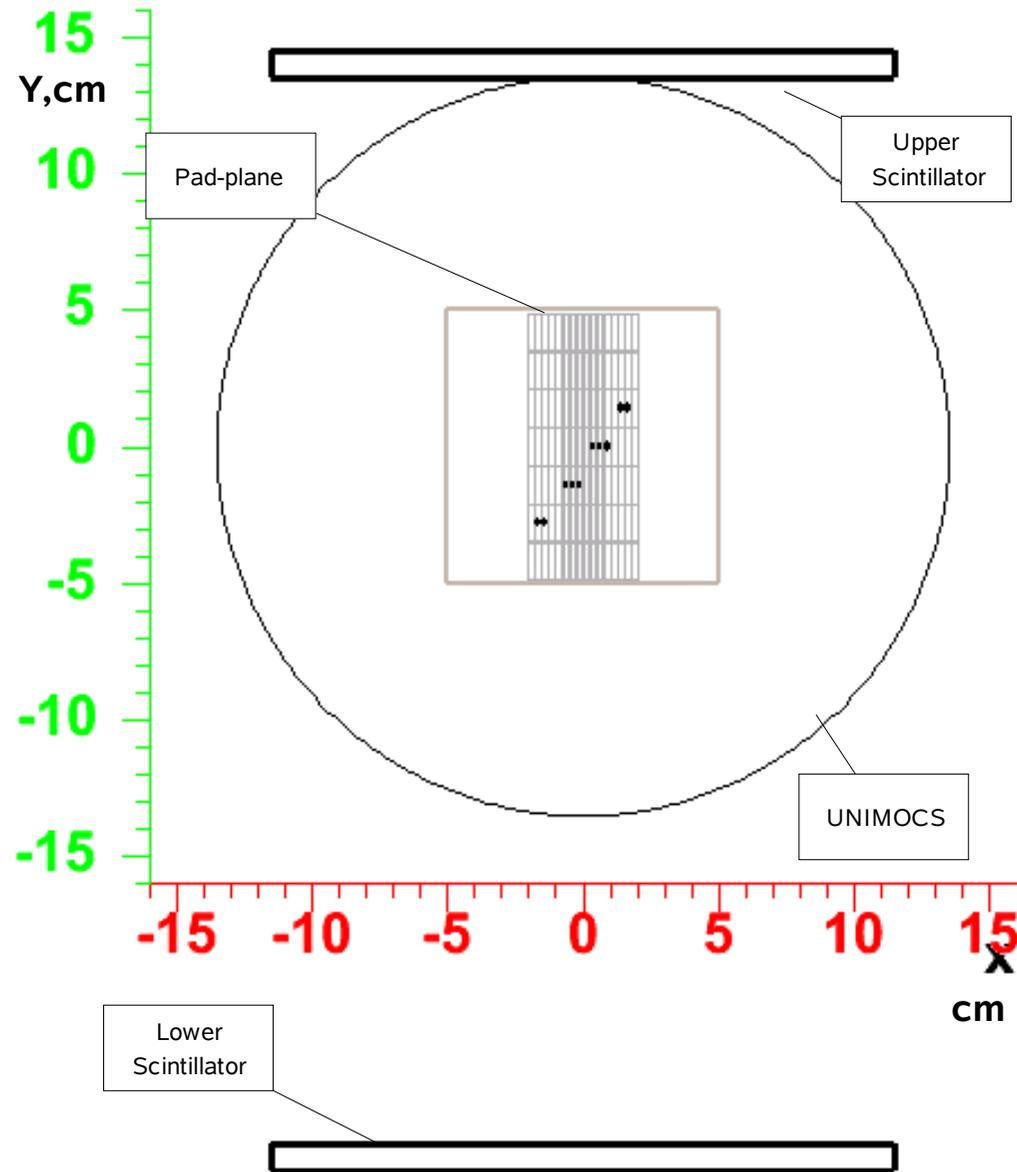
*Designed by O.Schäfer

UNIMOCS detector: first tracks



*DAQ software by
Natalia Okatieva (MEPhI),
DESY summer student

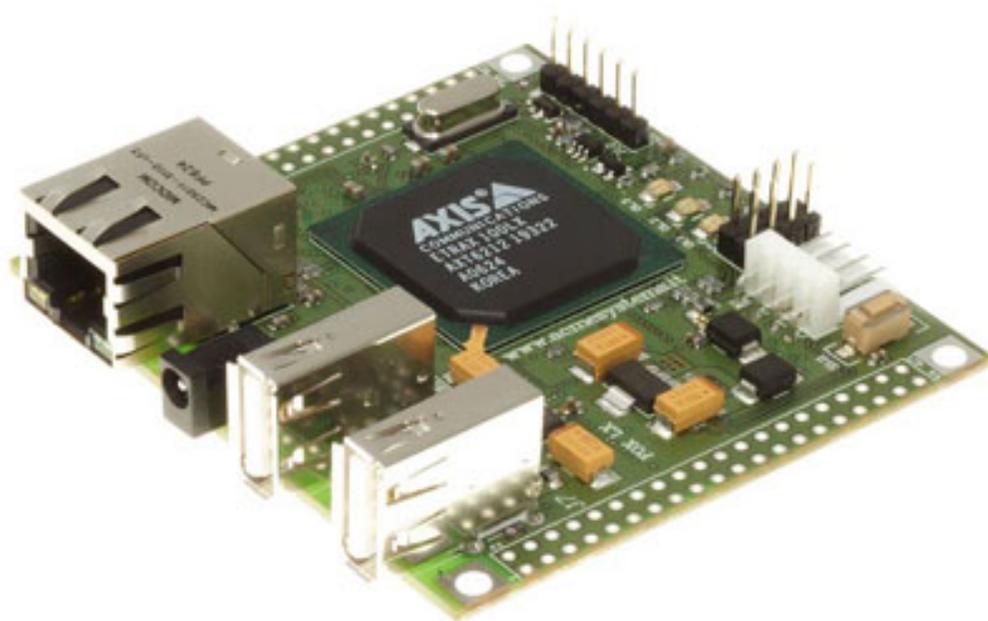
UNIMOCS detector: continuous readout (trigger on data)



Low voltage distribution box

The FOX board will be used to control parameters of the FEE-boards.
LVTTL (on the FOX board) -> LVDS-> cables -> LVDS-LVTTL -> DAC (thresholds,...)

Can be also used for power pulsing option (in development)



FOX Board, a complete Linux system (66 x 72 mm)

FOX LX832:

8MB FLASH 32MB RAM

32 bit, 100MHz CPU

- standard TCP/IP I/O ->
simplifies integration with existing DAQ
- Two 40 pins sockets step 2.54mm (0.1")
with LVTTL I/O
- Two USB ports: USB-disk, webcam,...

Summary and Outlook

TDC readout electronics with larger number of channels will be assembled for LP TPC.
The FEE boards are based on existing components.

Length of the twisted-pair cables to be defined (now: 5m cables) - the shorter the better.
Limitation comes from positioning of the VME crate in vicinity of the PCMAG.

Some questions still need to be answered:

- mechanical support of the Cable Pitch Adapters and twisted-pair cables
- proper “grounding” (including support structure, the magnet,...)
- cooling

FEE boards – November 2007, assembly in December.

Most of the hardware to be ready by the end of the year (except LV distribution box)

Software and documentation - January/February 2008