

ECal R&D: Prospects

For the baseline Si/W (option A):

- Complete the stated LCDRD goal – to make a test beam module with ILC-ready technology and put it in a beam (and analyze the data, and write up the results,...)
- Short answer: Too many uncertainties to evaluate this yet...
 - *Will* have the required silicon sensors.
 - *May* be able to do the bump bonding and flex cable fab.
 - RAL has offered help
 - Fab. of the KPiX (not cPiX) chip very uncertain
 - Best case: It all comes together by end of calendar 2008
 - “Closeout” LCDRD funds likely to be crucial.

For the terapixel/MAPS option (B): Will press on (chip revisions and evaluations) until told to stop.

Simulation studies for LOI (see talk at ALCPG07) were already manpower limited ! A deadline stretchout may help get more studies done.

(How will intermediate work be documented?)