

DOOCS DAQ software for the EUDET prototype

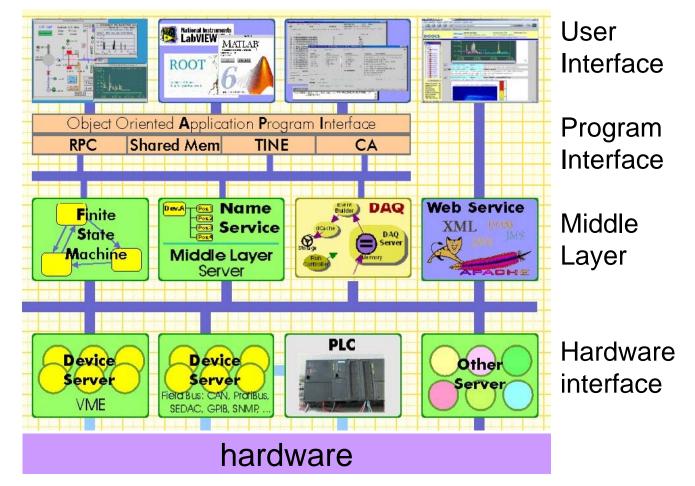
Valeria Bartsch (UCL) Andrzej Misiejuk (RHUL) Tao Wu (RHUL)





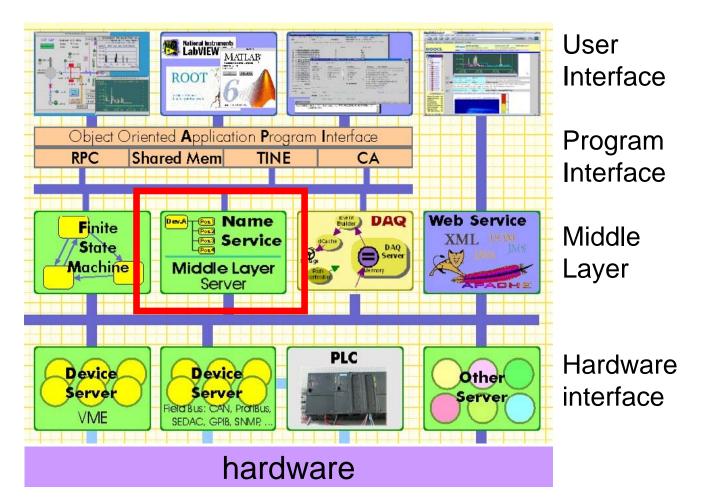
Overview over the task - DOOCS software -

http://tesla.desy.de/doocs/doocs.html



Overview over the task - ENS naming service -

provided by DOOCS and already in use for RPC communication between client and server



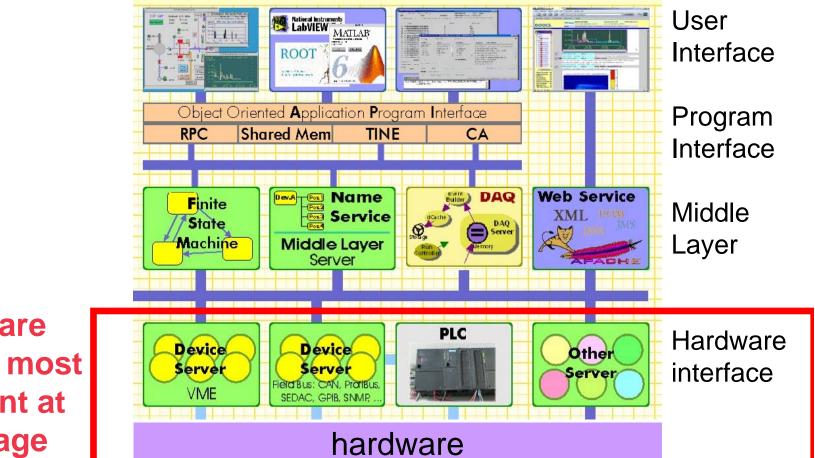
ENS Naming Service

Screenshot of the rpc_util GUI

DOOCS Communication and Plot Utility (Version 5.5.12) bartsch@pc71.hep.ucl.ac.uk TEST.DOOCS ODR ODR1 PARAM.DUMPTOHD	
Facility Device Location TEST.DOOC EVB.SAVE ODRSVR Property Description ODRSVR ODR1 PARAM.GROUP Grouping@INT PARAM.IOTHREADS Number 10 threads@INT PARAM.DUMPTOHD Dump data on disk [0/1]@INT PARAM.DUMPTOHD Dump data on disk [0/1]@INT PARAM.DUMPSIZE Data dump size@INT PARAM.DUMPSIZE Data dump size@INT PARAM.DUMPSIZE Data dump size@INT PARAM.DORET Data Generator/Network [0/1]@INT PARAM.ACTCHAN Active Channel(s) 0/1 off/on@IN PARAM.NUMDMA Number of DMA buffers:@INT	
Read Plot ▼ READ -> File : /tmp/rpc_test_bartsch Format: ▼ none Show File Result : 1	

- Naming convention is already specified (similar for LDA, DIF and ASICS)
- Properties need input from hardware programmers

Overview over the task



Hardware interface most important at this stage

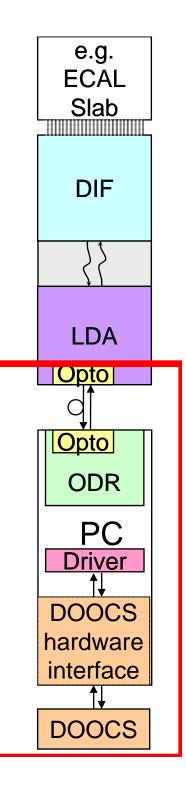
Hardware interface

Concentrating on the ODR interface:

- because it is the first hardware layer to talk to
- the device is close to be ready
- easy communication with colleagues at UCL and RHUL

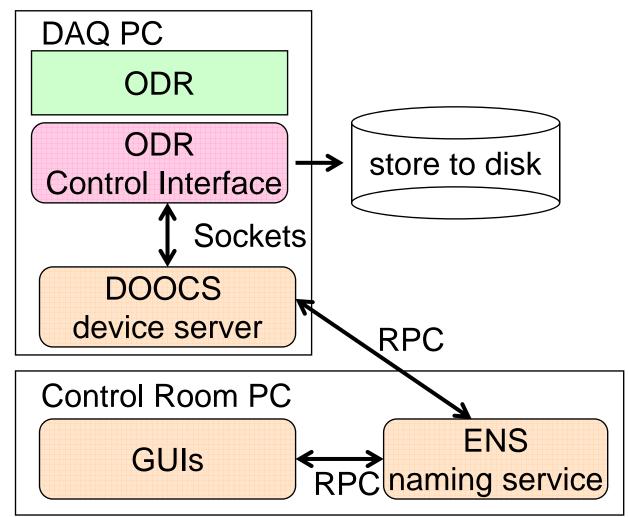
Plan:

- start with the LDA and DIF in September
- have the interfaces ready about end of the year



Overview over the ODR interface

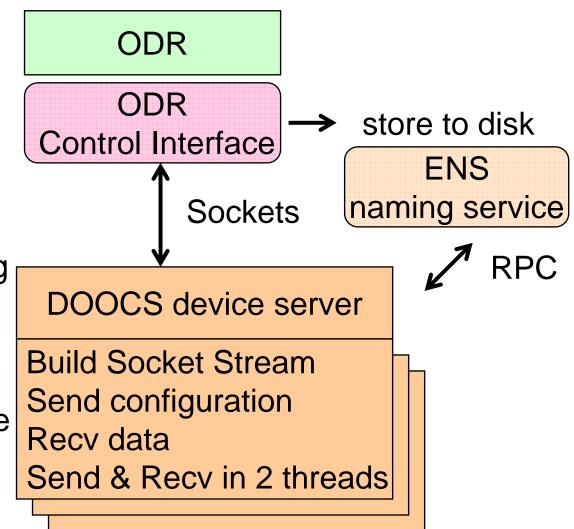
communication between different parts of DOOCS by RPCs
configuration files used to find different parts of the system



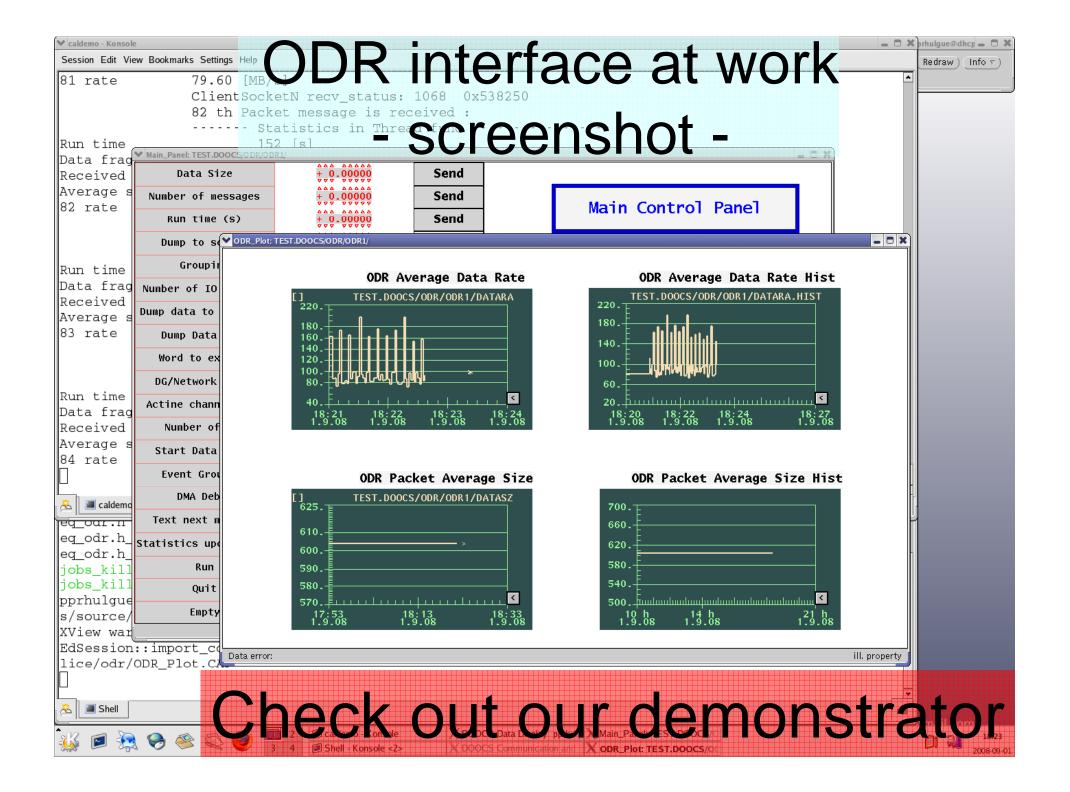
Overview over the ODR interface

• one device server can have many instance all connecting to different ports and hostnames

- using 2 threads: one for receiving, one for sending on the socket
- sockets format chosen
 to build an interface to the
 ODR and the LDA

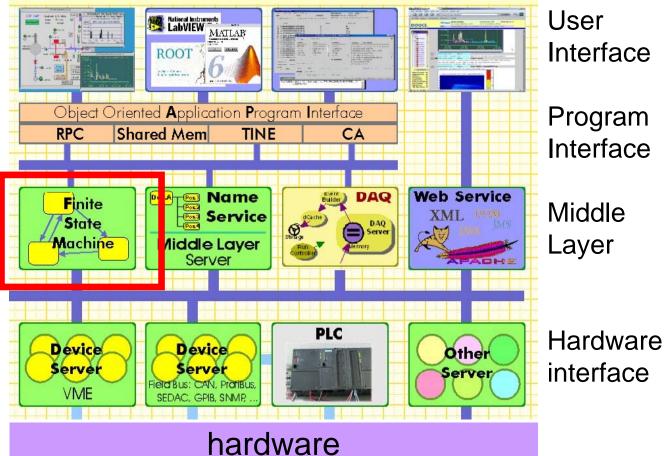


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Session Edit View Bookmarks Settings Help ODCCS Data Display pprhulgue@dhcp = * 0 ~/exp/calice_rem ODR interface at work File v Show v Redraw Info v						
caldemo@<2> cat MAC.txt						
00:A0:CC:73:44:DF						
./caldata_port_00:A0:CC:73:44:DF - Screenshot -						
./caldata	Data Size	÷.000000	Send			
caldemo@<	Number of messages	+0.00000	Send	Main Control Davel		
There is Card 0:	Run time (s)	+0.00000	Send	Main Control Panel		
✓ Shell - Konsole <	Dump to screen	÷0.00000	Send			
Session Edit Vi	Grouping	+0.00000	Send	ODR Commands		
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buttons.c	Number of DMA	÷;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;	Send	LDA Commands		
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ClientSoc	Event Grouping	÷0.00000	Send	Stop		
CVS data	DMA Debug	÷;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;	Send			
dd.cc	Text next message	+ 0 - 00000	Send	ODR Plots		
diff.log eq_odr.h	Statistics update Freq	÷ 0 • 00000	Send			
eq_odr.h_	Run	+ 0 - 00000	Send	Exit ODR Device Server		
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Overview over the task

important
ramps the whole
DAQ system up
and down
will be started
after the hardware
interfaces are
ready
(Jan-Mar 2009)



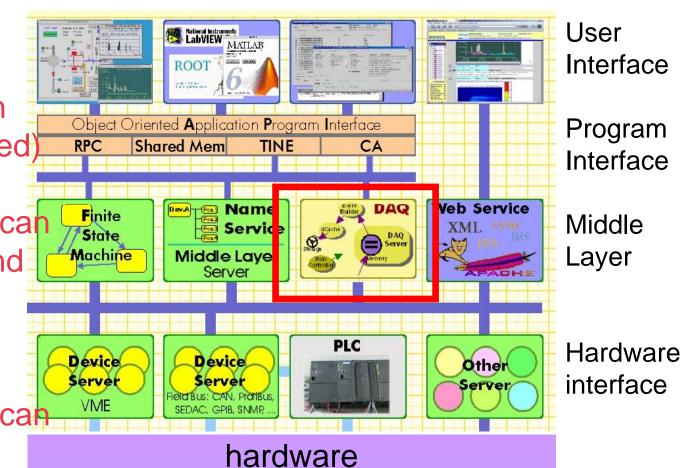
Overview over the task

DAQ:

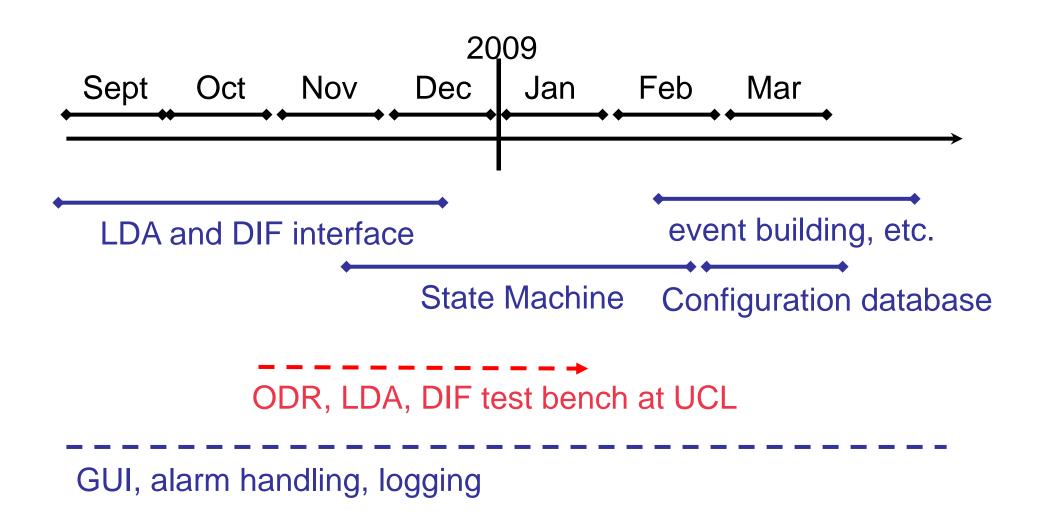
Event collection
Event building
LCIO conversion
(to be implemented)

Importance: low, can be done at the end of the software project;

Alternatively this can be done offline

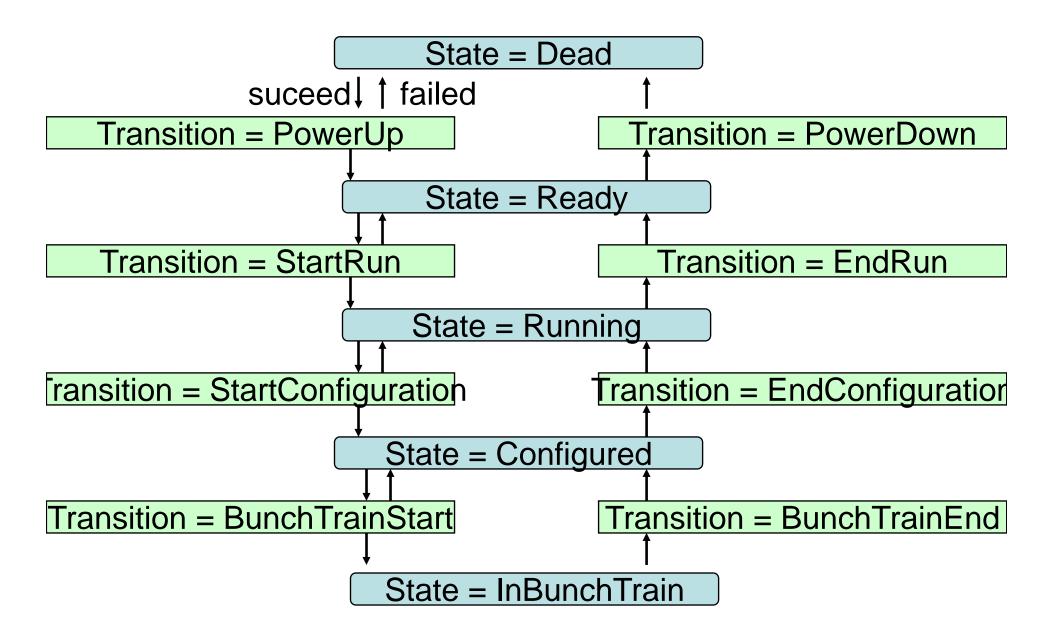


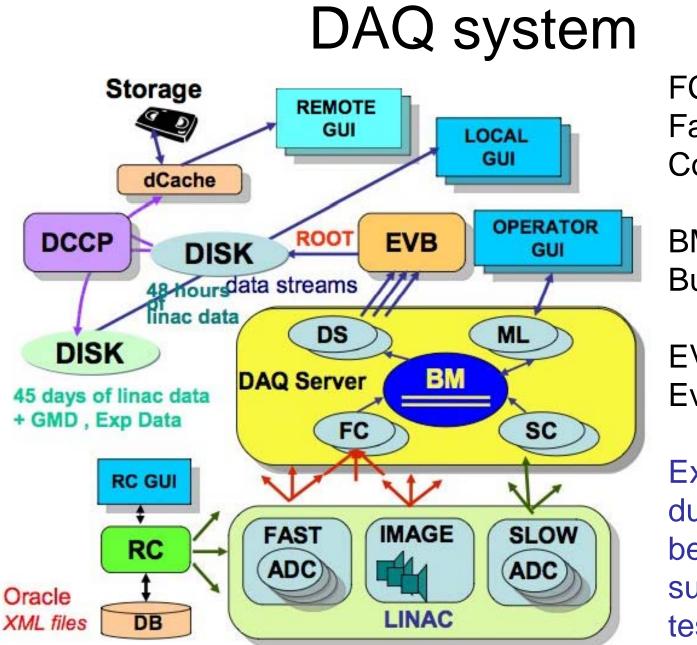
suggested timeline



backup slides

State Analysis





FC/SC: Fast/Slow Collector BM: **Buffer Manager** EVB: **Event Builder Example with** dummy data has been successfully tested

Alarm handling

