



Global R&D Effort of SCRF Cavity Development for the International Linear Collider

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Project Managers for
ILC Global Design Effort

To be presented during visiting SCRF cavity manufacturers
February/March, 2009


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Acknowledgements

- **We would thank**
 - *your kind acceptance for our visiting here and the opportunity to learn your industrial efforts.*
- **We would thank**
 - *the ILC SCRF collaborators for their kind cooperation to prepare for this presentation.*


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Objectives of Visiting

- **Learn industrial status and possible future** at cavity manufacturers, through visiting the factory, presentations and discussions with factory staff,
- **Communicate TD-Phase R&D Plan**, and inform necessary boundary conditions, “plug-compatibility”, in the world-wide R&D stage,
- **Request close collaboration** with laboratories to further industrial R&D effort, particularly, to improve “field gradient” and “cost effective production” to prepare for the industrialization (mass production),
- **Establish close communication** and confident relationship between ILC-GDE and manufacturers.

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Outline

- **Introduction**
- **R&D Status**
- **Plan for Technical Design Phase**
- **Global Plan and Project Management**
- **Summary**

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ilc **Particle Accelerators**
beyond limit of circular accelerators

THE ENERGY FRONTIER (Discoveries)

Hadron Colliders: (top quark), Tevatron, LEP II, LHC, LEP I, PETRA, PEP, CESR, SPEAR II, SORCE, e⁺e⁻ Colliders.

ILC

Electron machine
Ring accelerator
Linear Accelerator

Energy loss must be replaced by RF system
cost scaling $\propto E_{cm}^2$

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ilc **ILC Reference Design**

- 11km SC linacs operating at 31.5 MV/m for 500 GeV
- Centralized injector
 - Circular damping rings for electrons and positrons
 - Undulator-based positron source
- Single IR with 14 mrad crossing angle
- Dual tunnel configuration for safety and availability

Reference Design – Feb 2007

Schematic Layout of the 500 GeV Machine

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ilc **SCRF Technology Required**

Parameter	Value
C.M. Energy	500 GeV
Peak luminosity	$2 \times 10^{34} \text{ cm}^{-2} \text{ s}^{-1}$
Beam Rep. rate	5 Hz
Pulse time duration	1 ms
Average beam current	9 mA (in pulse)
Av. field gradient	31.5 MV/m
# 9-cell cavity	14,560
# cryomodule	1,680
# RF units	560

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ilc **Technical Design Report**
to be completed by 2012

Reference Design, 2007 >> Technical Design Phase, 2008-2012
We are now at the stage of progressing from the RD to TD

ILC Research and Development Plan for the Technical Design Phase

Release 2
June 2008

ILC Global Design Effort
Director: Barry Barish

Prepared by the Technical Design Phase Project Management

Project Managers:
Marc Risse
Nick Walker
Akira Yamamoto

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ilc Progress in Single Cell Cavity

RE, LL, IS, new-RE shapes

Parameter	TESLA	IL/IS	RE
iris aperture (mm)	70	60/61	66
$E_{\text{max}}/E_{\text{ave}}$	1.98	2.96/2.02	2.21
$B_{\text{max}}/B_{\text{ave}}$ (no Ti/MV-to-ell)	4.15	3.61/3.56	3.76
Clear sheet impedance: R/Q (Ω)	114	134/138	127
Geometric factor: G (Ω)	271	284/285	277
$G = R/Q \cdot (Q \times Q \times 10^9)$	3.08	3.80/3.93	3.51

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ilc Standard Procedure Established

	Standard Fabrication/Process
Fabrication	Nb-sheet purchasing
	Component Fabrication
	Cavity assembly with EBW
Process	EP-1 (~150um)
	Ultrasonic degreasing with detergent, or ethanol rinse
	High-pressure pure-water rinsing
	Hydrogen degassing at > 600 C
	Field flatness tuning
	EP-2 (~20um)
	Ultrasonic degreasing or ethanol (or EP 5 um with fresh acid)
	High-pressure pure-water rinsing
	Antenna Assembly
	Baking at 120 C
Cold Test (vertical test)	Performance Test with temperature and mode measurement

Key Process

Fabrication

- Material
- EBW
- Shape

Process

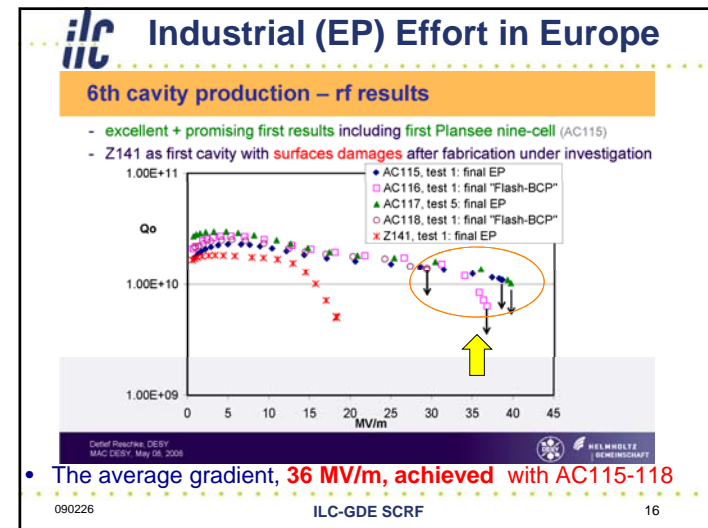
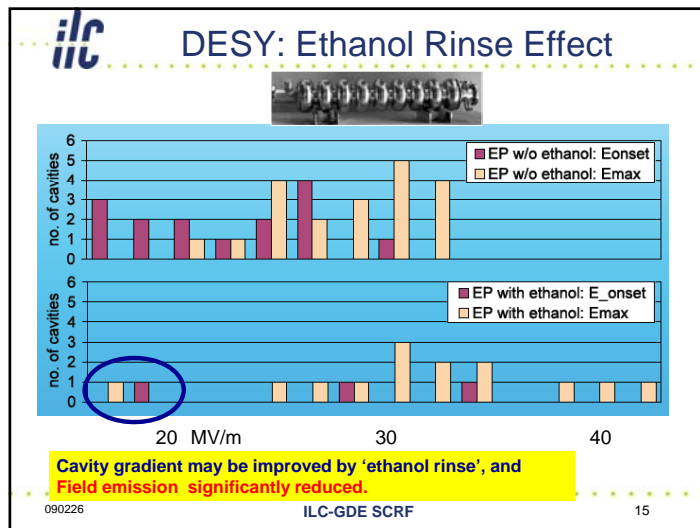
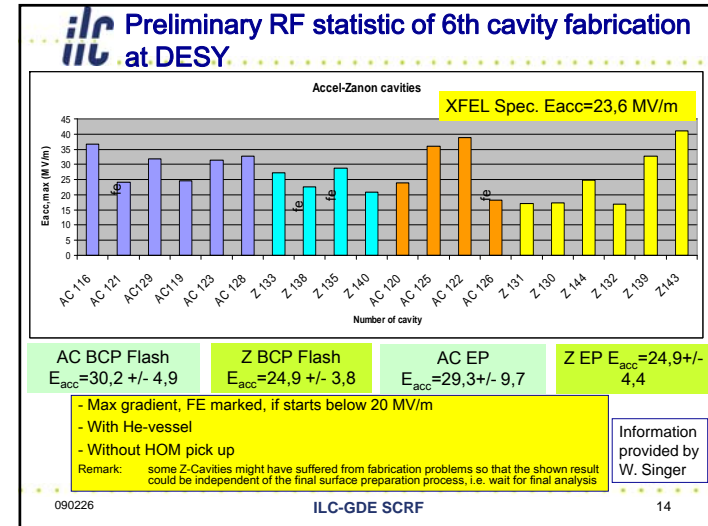
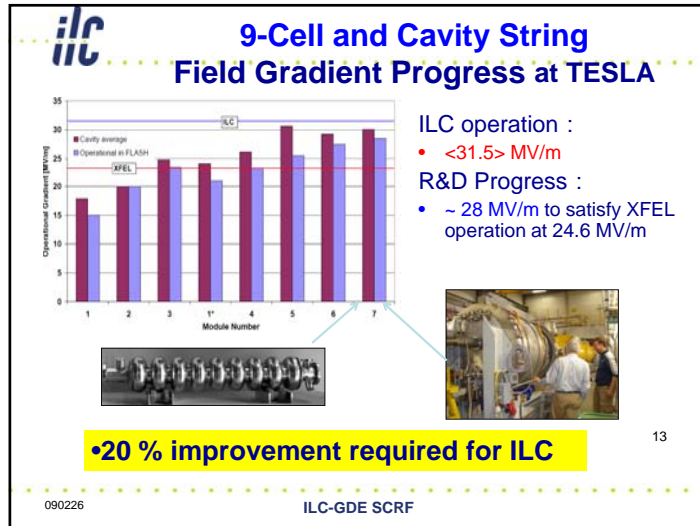
- Electro-Polishing
- Ethanol Rinsing or Ultra sonic. + Detergent Rins.
- High Pr. Pure Water cleaning

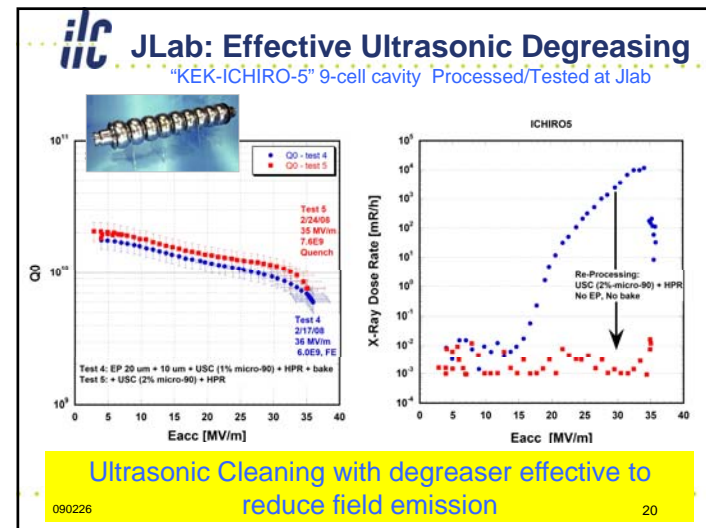
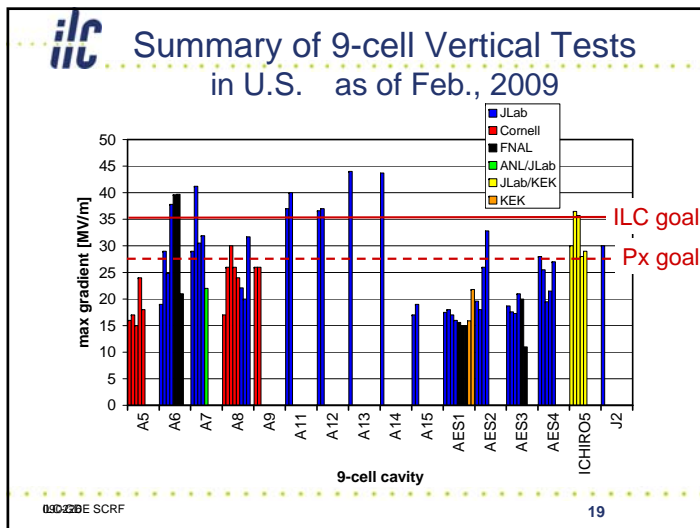
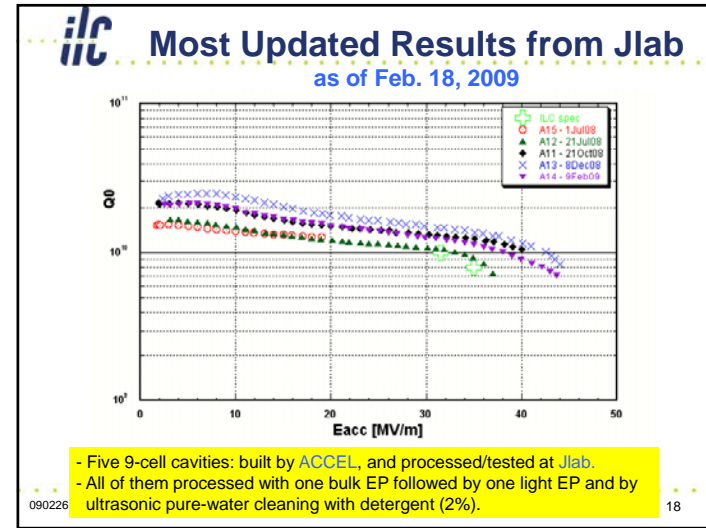
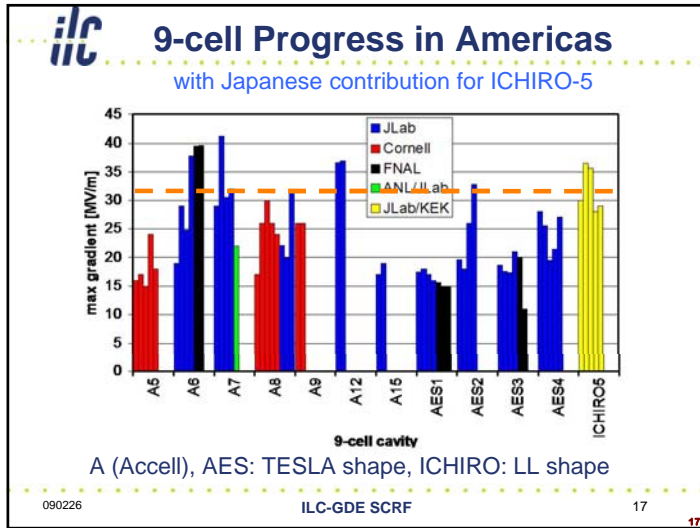
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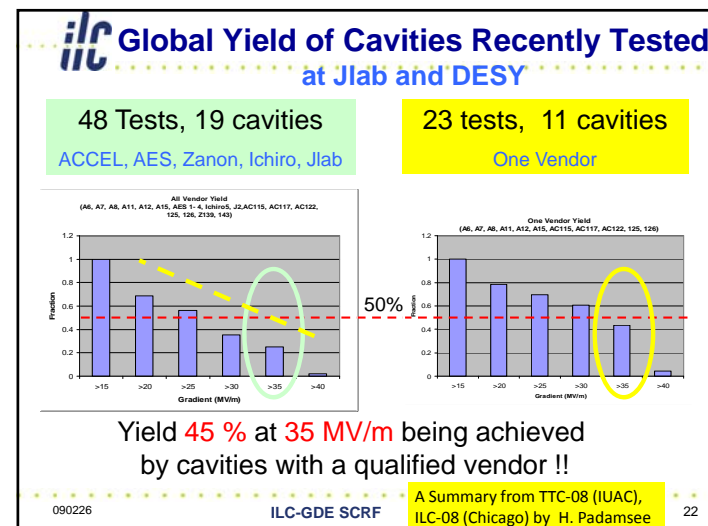
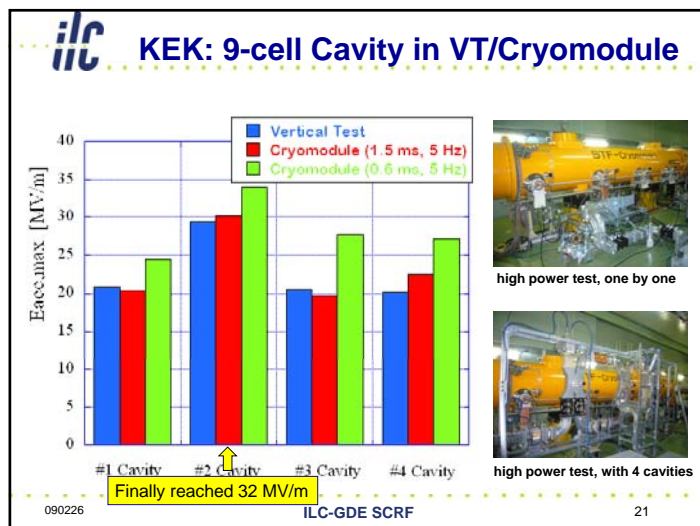
ilc Status of 9-Cell Cavity

- Europe (DESY, Saclay)
 - Gradient: > ~ 40 MV/m (max) ,
 - Industrial (bulk) EP demonstrated
 - Field emission reduced with ethanol rinsing
 - Surface process with baking in Ar-gas
- Americas (Jlab, Cornell, FNAL/ANL)
 - Gradient: > ~ 40 MV/m (max),
 - Field emission reduced w/ Ultrasonic Degreasing & Detergent
- Asia (KEK, IHEP, RRCAT)
 - Gradient: 36MV/m (LL, KEK-JLab), 32 MV/m (TESLA-like, KEK)
 - Global cooperation of Indian institutions with
 - Fermilab, ANL, Jlab, DESY, and KEK

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ilc Outline

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ilc TDP Goals of ILC-SCRF R&D

- **Field Gradient**
 - 35 MV/m for cavity performance in vertical test (S0)
 - 31.5 MV/m for operational gradient in cryomodule
 - to build two x 11 km SCRF main linacs
- **Cavity Integration with Cryomodule**
 - “Plug-compatible” development to:
 - Encourage “improvement” and creative work in R&D phase
 - Motivate practical ‘Project Implementation’ with sharing intellectual work in global effort
- **Accelerator System Engineering and Tests**
 - Cavity-string test in one cryomodule (S1, S1-global)
 - Cryomodule-string test with Beam Acceleration (S2)
 - With one RF-unit containing 3 cryomodule

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ilc What do we need to re-visit? In Superconducting Cavity R&D

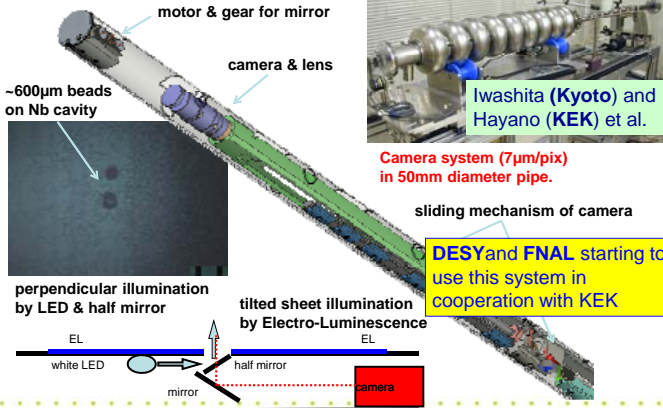
- **Niobium Sheet metal cavity**
- **Fabrication:**
 - Forming and **welding (EBW)**
 - Local repair
- **Surface Process:**
 - Chemical etching/polishing
 - Cleaning
- **Inspection/Tests:**
 - **Optical Inspection (warm)**
 - Surface Analysis (warm)
 - Thermometry (cold)

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ilc A New High Resolution, Optical Inspection

For visual inspection of cavity inner surface.



~600µm beads on Nb cavity

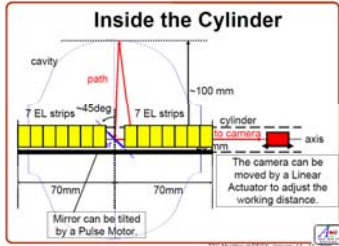
Camera system (7µm/pix) in 50mm diameter pipe.

DESy and FNAL starting to use this system in cooperation with KEK


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ilc Concept of Profile Measurement

Inside the Cylinder



Stripe Illumination(SI)

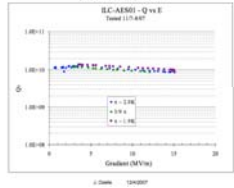


- Fourteen Electro-Luminescence(EL) strip sheets are 10mm in axial direction and cover 100mm in azimuthal direction.
- These fourteen strips can be turned ON/OFF one by one.
- Assuming that cavity's interior surface is a complete mirror, we can measure wall gradients of the cavity's interior surface with these ELs.

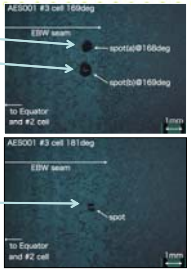
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ilc Consistent with Thermal Measurement at FNAL


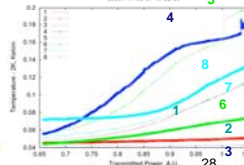
3rd Test Results



84µm height
60µm height
~21mm
43µm height

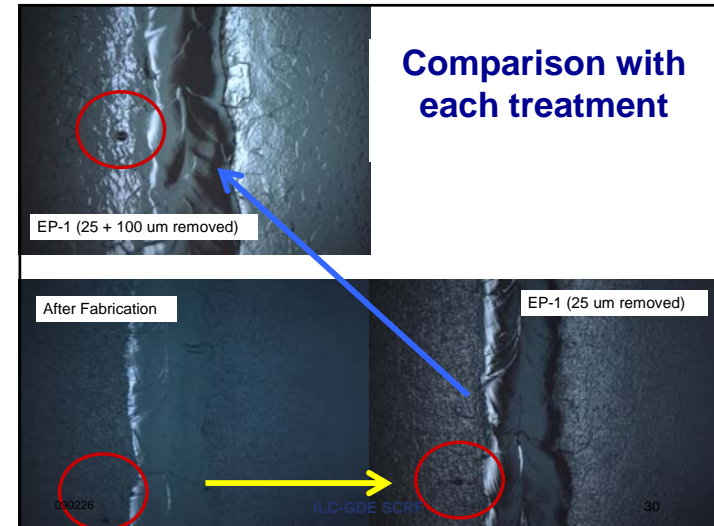
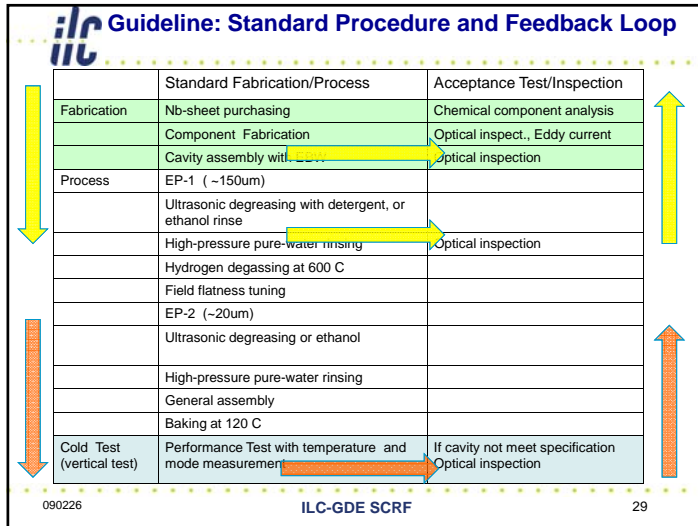


AES01 has hard quench at 15MV/m, its location was identified by Cernox at FNAL, (M. Champion et al., ASC-08)

Kyoto-camera found 3 spots in their exact location

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ilc Understanding of Sources for Quench

From TTC-09a Summary (by H. Padamsee)

- Sources for quench **below 25 MV/m** have been identified
- Thermometry first used to locate quench regions followed by **optical inspection**.
- Quench sites are predominantly **bumps and pits** on the **equator e-beam weld (EBW)**, or in the **heat affected zone**.

Picture example reported

A13 Cell #5 FE28, pit inside equator EBW

HAZ 100-200 μm dia.

JLab

Z130 Cell #4 18deg, pit at equator EBW ~500 μm dia.

quench at 18-22MV/m DESY

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ilc JLab

- E-beam melting to repair pits
- Try this on a single cell?

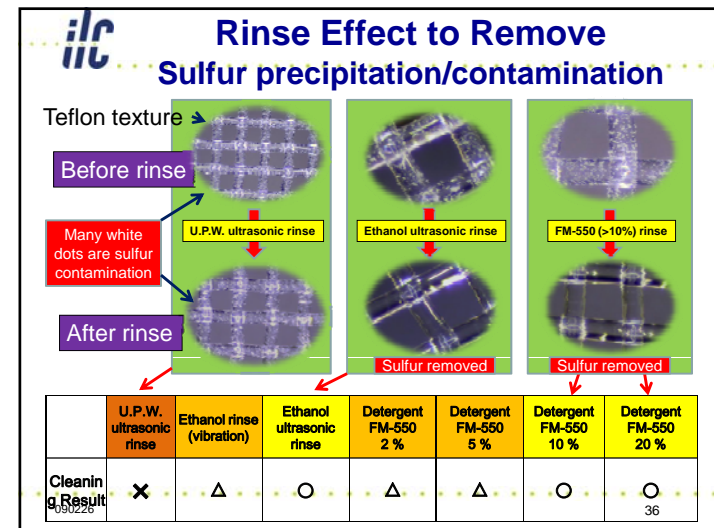
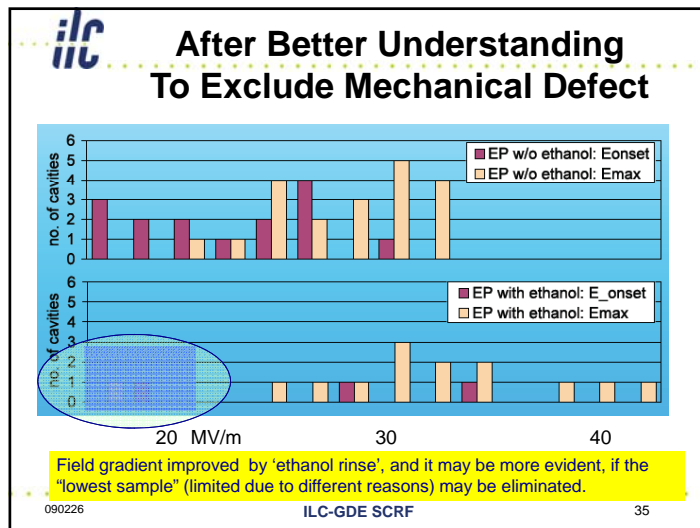
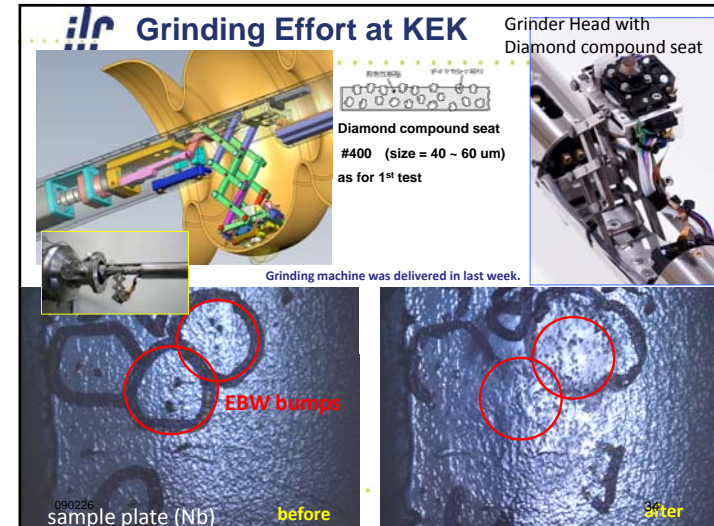
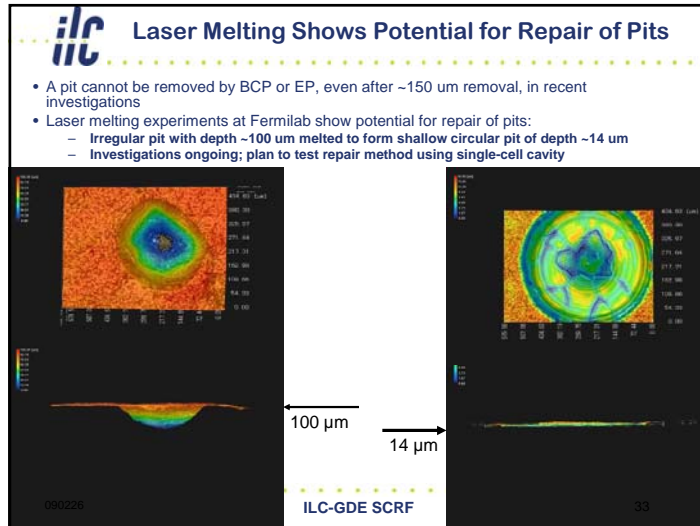
A summary from TTC (H. Padamsee)

Controlled defects (pit) Created along center line

original

Re-melted

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ilc Summary of R&D Efforts/Subjects

- Establish technology for **defect-free production**, with “quick” feedback using inspection camera results
 - Upgrade “inspection camera”, and
 - Develop other inspection tools,
- Identify, more accurately, origin of field emission after surface treatment
 - Research and improve “surface-analysis”: XPS, SEM ,,,
- Establish and Demonstrate countermeasures:
 - The final treatment to remove FE source such as **sponge wipe**, **degreaser rinse**, **ethanol rise**,
 - Repair method such as **grinding tool** for curing damaged cavities

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ilc Importance of Plug-compatibility in Development Stage

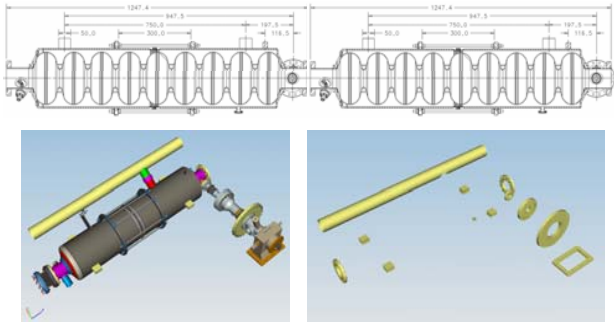
- Creative/Innovative work** for further improvement with keeping “redundancy” and “replaceable >> plug-compatible” condition
- Seek for **Cost-effective Fabrication** for “mass production”
- Global cooperation and share for intellectual engagement

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- “Plug-compatibility” essentially important

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ilc Plug-compatibility of Cavities Important for Global Cooperation

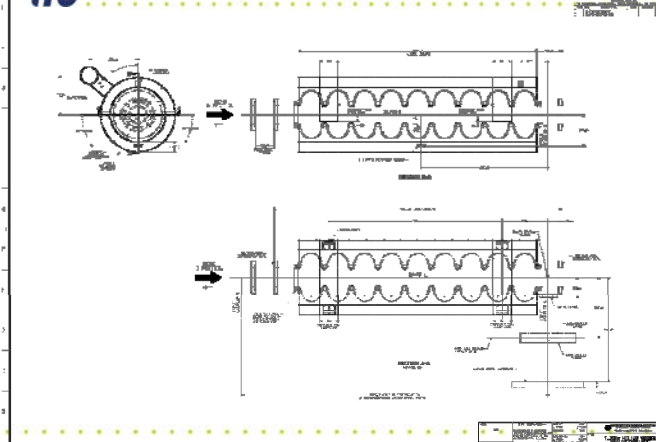


Technical drawings and 3D models of cavity components showing plug-compatible interfaces. The drawings include dimensions and labels for various parts. The 3D models show the assembly of the cavity components, highlighting the plug-compatible interface.

Plug-compatible interface being established

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ilc Cavity: Plug-compatible Interface

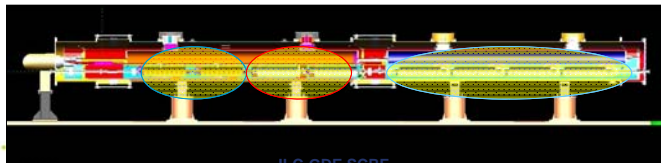


Technical drawing of a cavity showing the plug-compatible interface. The drawing includes dimensions and labels for various parts, illustrating the design for plug-compatibility.

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ilc Cavity String Test in Cryomodule with Plug Compatibility

- Cavity integration and the String Test to be organized as a global cooperation (S1-Global):
 - 2 cavities from EU (DESY) and AMs (Fermilab)
 - 4 cavities from AS (KEK)
 - Cryomodules: from EU (INFN) and AS (KEK)
- A practice for the plug-compatible assembly

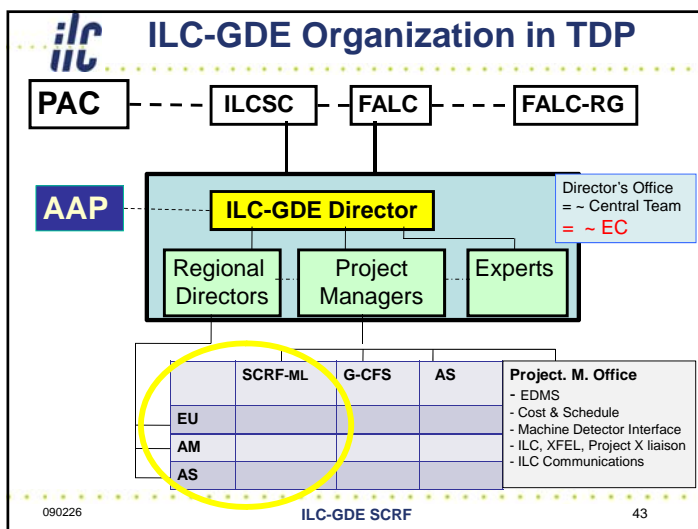


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ilc Project Plan in 2008-2010

- Field gradient (S0)
 - To be re-optimized, based on the R&D progress (2010),
- Plug-compatibility
 - Common interface conditions being fixed ,
 - Overview document published
- System engineering/test plan, (S1, S2)
 - Work sharing in cavity string in global effort (S1-Global)
 - Accelerator system test with beam
 - Necessary detailed study and re-coordination under limited resources, including schedule
- Effort for "minimum machine",
 - Cluster or Distributed RF power sources and distribution,
- Prepare for AAP Interim Review in April, 2009
- Global Communication and cooperation with Laboratories & Industries
 - Visit Industries: ACCEL, ZANON, AES, Niowave, MHI,

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ilc Global Plan for SCRF R&D

Calendar Year	2007	2008	2009	2010	2011	2012
Technical Design Phase	TDP-1			TDP-2		
Cavity Gradient R&D to reach 35 MV/m		Process Yield > 50%		Production Yield >90%		
Cavity-string test: with 1 cryomodule		Global collab. For <31.5 MV/m>				
System Test with beam 1 RF-unit (3-module)		FLASH (DESY)		STF2 (KEK) NML (FNAL)		

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ilc Summary

- Technical Design Phase in progress:**
 - Phase-1: Technical reality to be examined,**
 - 35 MV/m with yield 50 % in surface process and
 - 31.5 MV/m with the cavity-string in a cryomodule
 - Phase-2: Technical credibility to be demonstrated**
 - 35 MV/m with the yield 90 % for 9-cell in manufacturing
 - Beam acceleration with the field gradient 31.5 MV/m.
- We aim for**
 - Global R&D efforts with various efforts keeping “plug-compatibility” concept.
 - Cooperation of world-wide Institutions and Industries would be crucially important and expected.

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ilc Information and References

General Information

- ILC
 - <http://www.linearcollider.org>
 - <http://www.linearcollider.org/cms/?pid=1000613>
- This presentation (Cavity Manufacturer visiting, Feb./March, 2009)
 - http://ilc-edmsdirect.desy.de/ilc-edmsdirect/file.jsp?edmsid=*868405

ILC-GDE Reports

- Reference Design Report
 - <http://www.linearcollider.org/cms/?pid=1000437>
- TDP R&D Plan, Release 3
 - http://ilc-edmsdirect.desy.de/ilc-edmsdirect/file.jsp?edmsid=*813385

Public Presentations/Proceedings:

- “Global R&D effort for the ILC linac technology”, presented by A. Yamamoto, at EPAC08,
 - http://ilc-edmsdirect.desy.de/ilc-edmsdirect/file.jsp?edmsid=*868435
- “Superconducting RF cavity development for the International Linear Collider”, presented by A. Yamamoto, at ASC08, to be published in IEEE Trans. Applied Superconductivity,
 - http://ilc-edmsdirect.desy.de/ilc-edmsdirect/file.jsp?edmsid=*868465

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ilc international linear collider

ILC Global Design Effort Project Manager visit to SCRF cavity manufacturers

February - March 2009

In early 2009 the ILC Global Design Effort Project Managers (Akira Yamamoto, Marc Ross, and Nick Walker) visited and were graciously hosted by many of the world's top superconducting RF cavity manufacturers. The objective of the visit was to:

- Learn industrial status and possible future of cavity manufacturers.
- Communicate the ILC-GDE Technical Design Phase R&D Plan.
- Request better industrial R&D effort, particularly to improve “test gradient” and “test effective production” in order to prepare for the industrialization (mass production).
- Establish close communication and a working relationship between ILC-GDE and vendors.

This web page is intended to capture the material presented to vendors and to include key references.

Global Design Effort of SCRF cavity development for the International Linear Collider (pdf, 194k)
Akira Yamamoto, Marc Ross, and Nick Walker - Project Managers for the ILC Global Design Effort, material presented to each of the SCRF cavity manufacturers.

Superconducting RF cavity development for the International Linear Collider (pdf, 476k)
Akira Yamamoto for the ILC Global Design Effort, paper presented at Applied Superconductivity Conference 2008 (ASC 2008).

Global Design Effort for the ILC linac technology (pdf, 476k)
Akira Yamamoto for the ILC Global Design Effort, paper presented to EPAC 2008.

Reference Design Report
Download the pdf
Volume 3 - Accelerator
Download the pdf (2008)

ILC Research and Development Plan for the Technical Design Phase
Download the pdf

(Last updated: 25 February 2009)

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Technical Questions to be Discussed

- Manufacturing process and capacity?
 - How we may improve “field gradient”, and how we may establish “quality control = stable performance”?
 - Material, forming, chemical process, packaging
 - Capability for Full production process by yourself and/or any specific collaboration with other companies in specific process?
 - Quality assurance program?
 - License/authorization for high pressure vessel manufacturing
 - Capacity in production: status and plan?
- Any requests/questions to ILC-GDE?

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Backup for Discussions

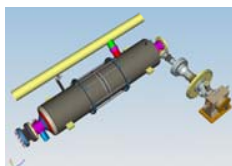
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Plug-compatible Conditions



Item	Can be flexible	Plug-comp.
Cavity shape	TeSLA/LL/RE	
Length		Fixed
Beam pipe flange		Fixed
Suspension pitch		Fixed
Tuner	Blade/Jack	
Coupler flange (warm end)		Fixed
Coupler pitch		fixed
He -in-line joint		TBD

Plug-compatible interface being established

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Global Cooperation: Plug-compatible Design and R&D

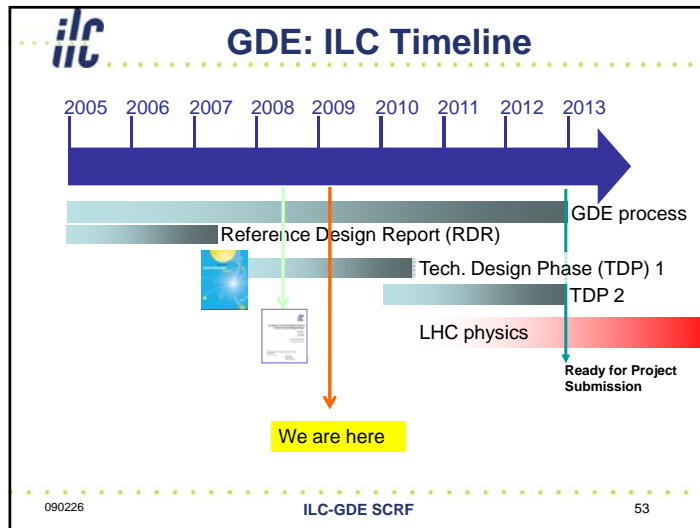
- Cost driven R & D process
- Innovative and Intellectual engagement
- Technology transfer to Industry
- Expert base



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ilc Cooperation with EuroXFEL and Further SCRF Acc. Projects

European X-ray Free Electron Laser Facility

- EuroXFEL SRF design gradient : **23.6 MV/m**
- Machine designed: **28 MV/m**
- ~ 100 SCRF cryomodule, based on the experience at TTF, DESY,
- Leading SCRF industrialization (scale: **1/20 of ILC**, in 5 years)
- Keep close cooperation with XFEL, on-going project.

Further SCRF Accelerator Project Plans:

- Project X at FNAL, SC Proton Linac at CERN, ERL at KEK, Indian Accelerator Project
- Best effort for common design and cost-effective design

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ilc SRF Test Facilities

FNAL
NML facility
Under construction
first beam 2010
ILC RF unit test

DESY
TTF/FLASH
~1 GeV
ILC-like beam
ILC RF unit
(* lower gradient)

KEK, Japan
STF (phase I & II)
Under construction
first beam 2011
ILC RF unit test

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ilc 9mA Experiments in TTF/FLASH

		XFEL	ilc	FLASH design	FLASH experiment
Bunch charge	nC	1	3.2	1	3
# bunches		3250*	2625	7200*	2400
Pulse length	μs	650	970	800	800
Current	mA	5	9	9	9

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