



Laboratoire d'Annecy-le-Vieux
de Physique des Particules



RECONSTRUCTION, ANALYSIS & SIMULATION SOFTWARE FOR MICROME GAS DH CAL PROTOTYPES

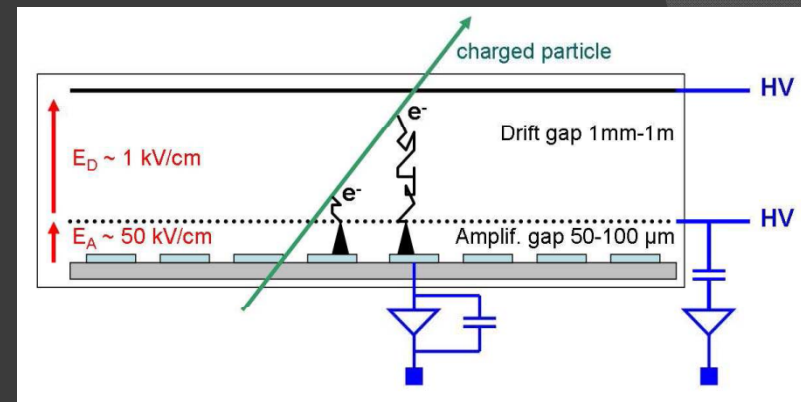
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LAPP, Annecy, France

Introduction

- Motivations for a Digital hadronic calorimeter
 - Count hits instead of measuring energy deposits
 - Reduce cost with still good enough energy resolution
 - Scintillator
 - Gas (GEMs, RPC, Micromegas)

- Micromegas

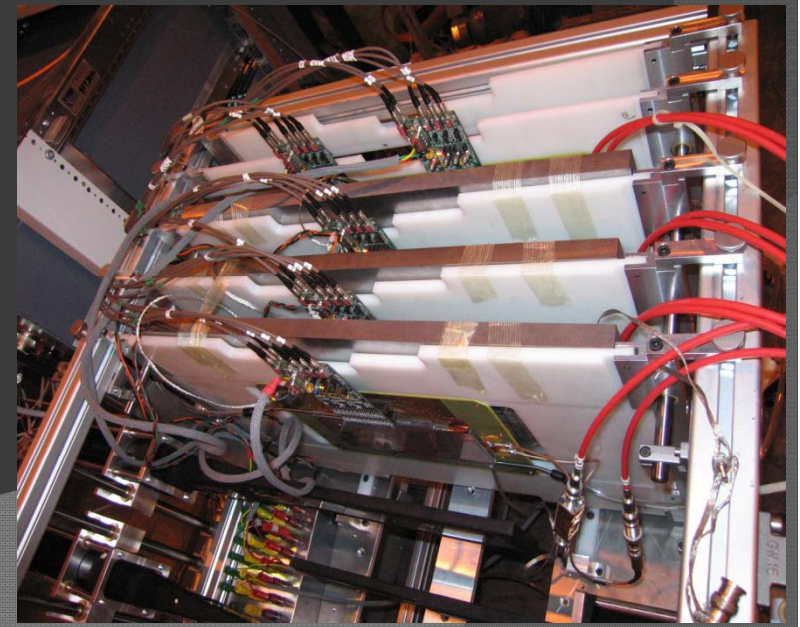
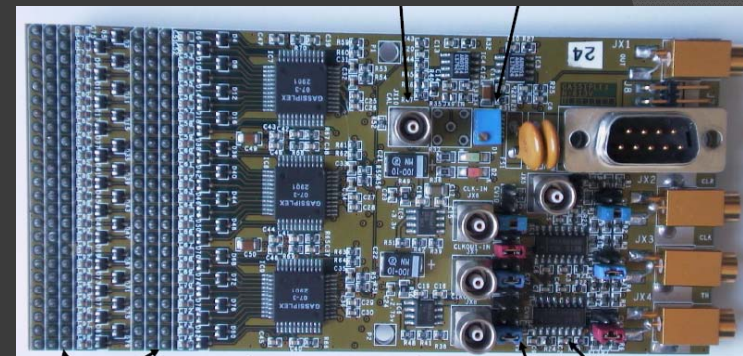
- Available in large areas called Bulk Micromegas, e.g. $40 \times 40 \text{ cm}^2$
- Very short recovery time (100-200 ns)
- Narrow charge distribution ($\sigma_t \sim 10\text{-}20 \mu\text{m}$)
- Gas gain of $10^4\text{-}10^5$ depending on gas mixture



- Micromegas DHCAL with 3 mm drift gap in Ar-based mixture
 - Low hit multiplicity and high efficiency
 - Crucial for separating showers (PFA) and measuring the energy

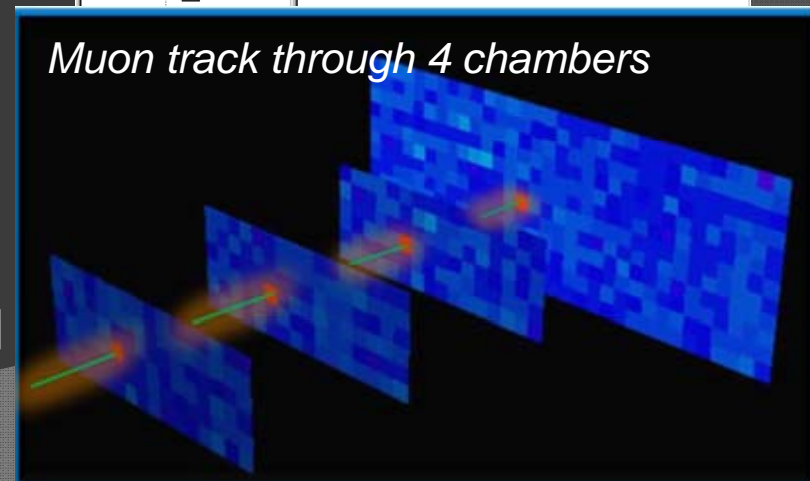
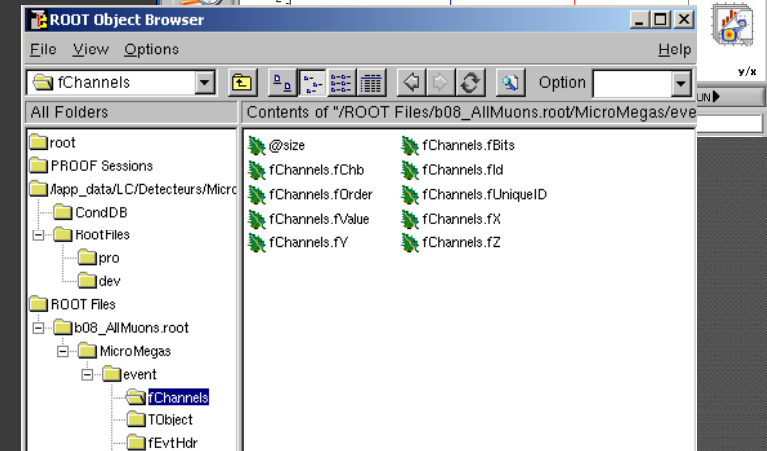
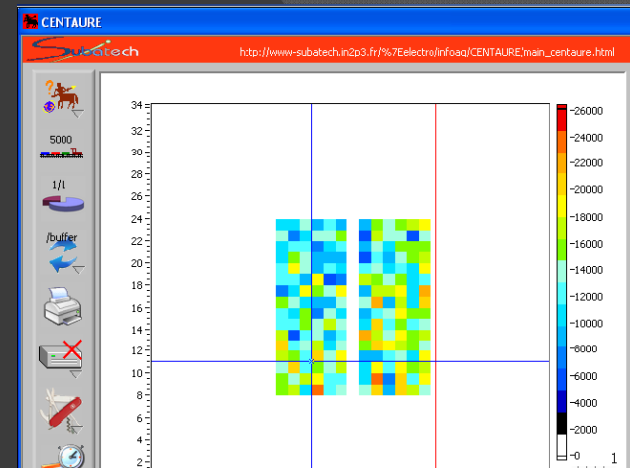
Detectors with analog readout (I)

- Prototypes fabricated at LAPP
 - 3 mm drift gap, 1 cm² pads , 6x12 cm² to 8x32 cm² area
 - With analog or digital readout
 - Short term plan (this year): 1 m² prototype
 - Long term plan: 1 m³ prototype
- Analog readout prototypes
 - 6x16 cm² and 12x32 cm² active areas
 - 96 and 384 readout channels
- Gassiplex chips
 - 16 channels with preamplifier and shaper
 - 6 chips per Gassiplex card
 - Multiplexed-signal directed to VME modules for digitization



Detector with analog readout (II)

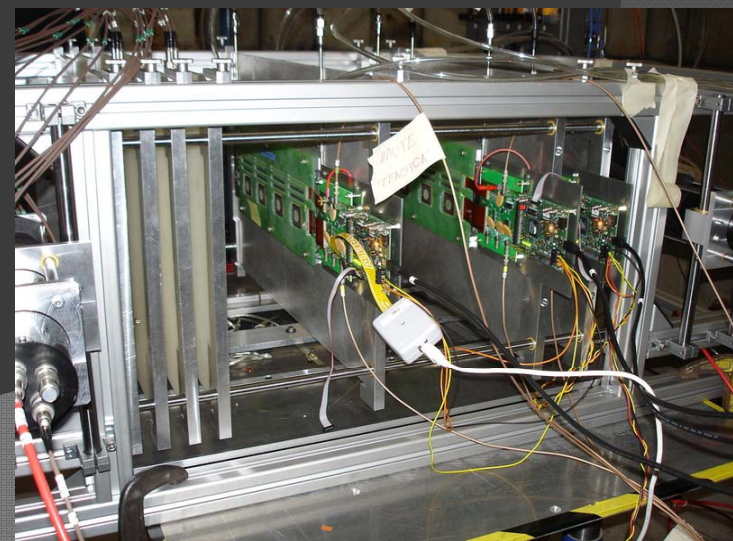
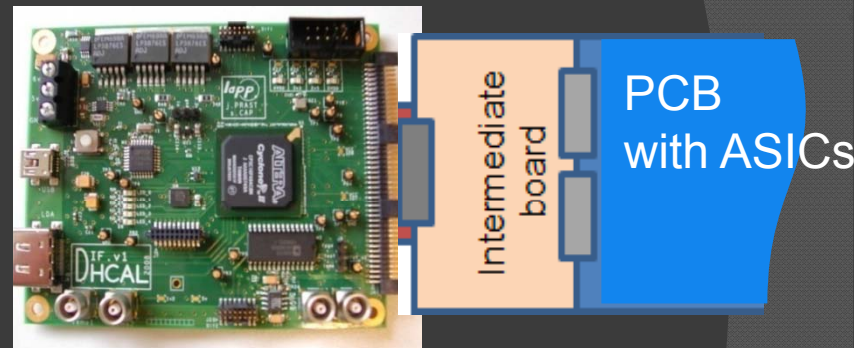
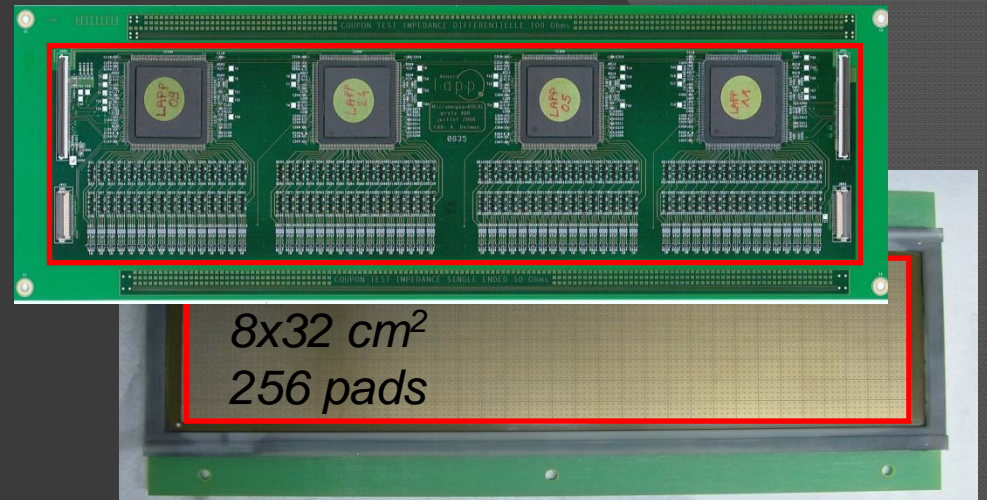
- Data acquisition
 - CENTAURE program
 - Read VME or CAMAC modules
 - Developed by D. Roy at SUBATECH/Nantes
 - Binary files
 - Home-made C++ programs
 - Data storage
 - TTrees and Root files
 - Analysis
 - Event display
 - Signal distribution
 - Efficiency
 - Multiplicity
- Beam test in Aug. & Nov. 08 at CERN
 - Analysis on-going



Muon track through 4 chambers

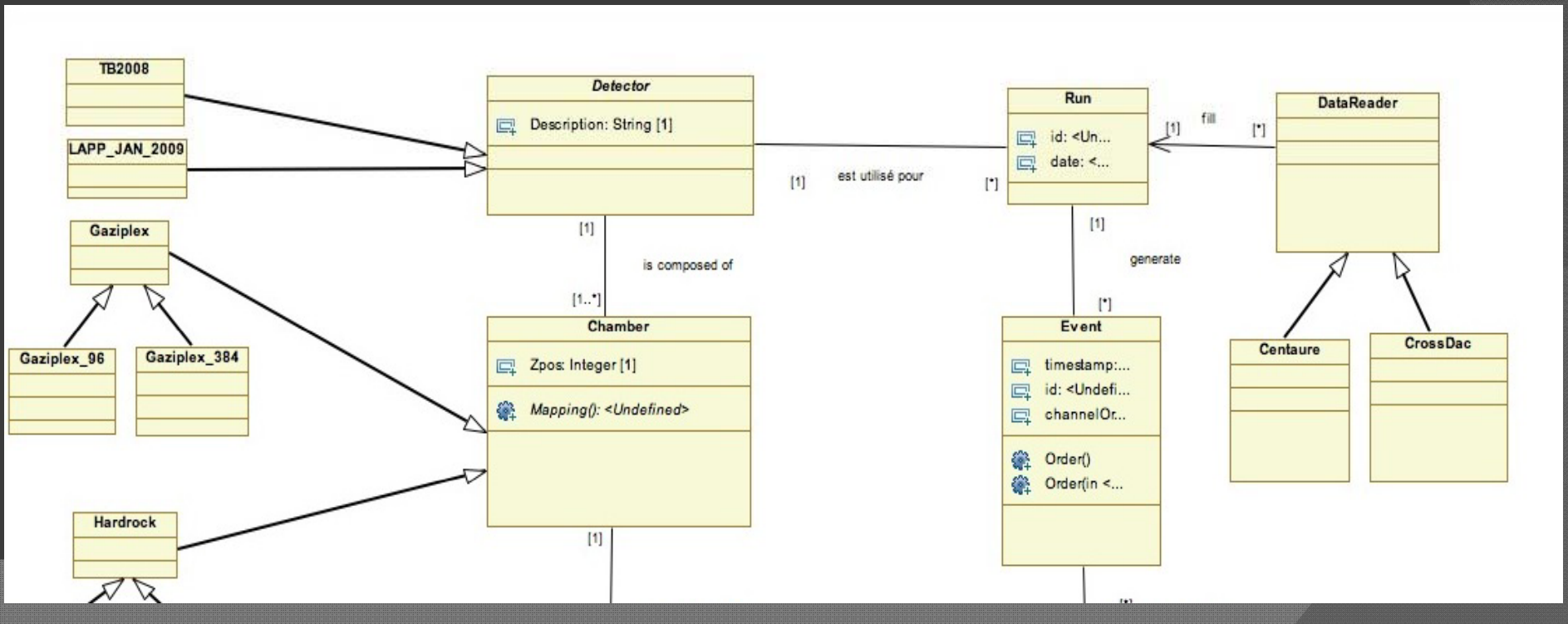
Detector with digital readout

- Detectors
 - HARDROC or DIRAC chips embedded in the PCB
 - $8 \times 32 \text{ cm}^2$ and $8 \times 8 \text{ cm}^2$ realized so far
- Chip/PC interface: DIF board
 - Separated from the SLAB
 - For large number of ASICs HARDROC, DIRAC and also SPIROC and SKYROC
- Software: CrossDaq
- Beam test in Nov. 08 at CERN
 - Not much time for preparation
 - HV stability issues
 - Data analysis not started yet



Common software for analog/digital readout

- Different detectors, data format and DAQ: need flexibility
- New Object Oriented program almost ready for use
 - Event reconstruction of any Run
 - Different chambers and initialization files for the DAQ softwares (Centaure, CrossDaq)
 - Allows for any combination of chambers
 - Run description contained in an XML file
 - Will also provide tools for data analysis



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<micromegas>

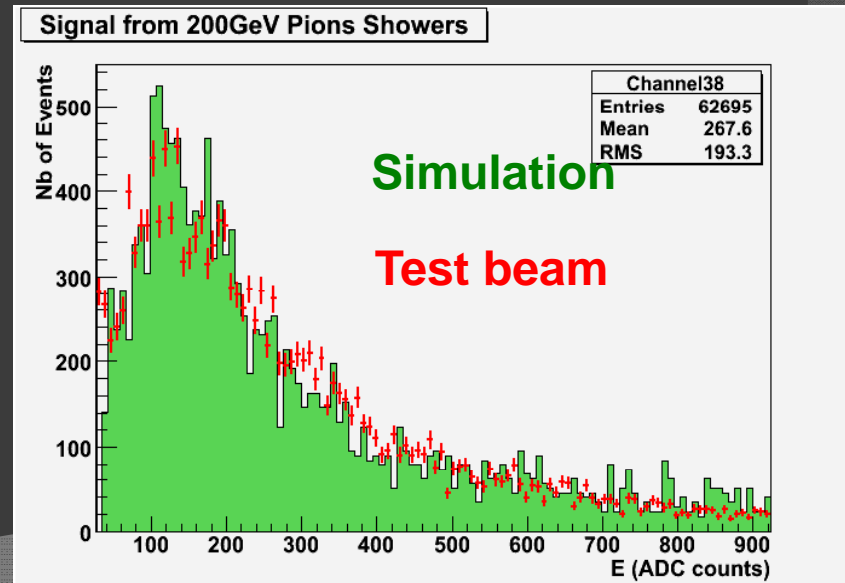
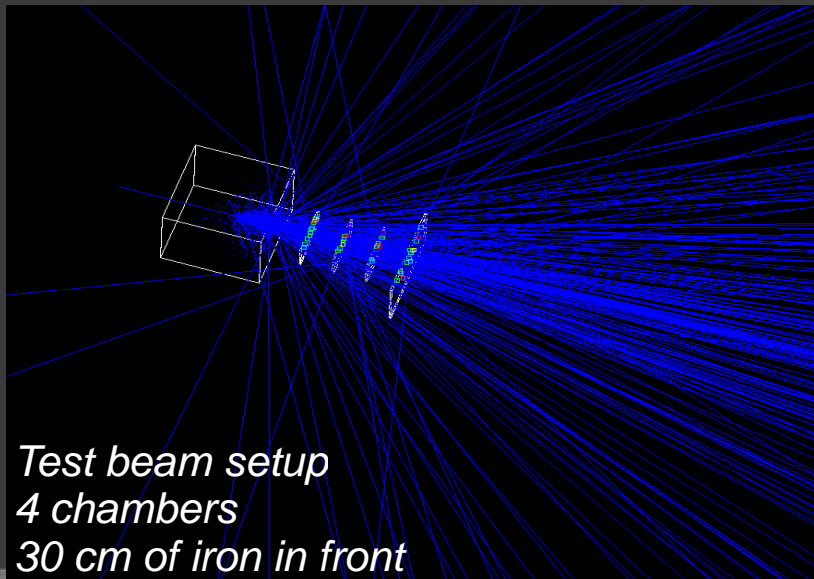
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  <info date="020109"/>

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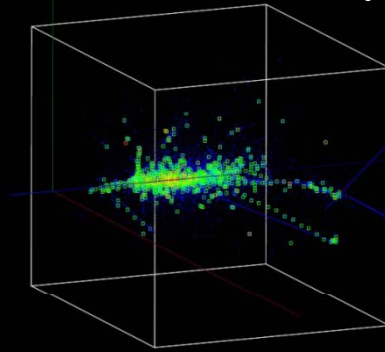
  <output root="/lapp_data/LC/Detecteurs/MicroMegas/data/testbeam.root" />

  <detector name="TB2008" description = "Detector stack used for the CERN November 2008 test
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</micromegas>
```

- Simulation tools:
 - SLIC full simulation of SiD concept (Geant 4)
 - Analysis done with Java Analysis Studio 3 (JAS3)
- Simulation of setup of 2008 beam tests
 - Comparison with real data
 - Better understanding of our detector
 - Preparation for next test beam

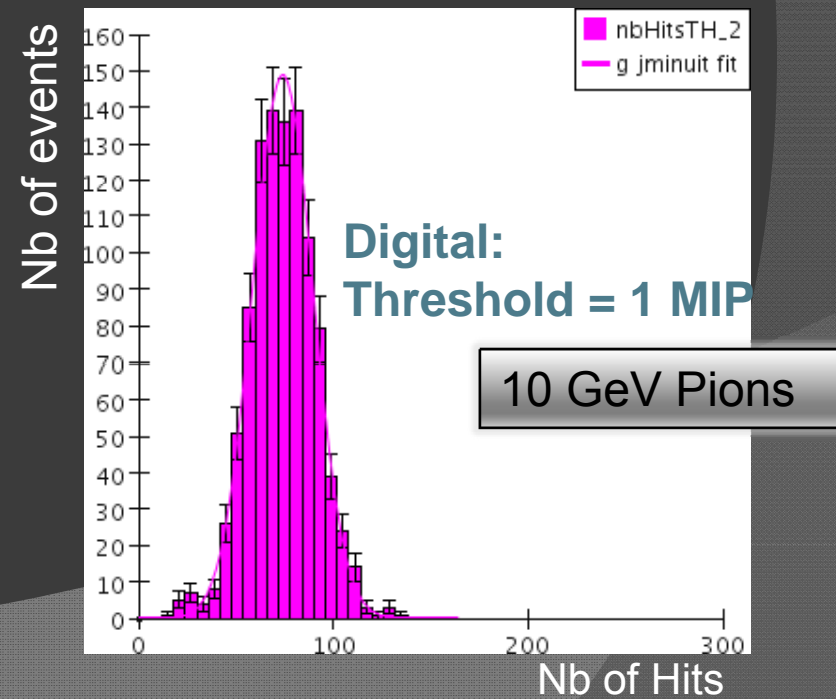
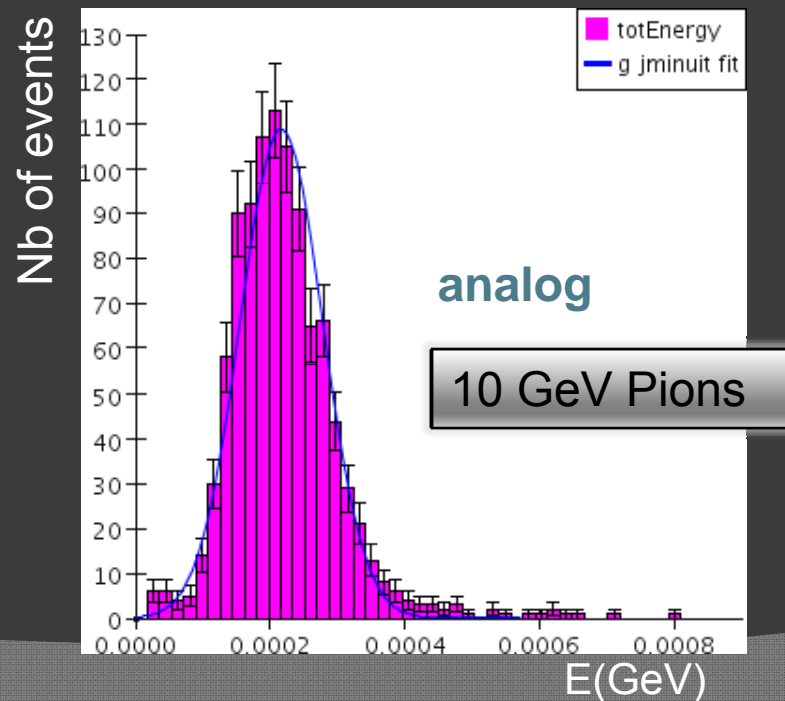


1 m³ HCAL with 40 planes



- DHCAL with Micromegas:
 - 1 m² prototype geometry implemented
 - Ideal Micromegas, digitization not yet implemented

- Look at the energy resolution of a 1 m³ calorimeter
 - Number of hits VS deposited energy for different gas thicknesses, N layers, absorbers
 - Digital/semi-digital: how many thresholds, at which values?



Thanks for your attention

Acknowledgements

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