

• IB Functions

• Routing:

- .Lanes - 4 ?
- .Buffers for LVDS (3 pr / lane)
- .17 single-ended + 3 differential: needs ~8mm per lane
- .Power I/P: how (connector, which side,...)

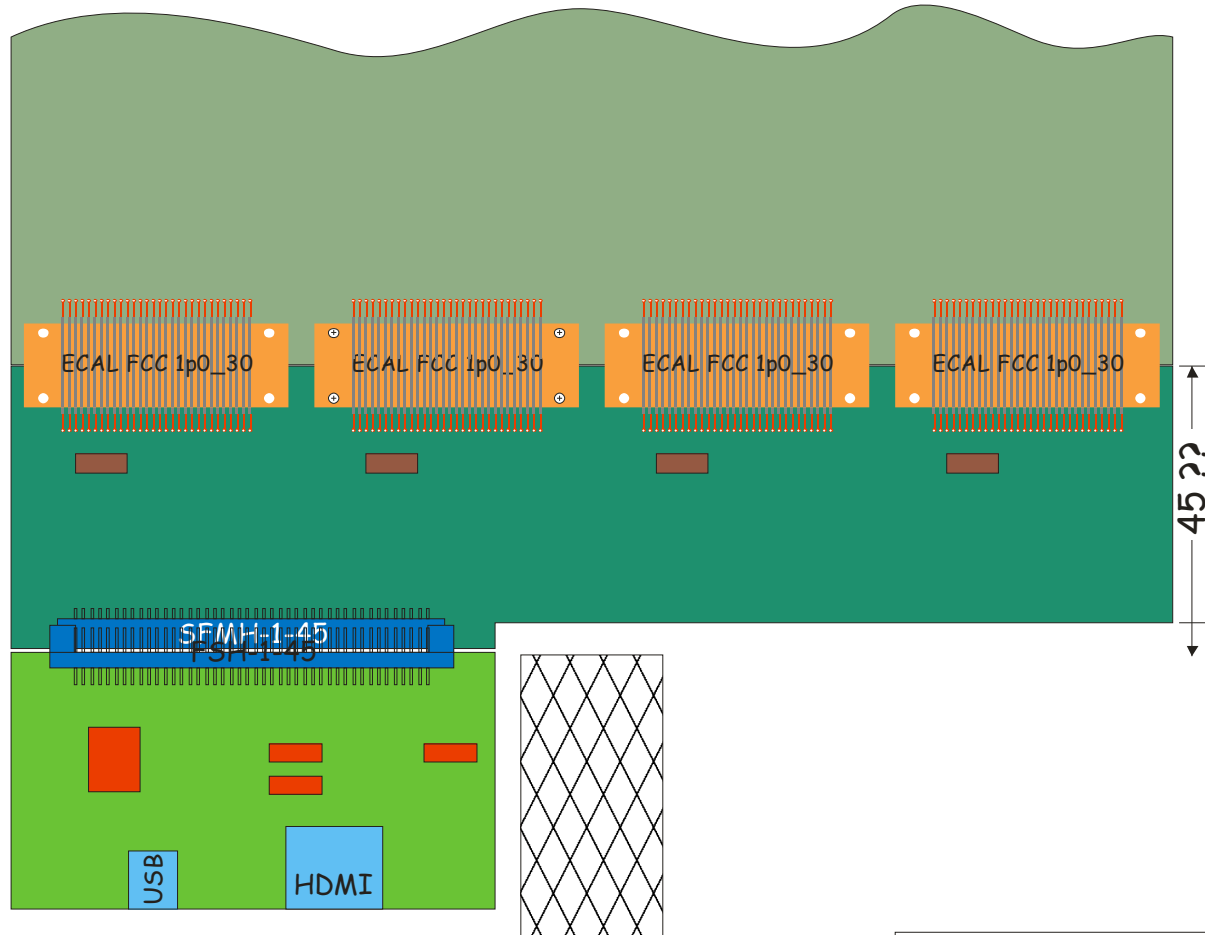
• Power: LDO Linear regulators (?), What I/P Volts?

- .For SLAB: SuperCap, regulator
- .For DIF: 3v3, 2v5, 1v8
- .For IB: 2v5 for LVDS buffers

- **IB Features**

- Match ASU thickness?
- Layers? - 6 or 8 - probably 8 for balanced build
- Shape? Cutouts for SuperCap and DIF connector
- Provide ASU stagger for EUDET Proto?

ECAL Intermediate Board & DIF (IB & DIF)



Not to scale

V0 of 2008-12-17

EUDET DIF/IB

ECAL DAQ: ASU