Muon System Status

Henry Band University of Wisconsin Gene Fisk Fermilab

Outline

- LOI Status
 - Good shape
 - Minor edits required
 - Move scintillation option to appendix?
- R&D proposals
 - Mostly focused on electronics/DAQ
 - Applicable to HCAL

• RPC

- Princeton -Aging
- U. Of Wisconsin -KPiX/IHEP RPCs
- Scintillators/SiPMs
 - Wayne State, Indiana,
 N.I.U., Notre Dame
 - Fermilab
 - INFN(Trieste)/Udine

New Web Page – Doug Wright LLNL

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Org Chart	Muon System Working Croup		
Participating Institutions			
Meetings			
Nonthly Collaboration Meeting	Inbox: Use this page to upload your files.		
Weekly Meetings			
Workshops and Conferences	Muon R&D		
Previous Events			
Documents	Meetings (All on one page)		
Simulation	08-11-17 LCWS2008 Chicago Muon Talks		
Detector versions	<u>08-09-17 SiD Workshop Boulder Muon Talks</u>		
Working Groups	U8-04-14 SID Workshop RAL Muon Talks O8-01-28 SID Workshop SLAC Muon Talks		
Web Site	07-10-22 ALCPG Fermilab Muon Talks		
Recent Updates	<u>07-05-30 LCWS 2007 DESY Muon Talks</u> 06-10-26 SiD Workshop SLAC Muon Talks		
Index	<u>05-12-16 SiD Workshop Fermilab Muon Talks</u>		
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Links	SiD related meetings since March 2008		
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	Send comments to Doug Wright (wiki doc icon glossary)		A Y
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Muon LOI

- LOI baseline is double gap RPCs operating in avalanche mode
- Alternate technology is scintillating strips read by SiPMs
- LOI section is 11 pages, > ½ on R&D
 - 1 page of references
 - 1 page R&D table

- 10-11 layers not yet optimized
 - ~1 cm resolution
 - 4500 m²
- Backgrounds
 - 3 10⁻³/cm²- train from beam halo induced muons
 - *10 at small radii
 (30cm) from 2γ
 hadrons & μ's

Muon /Flux Return



- Steel thickness determined by flux return requirements
- Modest detector resolution needs can be meet by scintillator strips or RPCs

- 10-11 layers
- ECAL + HCAL + Solenoid = 6 λ
- Muon = 14λ
- Study of pion misidentification vs cut on penetration depth in steel flux return, 10<p<50 GeV/c flat distribution



RPC Baseline

- Double gap RPCs operated in avalanche mode
- RPC and steel boundaries staggered to minimize geometric inefficiencies
- > 93% eff. per layer
- Digitized by KPIX64





Scintillating Strip – Alternate Technology

 Double layer of extruded "Minos" style scintillating strips - 4 cm by 1 cm by 2-6 meters long







- Single ended readout
- Need to measure photoelectron yields with SiPMs

RPC R&D – Wisconsin - H. Band

- RPC/KPiX Studies
 - Continuation of LCRD grant to study use of KPiX chip to digitize avalanche mode RPC signals
 - In collaboration with SLAC KPiX group Herbst, Freytag
- Progress to date
 - "Proof of concept"



RPC R&D – Wisconsin (2)





strips ->

• FY2009 Milestones

- Relocate test-stand
- Make current, rate, and efficiency measurements of IHEP test RPCs operating in avalanche mode.
- Readout multiple RPCs with 1 KPiX(v. 7) chip
- Readout negative RPC signals with KPiX(v. 7)
- Test KPiX (v. 7 & v. 8) trigger and reset operating modes.
- Optimize RPC/KPiX interface board design to maximize efficiency and minimize strip multiplicity.

RPC R&D – Wisconsin (3)



- FY2010 & FY2011 Milestones:
 - Readout multiple KPiX chips
 - Use position and charge information from multiple RPC/KPiX devices to make fitted cosmic ray tracks
 - Study position resolution of RPC/KPiX tracks,
 - Test HCAL prototypes in teststand
 - Study response on IHEP RPCs to HF.
 - Begin IHEP RPC aging studies

RPC R&D - Princeton

- Aging Study for SiD Hcal and Muon System RPCs
- Progress to date



Marble side of BaBar Bakelite plate, the marble-pattern is completely disappeared, also discolored.

Brown side of Bakelite plate shows slightly discolored mark.







Figure 5. HF corrosive action on BES III bakelite surface.

RPC R&D – Princeton (2)

- FY2009 Milestones
 - Purchase optical microscope, open the previously aged RPC and survey the inner surface
 - Set up expanded cosmicray-trigger counter array
 - Prepare 5 new BESIII-type test RPCs
 - Start a new round of aging tests.

- Beyond FY2009
 - Collaborate with IHEP and Gaonengkedi to try out various new Bakelite electrodes
 - Bench top test robustness to HF
 - General performance test for new Bakelite electrode
 - Aging test for the new RPC.

Scint-SiPM Muon/Tail-catcher R&D Feb 17, 2009

G. Fisk, A. Para, P. Rubinov - Fermilab, D. Cauz, A. Driutta, G. Pauletta - IRST/INFN-Udine, R. Van Kooten, P. Smith - Indiana Univ., A. Dychkant, D. Hedin, V. Zutshi - No. Ill. Univ., M. McKenna, M. Wayne - Univ. of Notre Dame A. Gutierrez, P. Karchin, C. Milstene - Wayne State H. Band - Univ. Of Wisconsin

* Non-funded collaborators

Scintillator/SiPM

Priority	R&D Item	Institutions	Personnel
1	Silicon PMs from HPK and IRST - Bench Tests	Fermilab	Si Detector Facility: Para, Rubinov
	Current vs Bias Voltage to establish operating	Indiana	Van Kooten & students
	Voltage, gain, noise rate as a function of	INFN Udine	G. Pauletta & collaborators
	temperature, threshold, etc.	NIU	Hedin, Chakraborty, Dychkant, Zutshi
	Have 150 devices from IRST (Italy) & HPK (Japan)	Notre Dame	Wayne, Baumbaugh, McKenna
	LED pulser development.	Wayne State	Karchin, Gutierrez, students
1	Strip and Fiber Mechanical R&D.		
	Geometry of strip ends + SiPM FE miniature circuit.	Notre Dame	McKenna, Wayne
	Preparation of ~30 scint. Strips w/WLS fiber. QC	Fermilab	Rubinov, Fisk
	checks. Light pulser tests. Instrumentation.	INFN Udine	Pauletta
1	MTest device studies: both strips and	INFN Udine	Pauletta et al
	instrumentation.	Fermilab	Rubinov, Fisk
	Calibration measurements: 1, 2, 3, 4, n, p.e.s	Notre Dame	Baumbaugh
	obsv'd.	Wayne State	Gutierrez, students
	Signal/noise vs. transverse & longitudinal position.	-	
	CAMAC and Minerva electronics.		
2	FE electronics development: AC vs. DC coupling;	Fermilab	Rubinov
	Design of ASIC with temperature compensated gain;	Indiana	Van Kooten
	Strip signal transport, collection and digitization.	Wayne State	Karchin
	Multiplexing digitized signal scheme and design.	INFN Udine	Pauletta
2	Tail catcher with CALICE;	NIU	Chakaraborty, Zutshi
	Beam tests results vs. number of pixels;		
	Gain issues		
3	Fast timing measurements	NIU	Hedin
3	Simulations.	Rochester	Manly
	Testbeam software.	INFN Udine	Pauletta, et al
	Analysis software.	All	
4	Co-extrusion of scintillator and WLS fiber	Fermilab	Fisk
		Notre Dame	Ruchti, Wayne, McKenna

What R&D have we done?

Previous Studies Hamamatsu H7546B 64 channel MAPMTs calibrated using a 5mCi Sr⁹⁰ in contact w/plastic scintillator and WLS fiber to each MAPMT pixel.

Measured both single ended (S) and dual (D) readout.

3 pC for (S), 5 pC for (D) ~50% more light with (D)

Nominal gain ~ 2X10° @ 960 V



FY09 Activities

- Northern Illinois University procurement of new SiPM devices, comparison of LN2 and room temperature operation of SiPMs, CALICE-TCMT operation and analysis
- University of Notre Dame gain and noise of SiPMs versus temperature at room temperatures, comparison of commercial and specialized front-end amplifiers, strip and fiber mechanical R&D
- Indiana University design of bias voltage and temperature control system, test-beam support
- Wayne State University comparison of SiPMs from different manufacturers, test beam support

Very preliminary results



Very preliminary results(2)

- Plan is to pull single p.e. peak from noise data.
- This makes the detector self-calibrating



Very preliminary results(3)

• A scan of the 1.8m bar across the beam gives an estimate of the attenuation length



Summary

- Muon LOI section requires minor edits
- 3 Detector R&D proposals submitted 6 institutions + Fermilab, SLAC, IHEP, INFN
- R&D details in Tuesday parallel session