



# A Large TPC Prototype for an ILC Detector

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On behalf of the LCTPC Collaboration

- Introduction
  - **Linear Colliders, Detectors**
- The Large Prototype TPC
- Beam test setup in Desy
- 3 detector technologies
  - **Test-beam results**
- Laser Calibration setup
- Conclusions and future plans

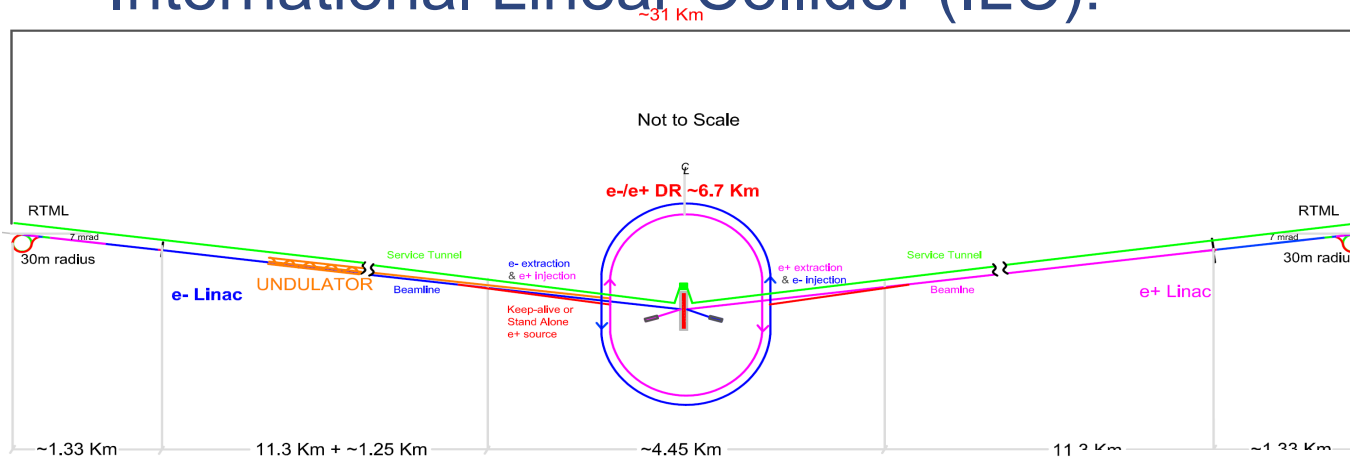


# Future e<sup>+</sup>e<sup>-</sup> Linear Colliders



A future e<sup>+</sup>e<sup>-</sup> linear collider will be needed to study in details the LHC discoveries

## • International Linear Collider (ILC):



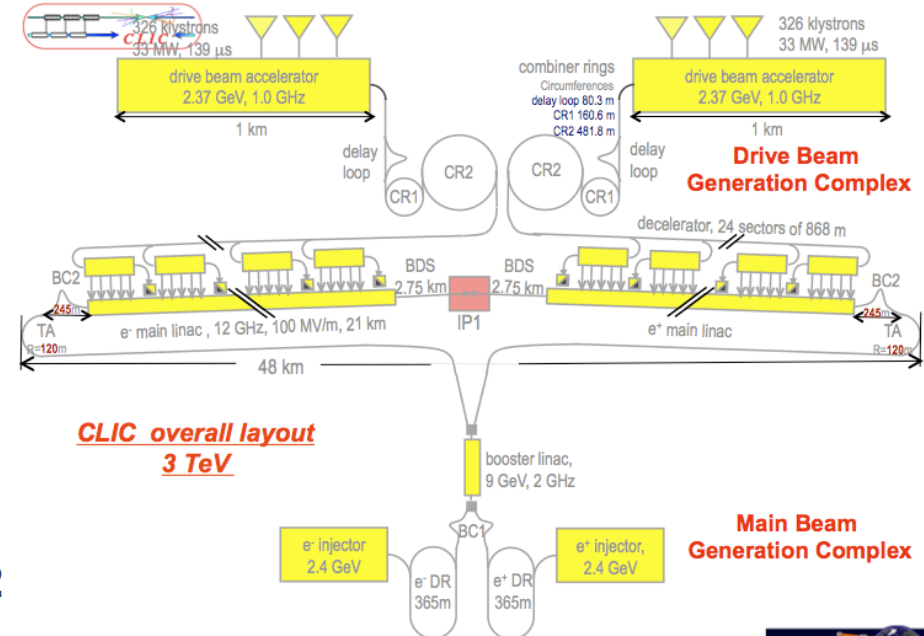
Schematic Layout of the 500 GeV Machine

- E<sub>CM</sub>=0.5 - 1.0 TeV
- L=2x10<sup>34</sup> cm<sup>-2</sup> s<sup>-1</sup>
- Expect EDR phase ends 2012, constr. starts > 2015

## • Compact Linear Collider (CLiC):

- Higher energy (3TeV)
- Compact
- Technology not available before 2015

=> ILC remains the baseline

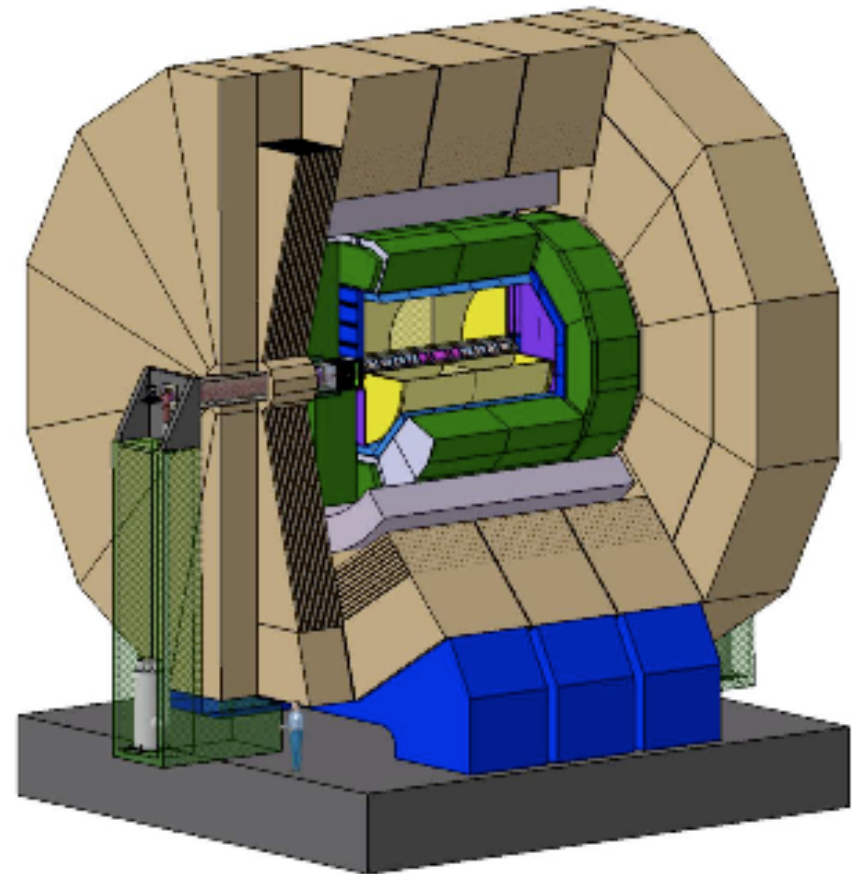


- Concept based on particle flow
  - > large tracker and high granularity calorimeters inside high (3.5T) magnetic field:
  - Si vertex detector
  - Large TPC (L=4.3m, Ø=3.6m)
    - 200 hits/track
    - $\delta 1/pt \sim 9 \cdot 10^{-5} / \text{GeV}/c$
    - $\sigma(r\phi) < 100 \mu\text{m}$
    - Rad length:  $0.04X_0 - 0.15X_0$
    - $dE/dx$  resol  $\sim 5\%$
  - Si envelope (in- and outside)
    - Precise calo impact

## – Calo

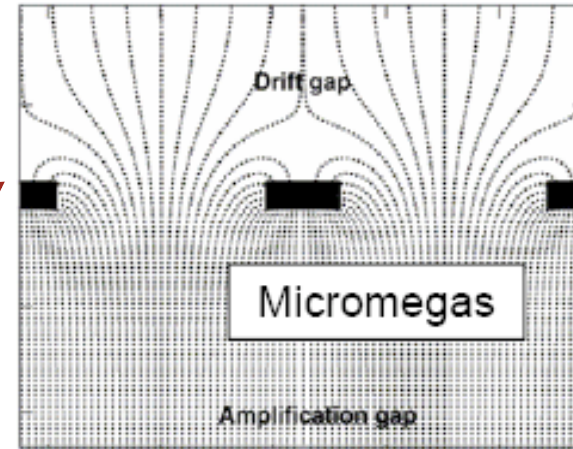
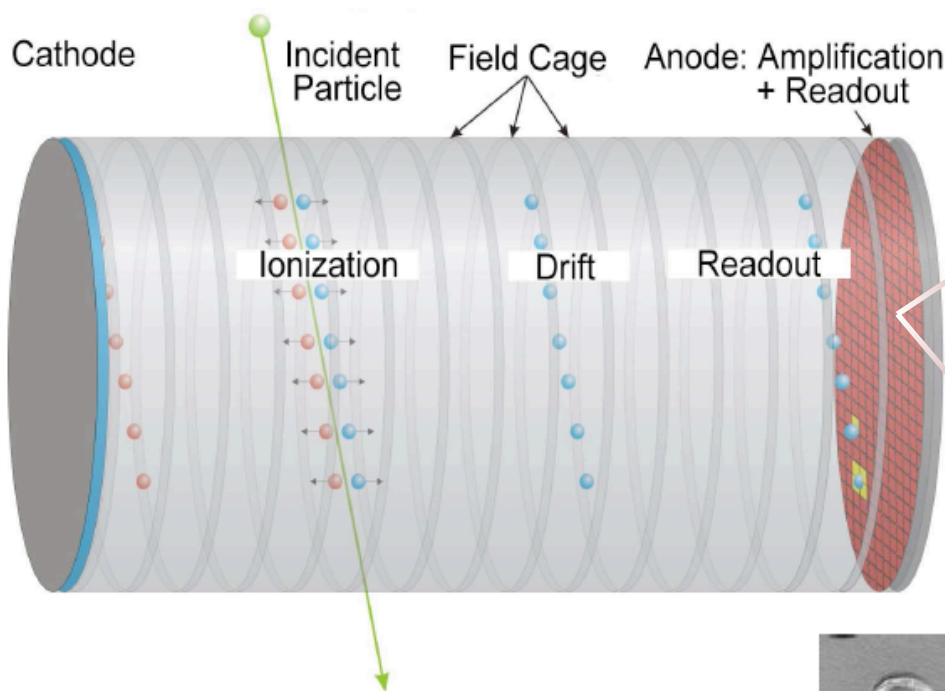
$$\sigma_{E_{em}}/E_{em} \simeq 15\%/\sqrt{E(\text{GeV})} \oplus 1\%$$

$$\sigma_{E_{jet}}/E_{jet} \simeq 30\%/\sqrt{E(\text{GeV})}$$

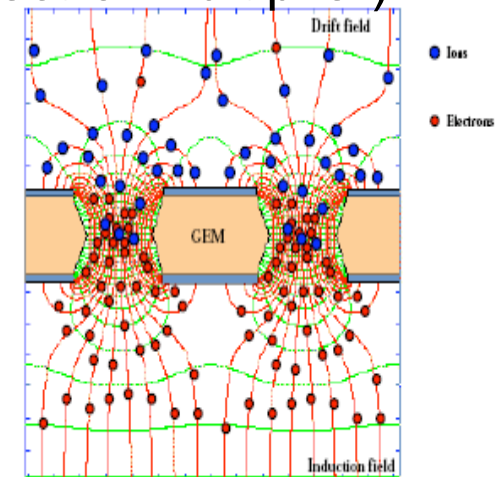
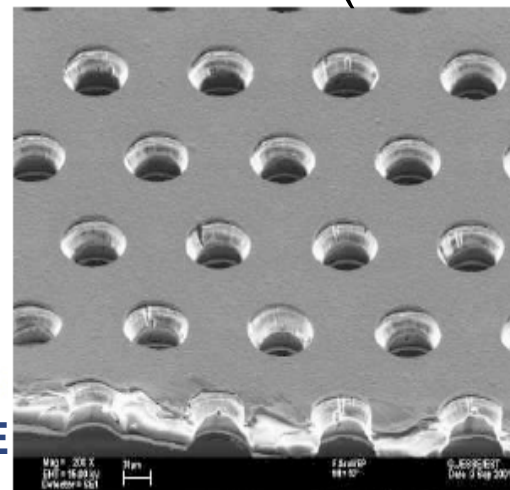




- Micro Pattern Gas Detector (MPGD) instead of MWPC for the electron amplification stage: Not limited by the ExB effects



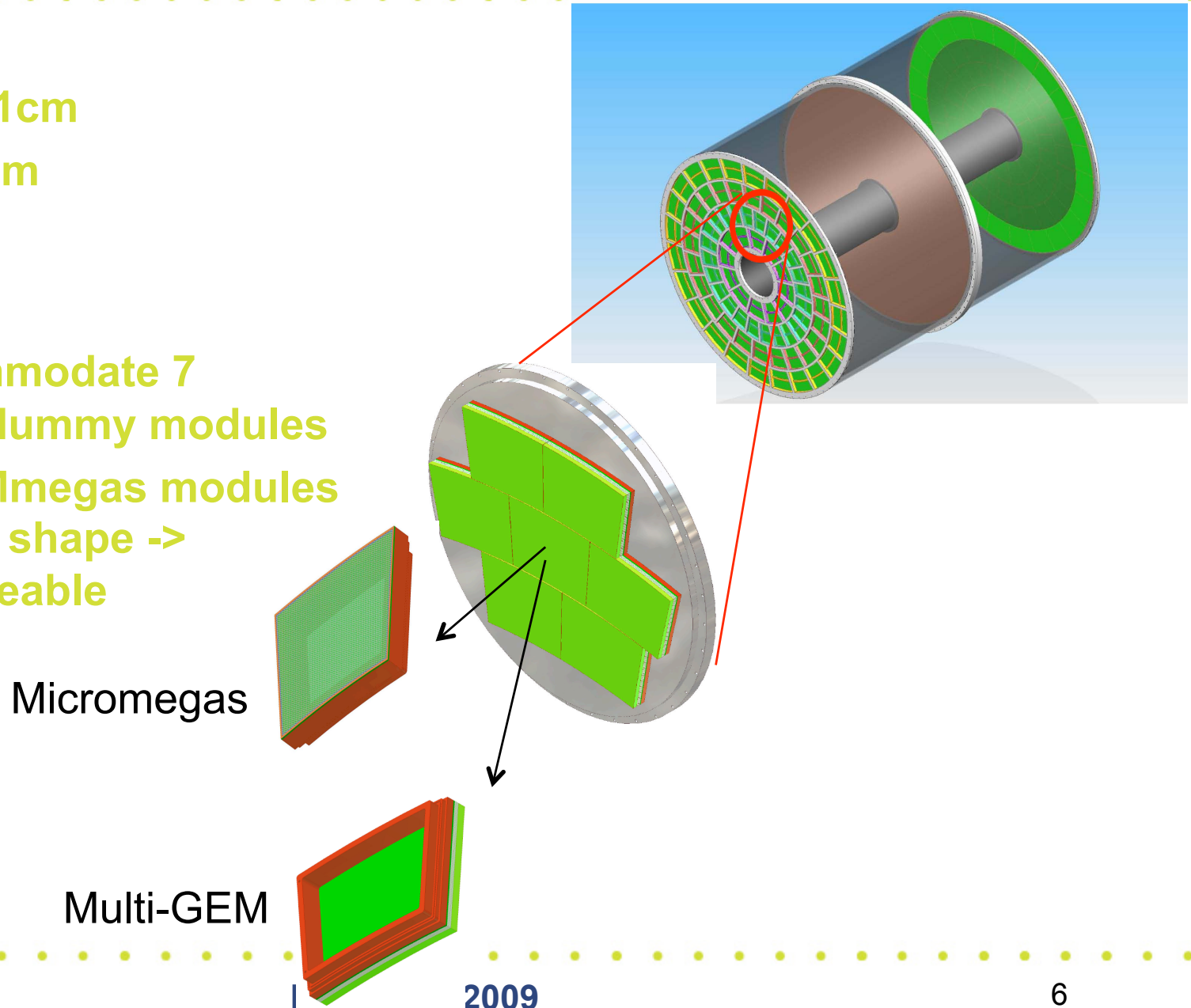
**MICROME GAS**  
**OR**  
**GEM (Gas ElectronMultiplier)**





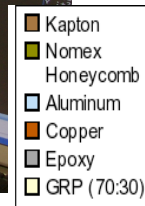
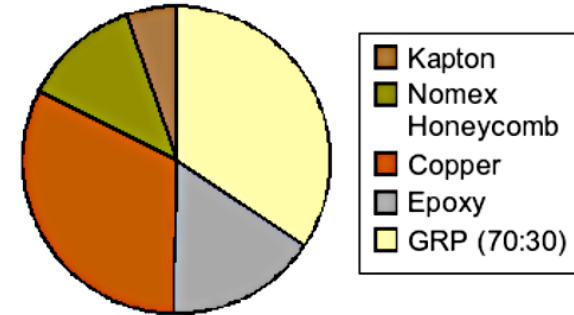
# Large Prototype TPC (LPTPC)

- Dimensions:
  - Length = 61cm
  - Diam = 77cm
- Endplate :
  - Aluminium
  - Can accommodate 7 detectors/dummy modules
  - GEM and Mmegas modules have same shape -> interchangeable

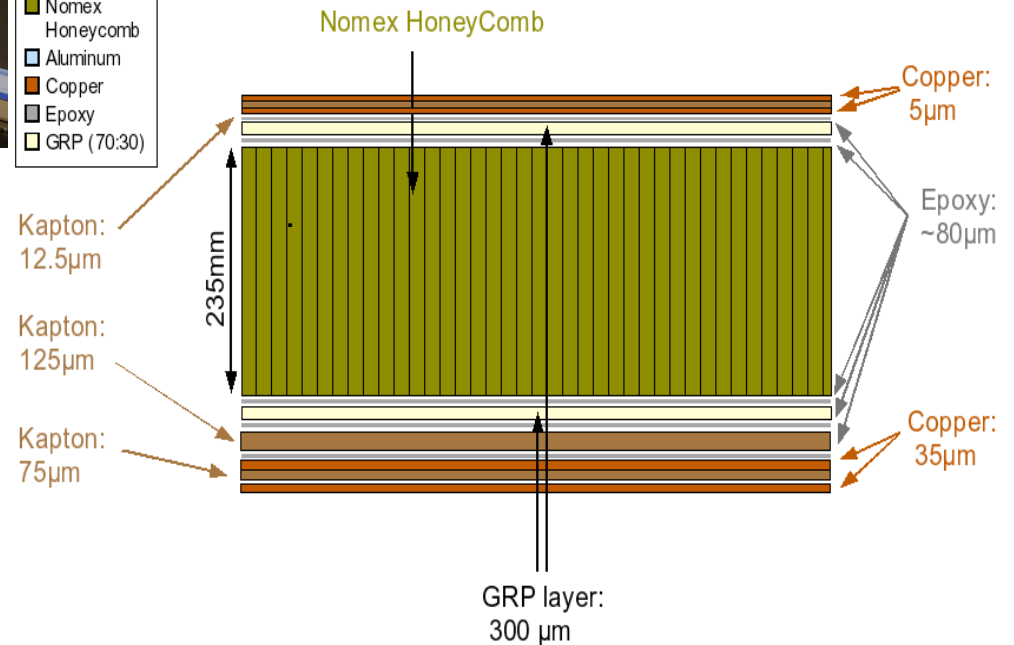




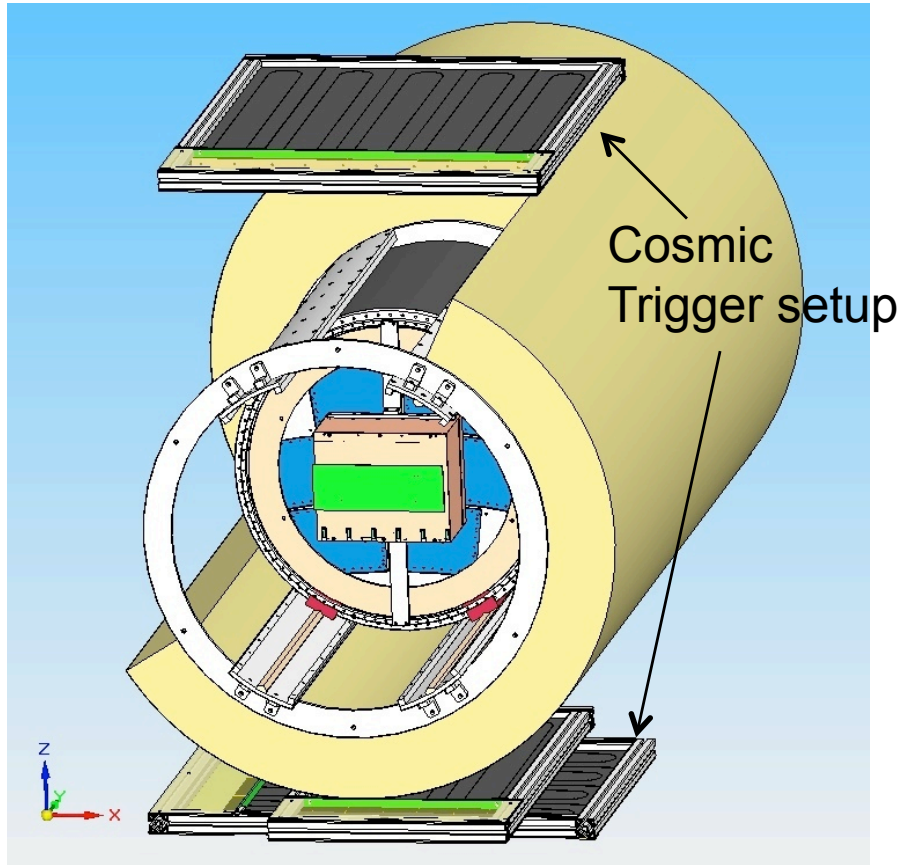
Radiation Length: 1.31% of  $X_0$



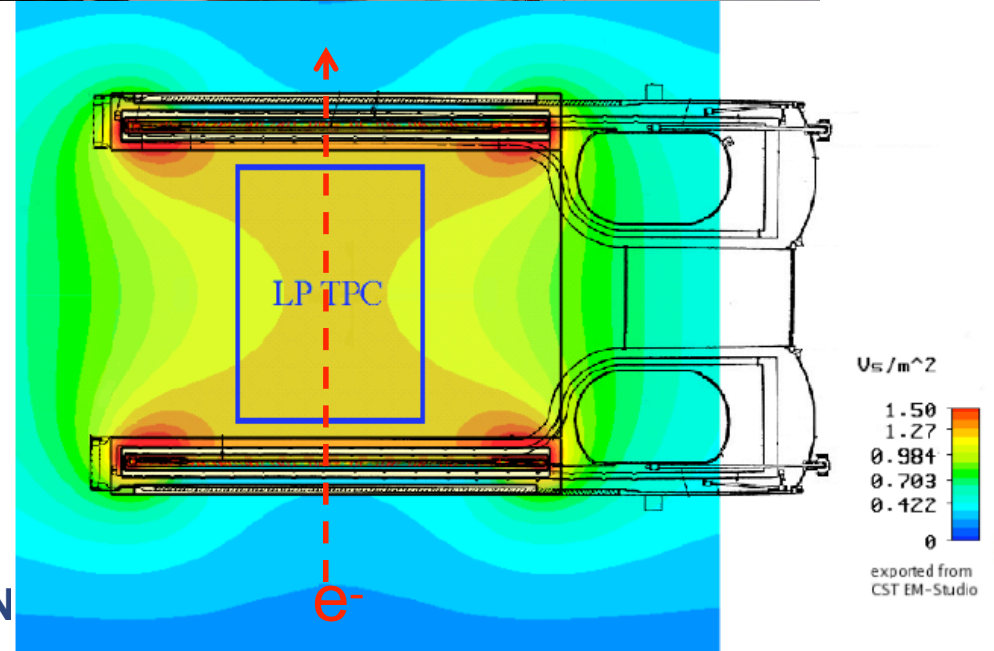
HV : up to 20kV



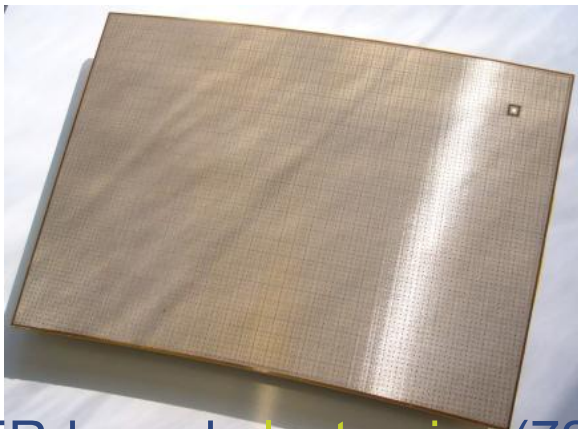




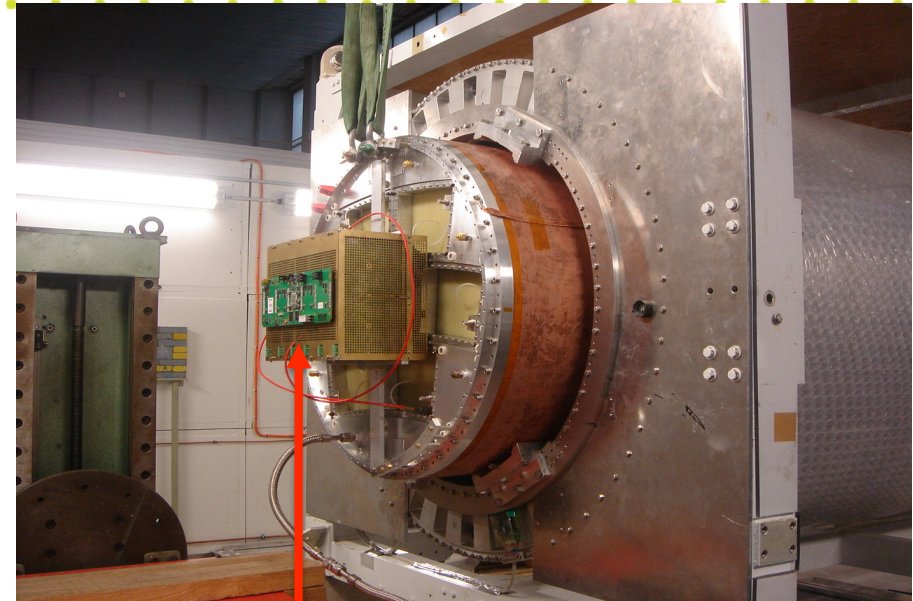
- LPTPC is only half of the LCTPC
- Slided in a superconductor solenoid of up to 1.5 T
- $1 < p < 6 \text{ GeV}/c \text{ e}^-$



- 3 bulk mMEGAS module tested
  - Regular anodes
  - Resistive anodes (carbon loaded kapton) with a resistivity  $\sim 5\text{-}6\text{ M}\Omega/\square$
  - Resistive anodes (resistive ink) with a resistivity  $\sim 1\text{-}2\text{ M}\Omega/\square$
- 1726 (24x72) pads of  $\sim 3\times 7\text{ mm}^2$

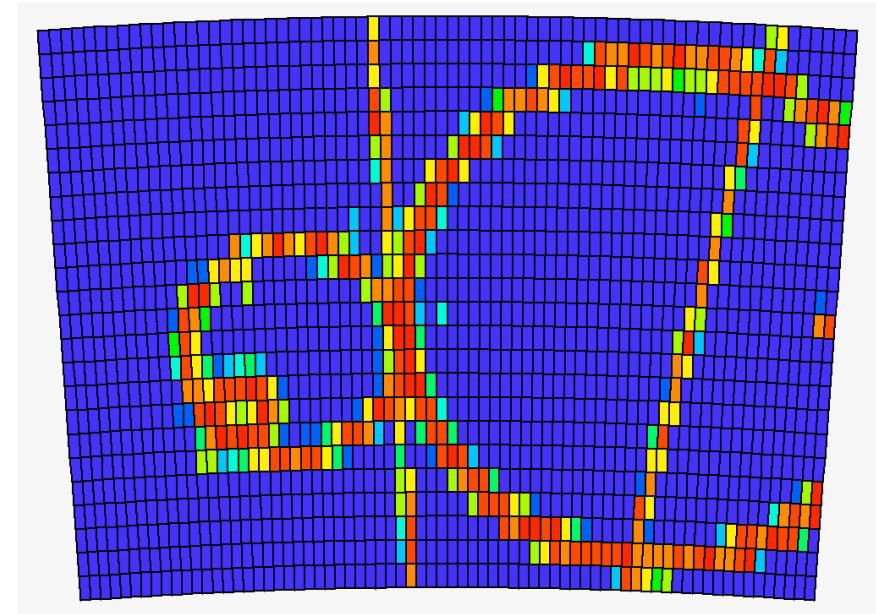
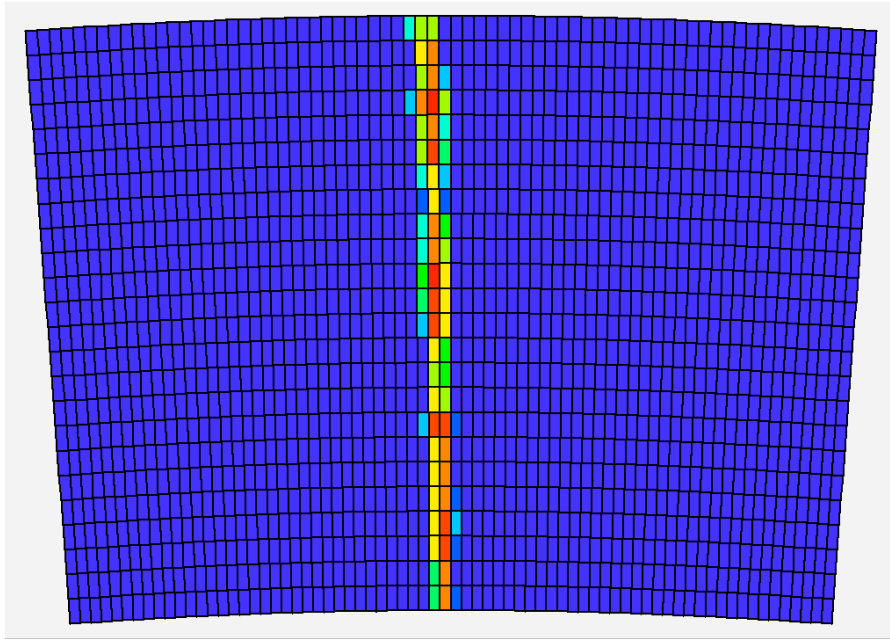


- AFTER-based electronics (72 ch./chip):
  - low-noise (700 e-) pre-amplifier-shaper
  - 100 ns to 2  $\mu\text{s}$  tunable peaking time
  - full wave sampling by SCA

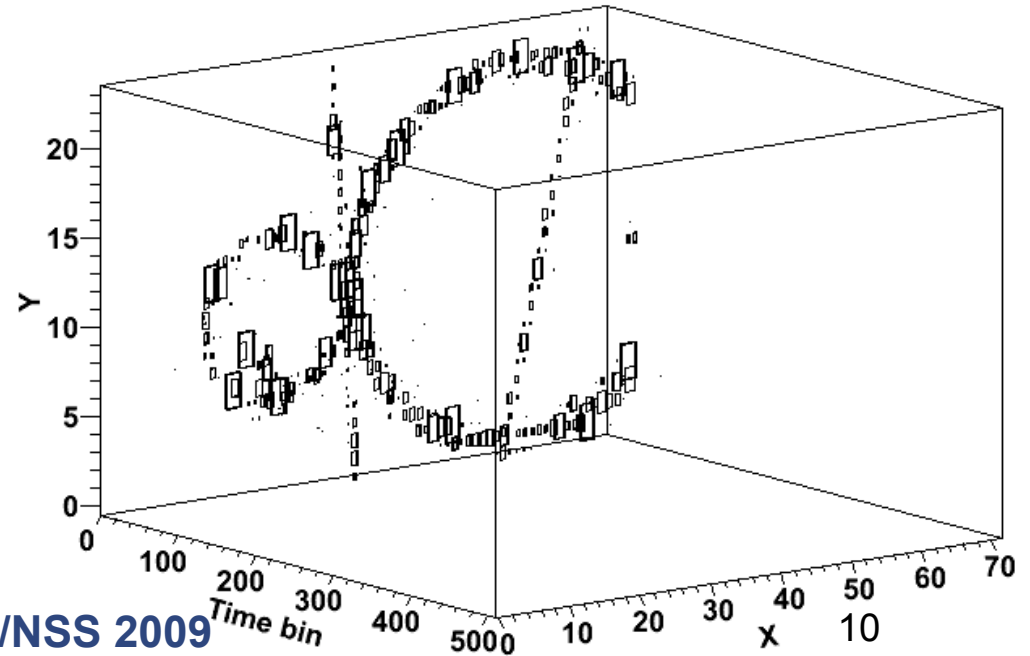




B=1T, T2K gas: Ar/CF<sub>4</sub>/iso-C<sub>4</sub>H<sub>10</sub> (95:5:3)

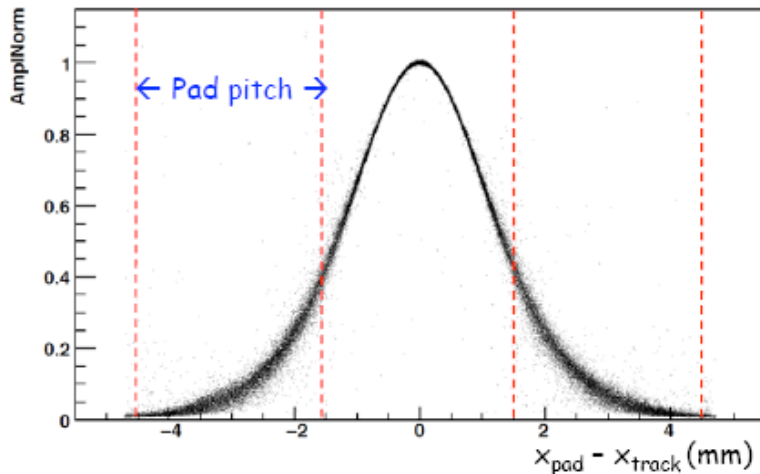


3D



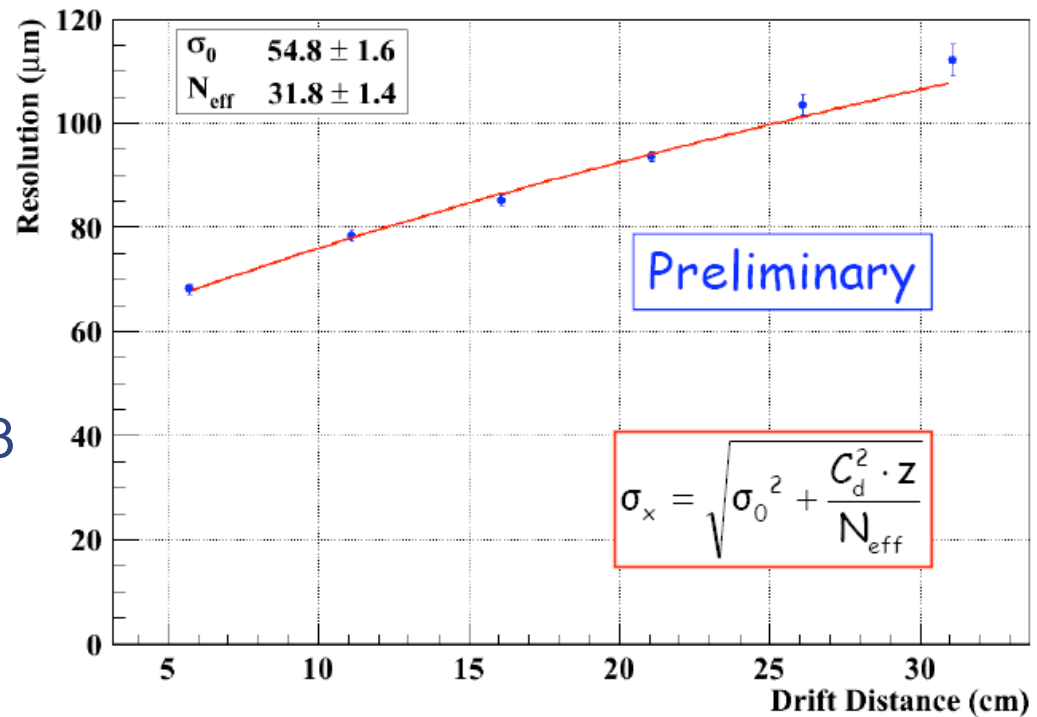
## 5 GeV e<sup>-</sup>, B=1T

- Pad response:
- Fraction of row charge on a pad VS.  $X_{\text{pad}} - X_{\text{track}}$  normalized to central pad charge

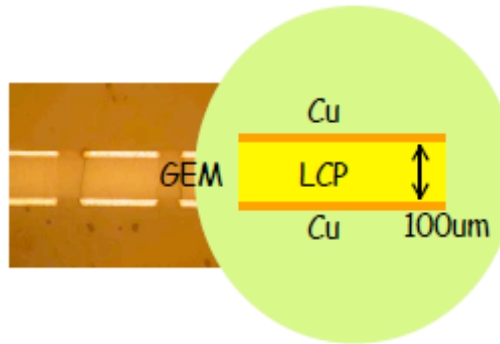


- Clear charge spreading over 2-3 pads (500 ns shaping)

- Spatial Resolution:
- At z=0,  $\sigma_0 = 54.8 \pm 1.6 \mu\text{m}$  ( $\sim$  w<sub>pad</sub>/55)
- Effective number of electrons  $N_{\text{eff}} = 31.8 \pm 1.4$  consistent with expectation

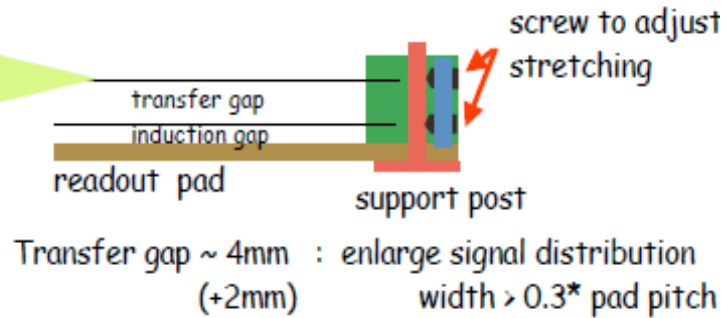


# Double GEM structure

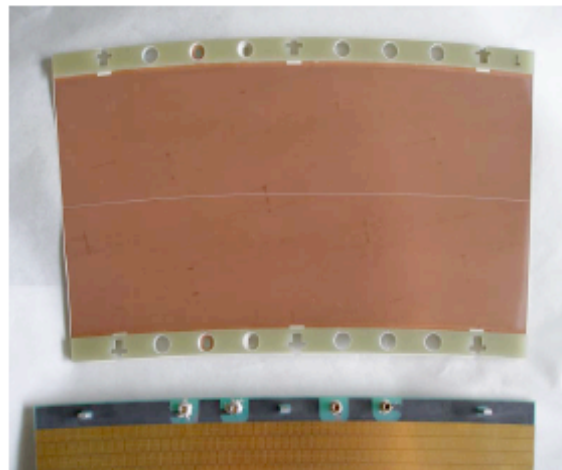
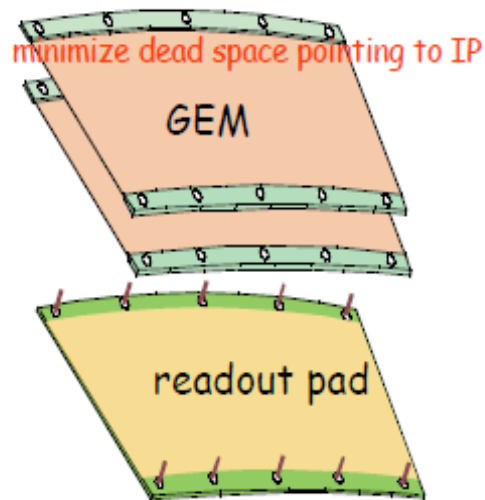


Can we stretch GEM ?

mounting(stretch) mechanism

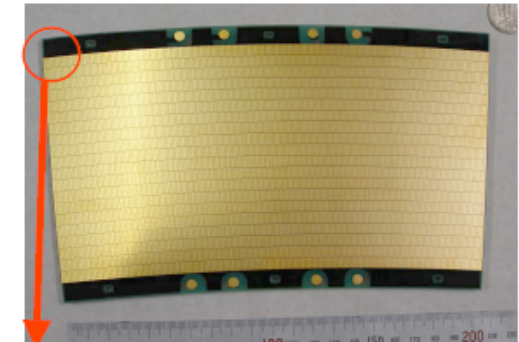


frame : top & bottom frame.  
no side frame



28 pad rows (176/192 pads/raw)  
~1.2(w) x 5.4(h) mm<sup>2</sup>  
staggered every each layer

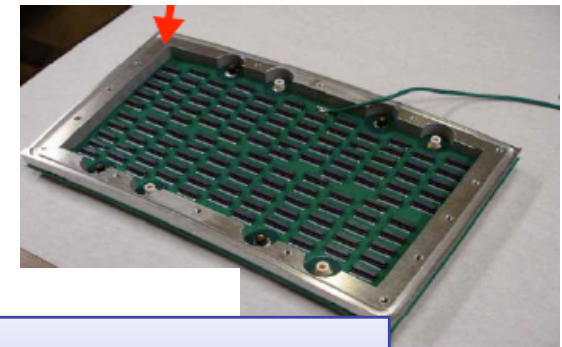
Total 5,152 ch/module



0.5 mm

6 layers PCB  
one GND layer

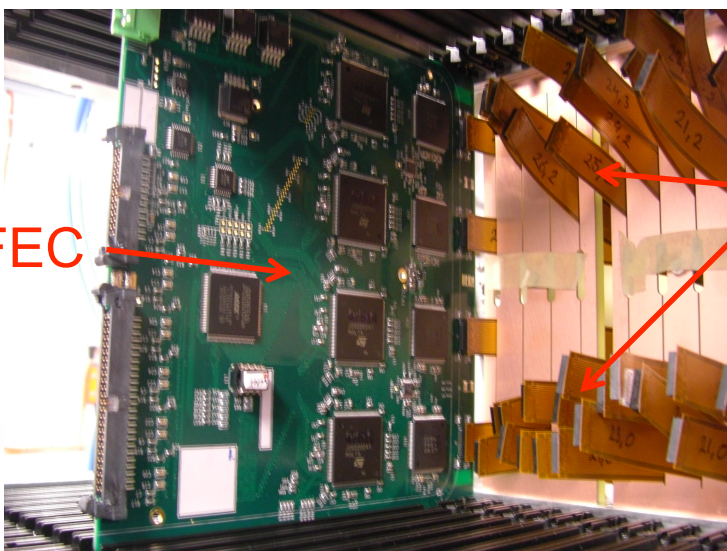
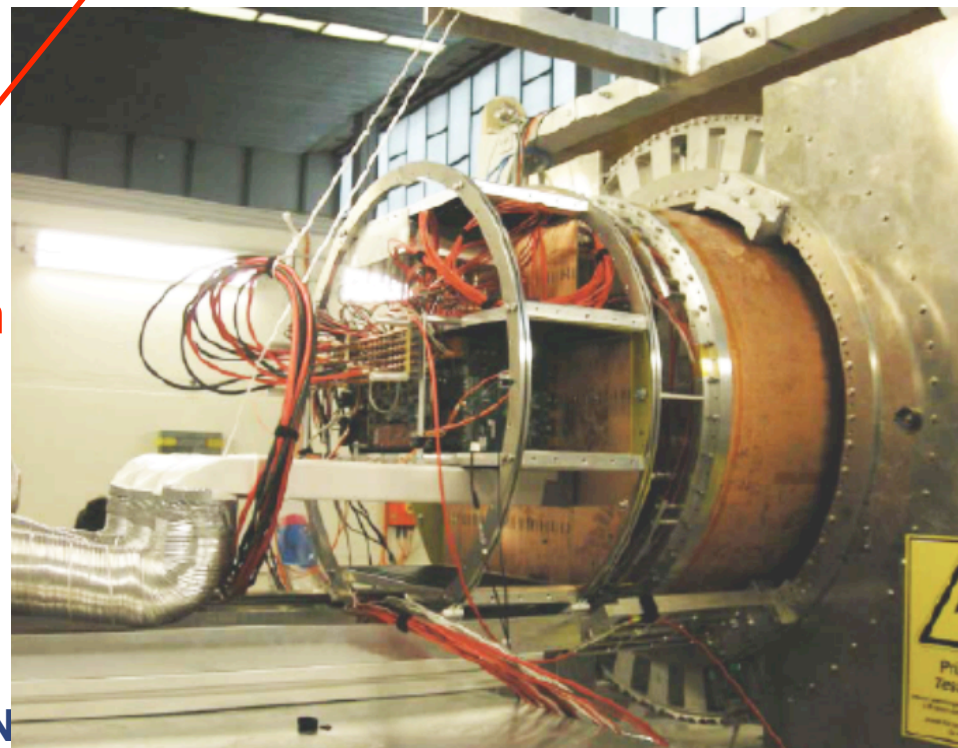
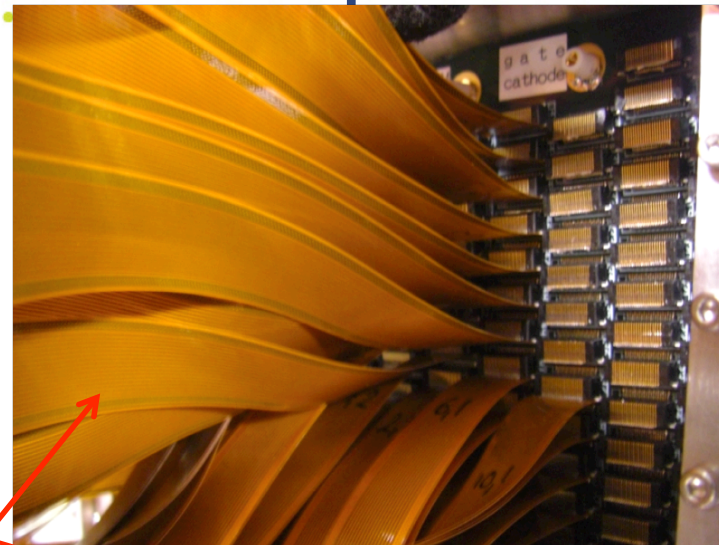
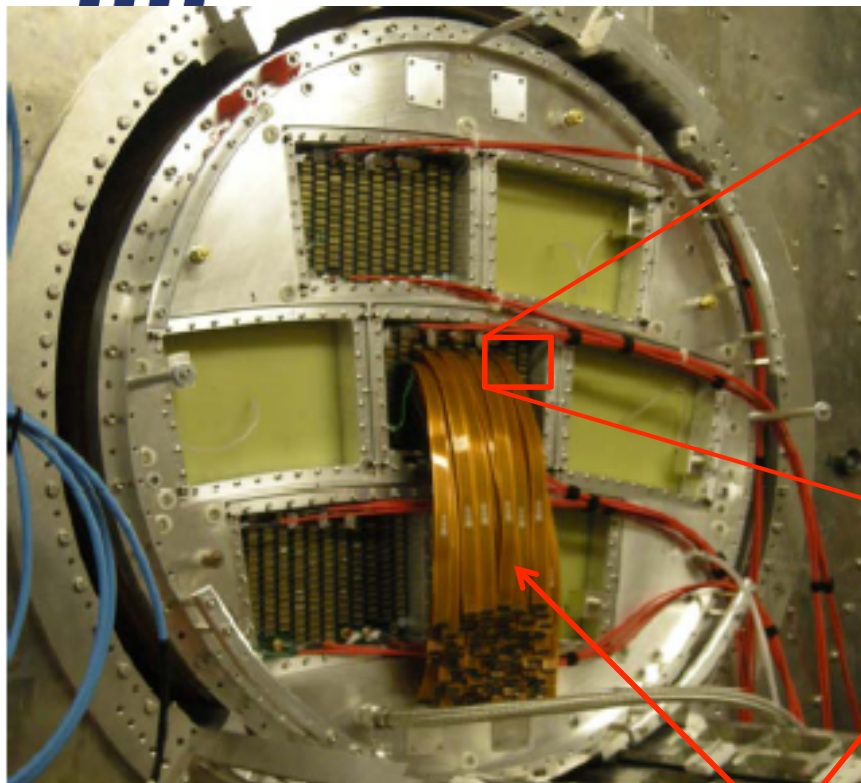
0.5 mm GND



- ALTRO (ALICE) Electronics  
+ new PCA16 preamp. (tunable gain, peak. and decay time, polarity,...)

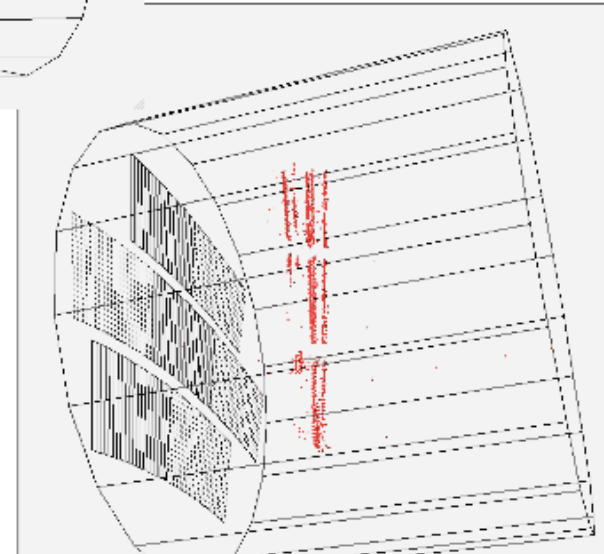
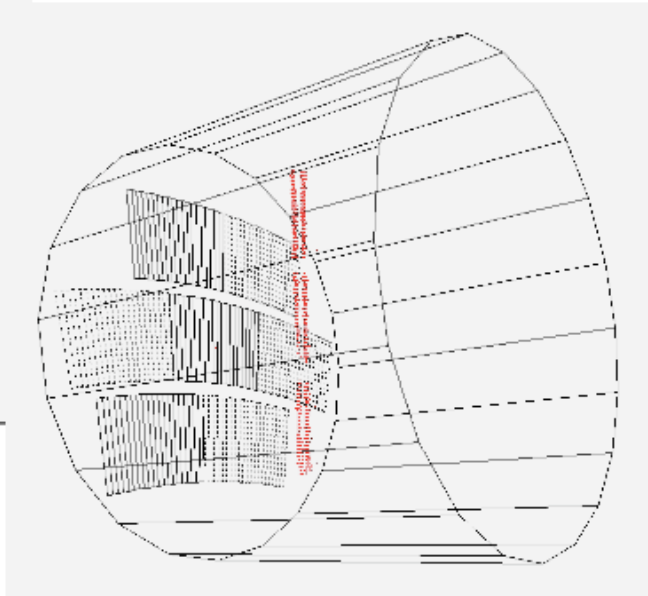
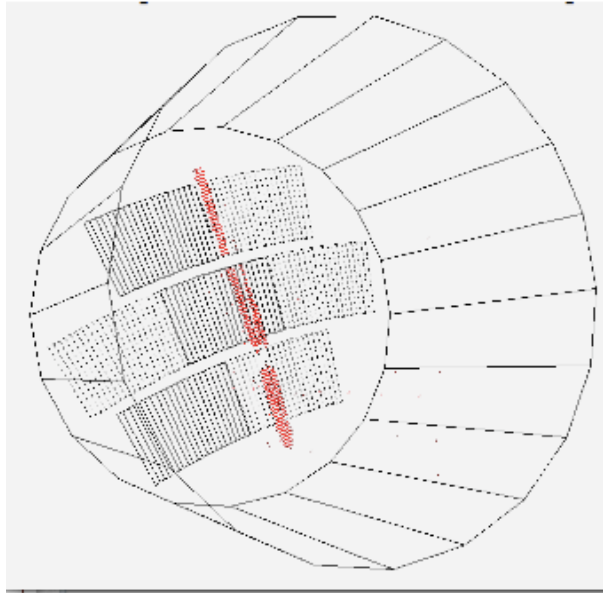


# Double GEM set-up



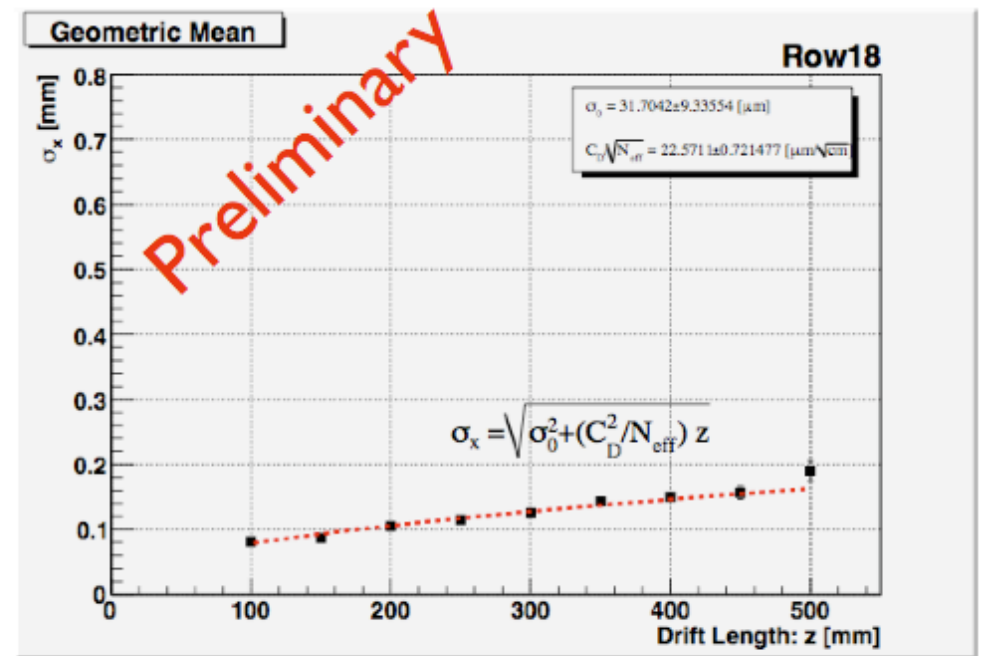
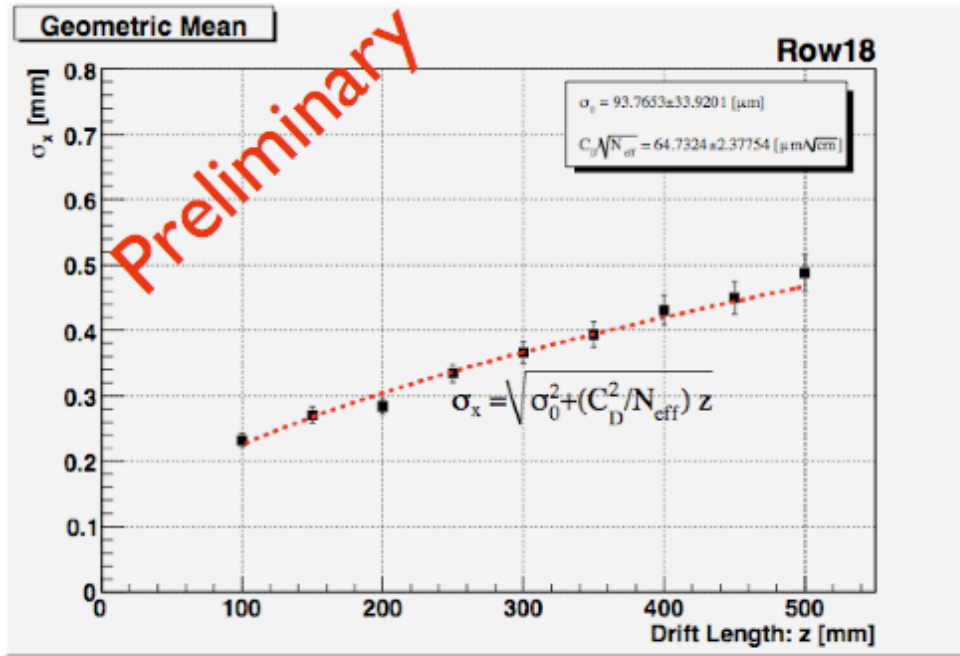
FEC

Kapton cables



At  $z = 0$ ,  $\sigma_0 = 93.8 \pm 33.9 \mu\text{m}$

At  $z = 0$ ,  $\sigma_0 = 31.7 \pm 9.3 \mu\text{m}$



**B=0T**

**B=1T**

**B=0T**

Garfield

**B=1T**

Garfield

$$\begin{cases} C_D = 303 \pm 1 [\mu/\sqrt{\text{cm}}] \\ \frac{C_D}{\sqrt{N_{eff}}} = 65 \pm 2 [\mu\text{m}/\sqrt{\text{cm}}] \end{cases}$$

$$C_D = 311.8 [\mu\text{m}/\sqrt{\text{cm}}]$$

$$\begin{cases} C_D = 101.6 \pm 0.4 [\mu/\sqrt{\text{cm}}] \\ \frac{C_D}{\sqrt{N_{eff}}} = 22.6 \pm 0.7 [\mu\text{m}/\sqrt{\text{cm}}] \end{cases} \quad C_D = 95.4 [\mu\text{m}/\sqrt{\text{cm}}]$$

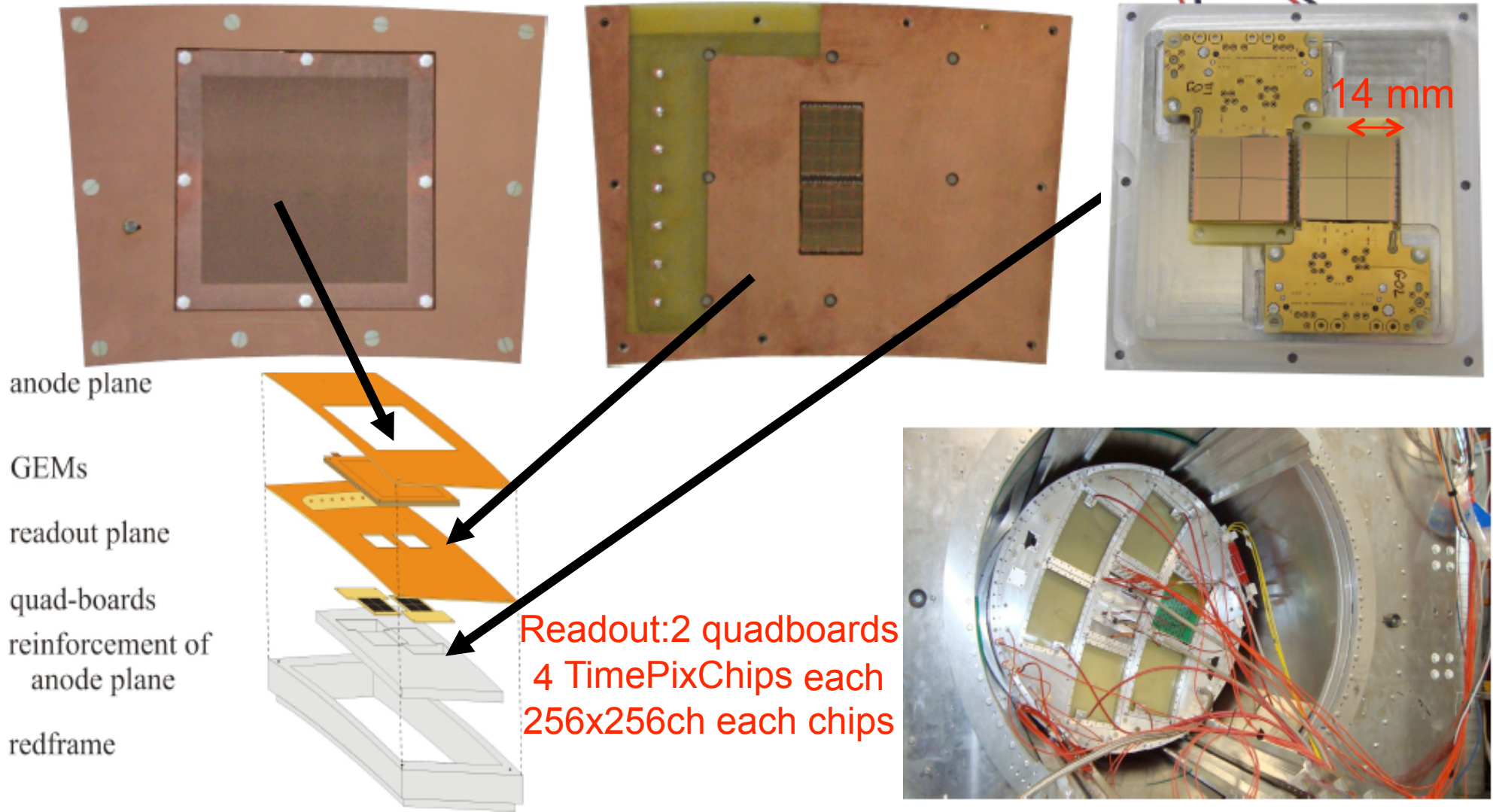
$$\longrightarrow N_{eff} \sim 22 \pm 1$$

Result of MP-TPC  
 $N_{eff} = 21 \pm 2$

$$\longrightarrow N_{eff} \sim 20 \pm 1$$

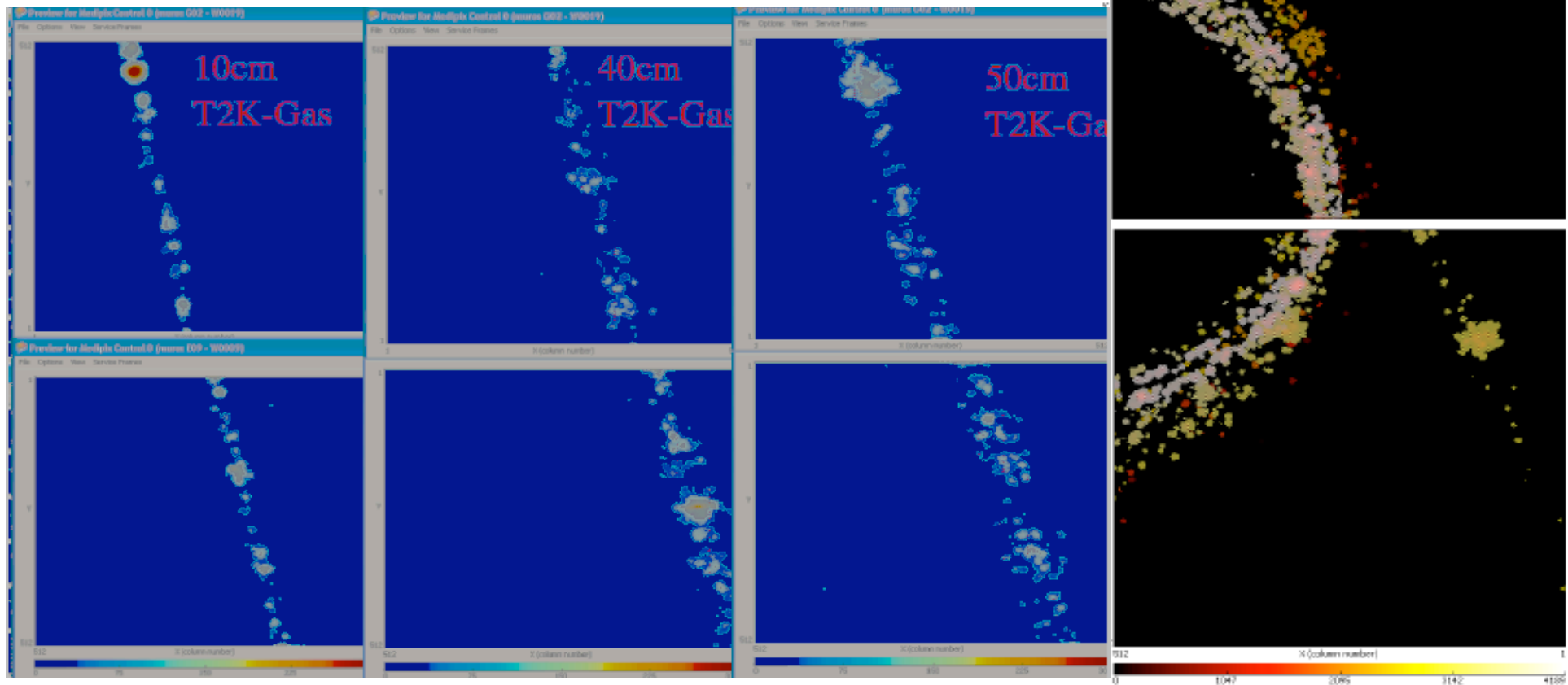


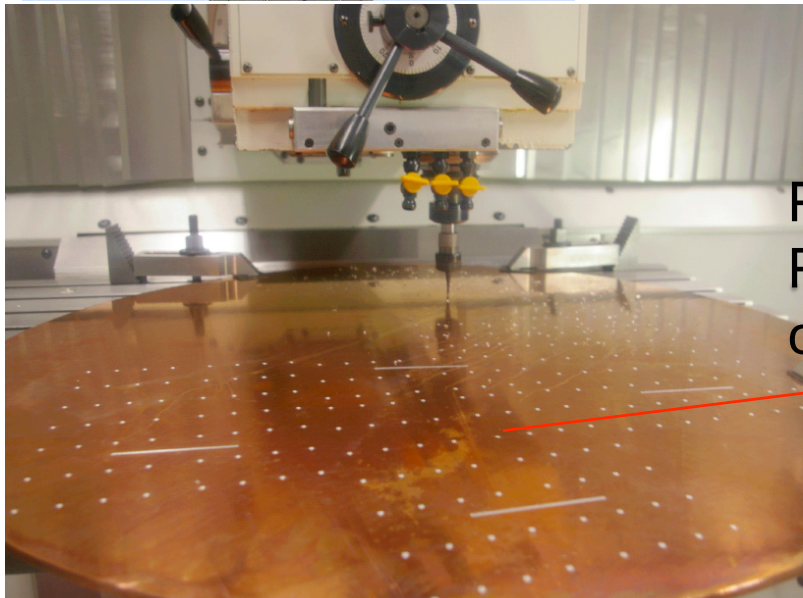
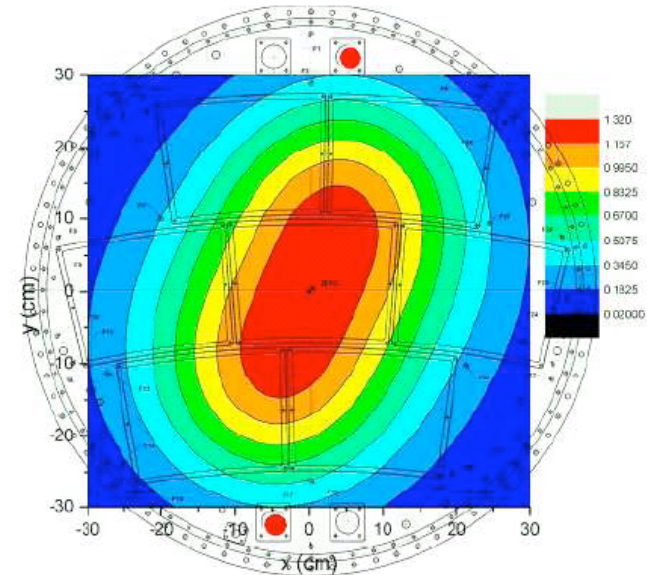
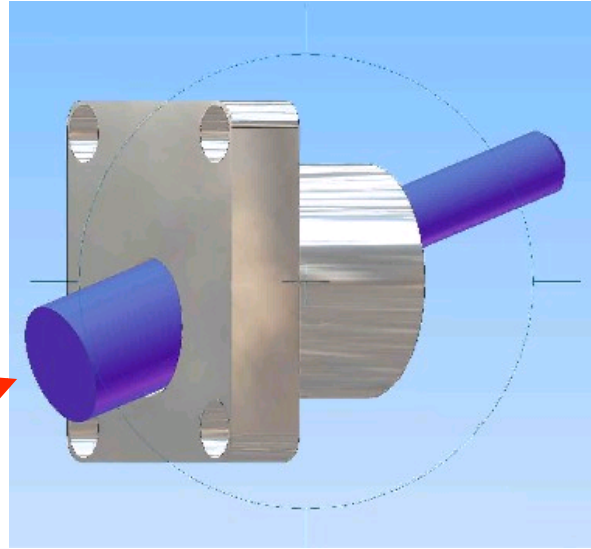
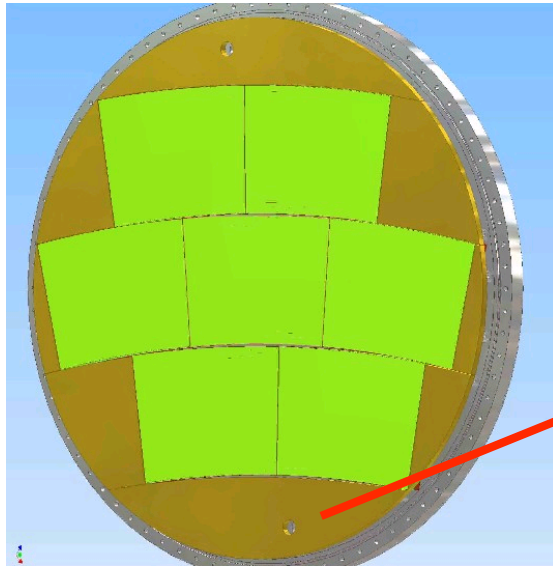
## 3-GEM + CMOS pixel readout



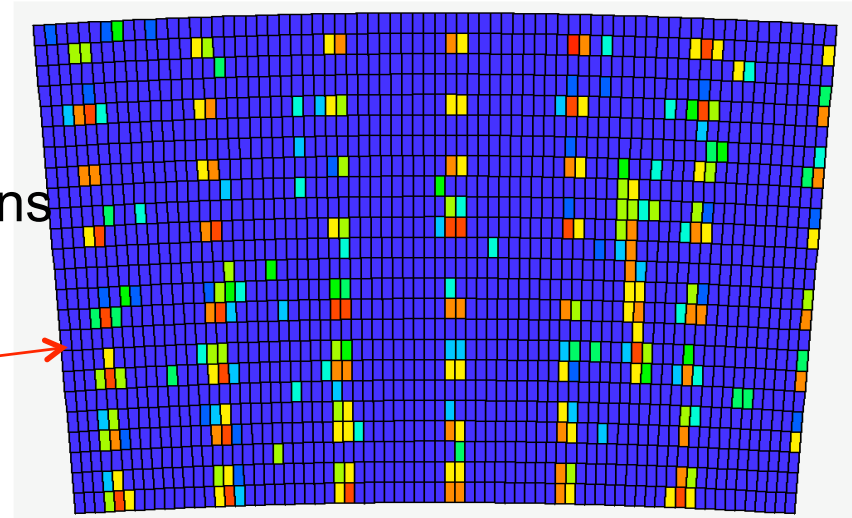
Readout: 2 quadboards  
4 TimePixChips each  
256x256ch each chips

Largest amount of readout channels  
on one anode for a TPC so far:  $\# \text{ ch} \approx 500 \text{ k}$





Photoelectrons  
Produced at  
cathode

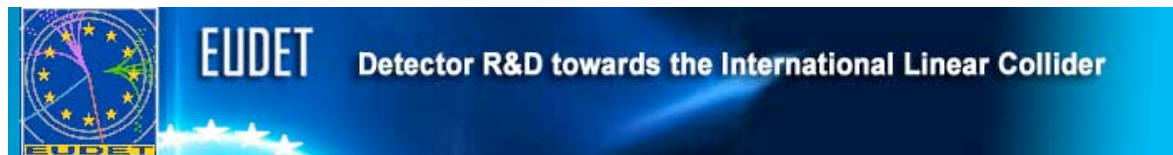


=> pattern seen with Micromegas



- A large prototype TPC has been built which allowed to address
  - **Assembling**
  - **Commissioning**
- LPTPC tested with several MPGD and electronics technologies
  - **MICROME GAS, GEM**
  - **AFTER, ALTRO, CMOS Pixel**
- Several beam tests have been performed with 5GeV  $e^-$  &  $B=1T$
- The analyses of all these data are ongoing and preliminary results look promising
- Future plans:
  - **Combined test with MICROME GAS and Si Envelope (Nov 2009)**
  - **Large scale test with 7 modules (~10 k channels) and more integrated electronics (spring 2010)**

# Acknowledgments

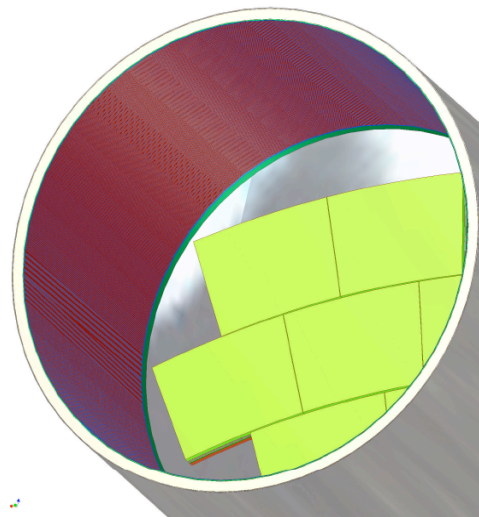
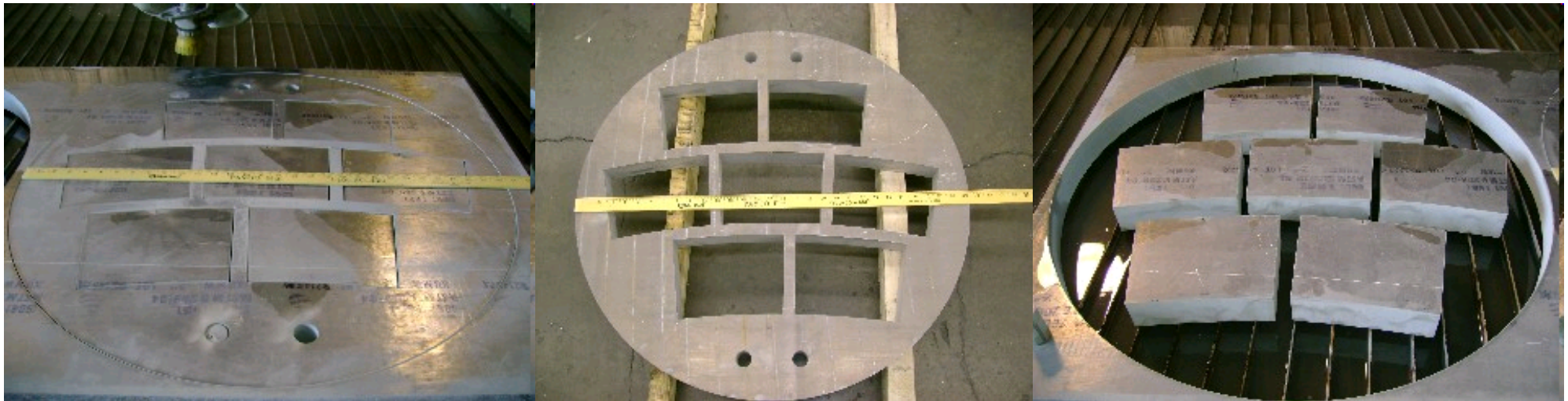




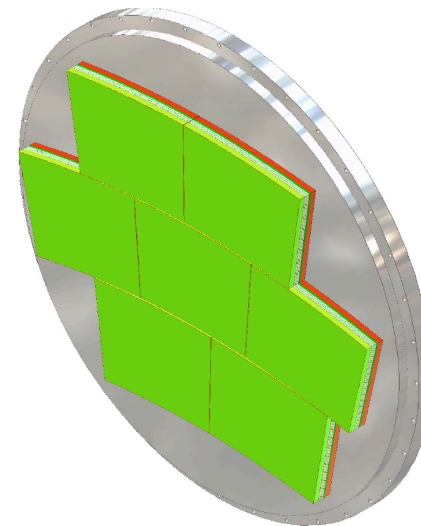


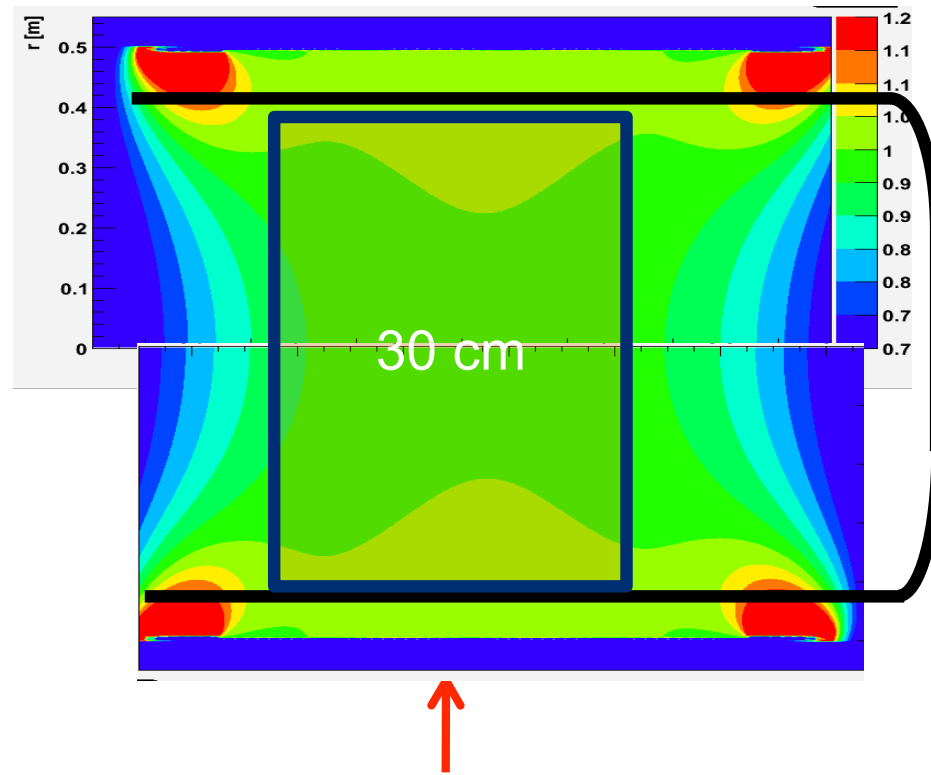
# Back-up slides

Size	$\phi = 3.6\text{m}, L = 4.3\text{m}$ outside dimensions
Momentum resolution (3.5T)	$\delta(1/p_t) \sim 9 \times 10^{-5}/\text{GeV}/c$ TPC only ( $\times 0.4$ if IP incl.)
Momentum resolution (3.5T)	$\delta(1/p_t) \sim 2 \times 10^{-5}/\text{GeV}/c$ (SET+TPC+SIT+VTX)
Solid angle coverage	Up to $\cos \theta \simeq 0.98$ (10 pad rows)
TPC material budget	$\sim 0.04X_0$ to outer fieldcage in $r$ $\sim 0.15X_0$ for readout endcaps in $z$
Number of pads/timebuckets	$\sim 1 \times 10^6/1000$ per endcap
Pad size/no.padrows	$\sim 1\text{mm} \times 4\text{--}6\text{mm}/\sim 200$ (standard readout)
$\sigma_{\text{point}}$ in $r\phi$	$< 100\mu\text{m}$ (average over $L_{\text{sensitive}}$ , modulo track $\phi$ angle)
$\sigma_{\text{point}}$ in $rz$	$\sim 0.5$ mm (modulo track $\theta$ angle)
2-hit resolution in $r\phi$	$\sim 2$ mm (modulo track angles)
2-hit resolution in $rz$	$\sim 6$ mm (modulo track angles)
dE/dx resolution	$\sim 5\%$
Performance	$> 97\%$ efficiency for TPC only ( $p_t > 1\text{GeV}/c$ ), and $> 99\%$ all tracking ( $p_t > 1\text{GeV}/c$ ) [82]
Background robustness	Full efficiency with 1% occupancy, simulated for example in Fig. 4.3-4(right)
Background safety factor	Chamber will be prepared for $10 \times$ worse backgrounds at the linear collider start-up

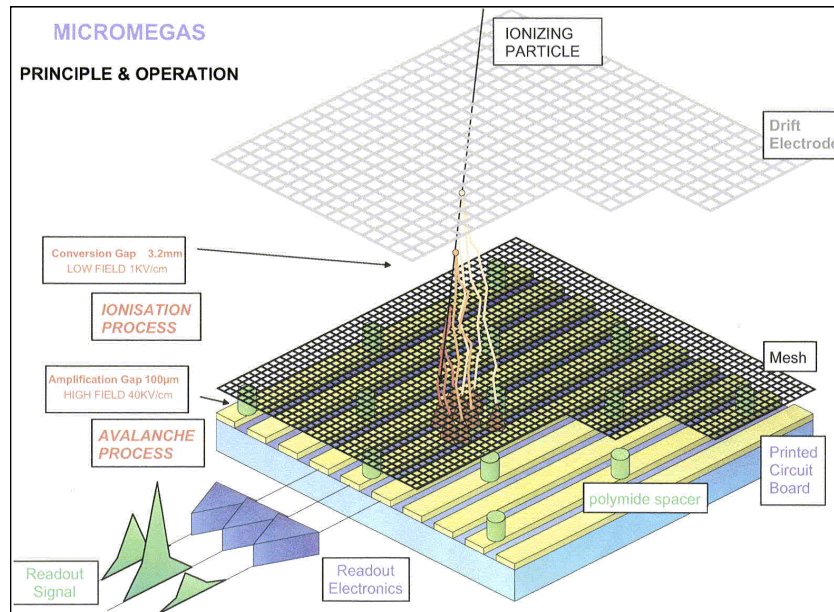


IEEE/NSS 2009

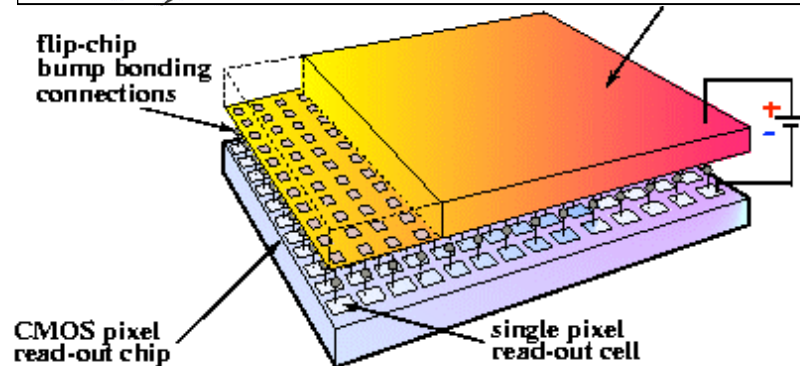








High field created by Gas Gain Grids  
Most popular: GEM and Micromegas

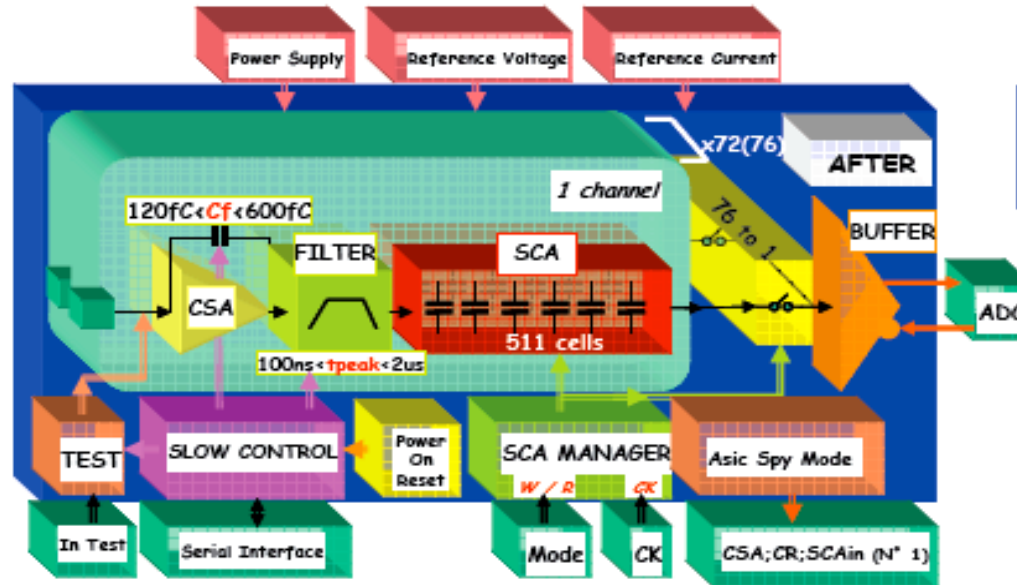


Use 'naked' CMOS pixel readout chip as anode

J. Timmermans  
NIKHEF

## AFTER Main Features

dapnia  
SECB  
cea  
saclay



- ⬇ No zero suppress.
- ⬇ No auto triggering.
- ⬇ No selective readout.

### Main features:

- **Input Current Polarity:** positive **or** negative
- **72** Analog Channels
- **4** Gains: 120fC, 240fC, 360fC & 600fC
- **16** Peaking Time values: (100ns to 2µs)
- **511 analog memory cells / Channel:**  
Fwrite: 1MHz-50MHz; Fread: 20MHz

- **Slow Control**
- **Power on reset**
- **Test mode:**  
calibration or test [channel/channel]  
functional [72 channels in one step]
- **Spy mode on channel 1:**  
CSA, CR or filter out

