(Very fast) SKIROC2 Overview

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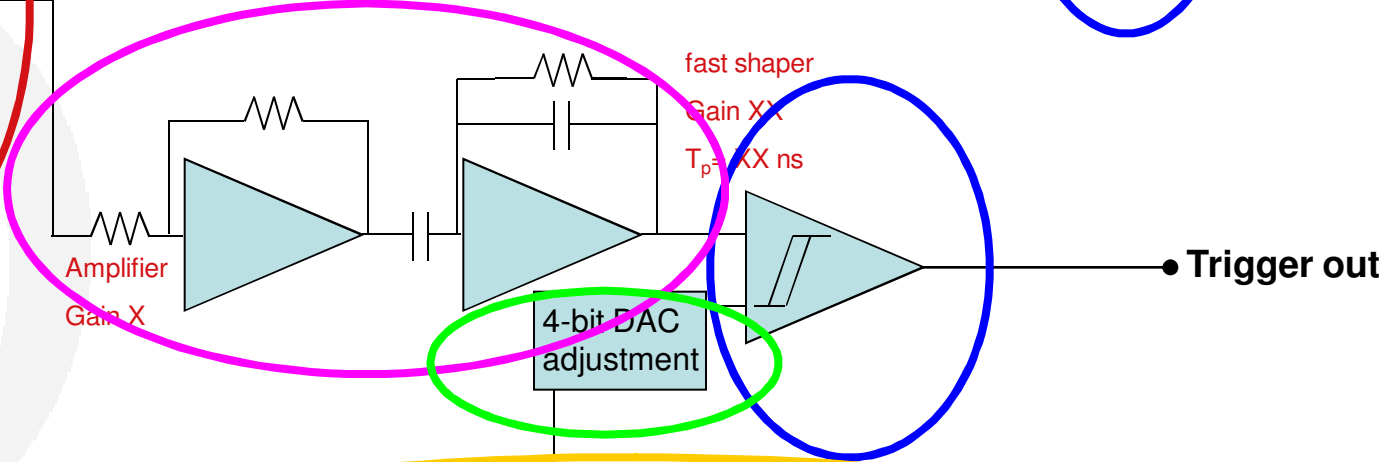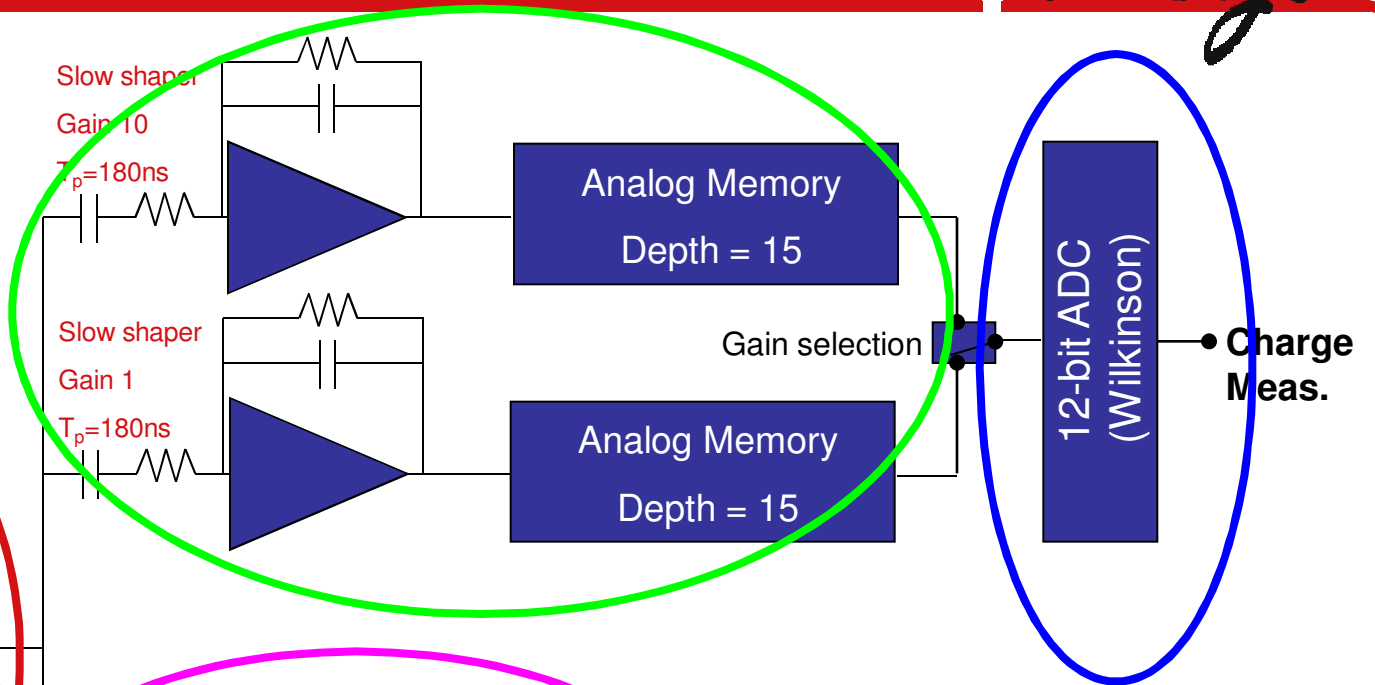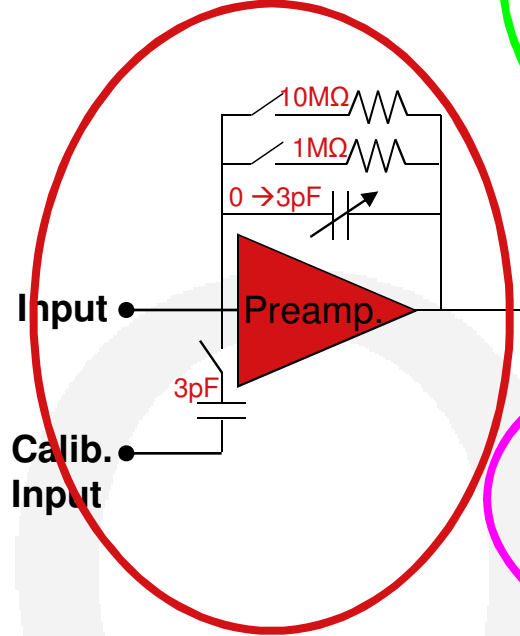
15 January, 2010

SKIROC2 One channel block scheme

- SPIROC
- SKIROC
- HARDROC
- PARISROC

Slow shaper
Gain 10
T_p=180ns

Analog Memory
Depth = 15

Slow shaper
Gain 1
T_p=180ns

Analog Memory
Depth = 15

Gain selection

12-bit ADC (Wilkinson)

Charge Meas.

10MΩ

1MΩ

0 →3pF

Input

Preamp.

3pF

Calib. Input

Amplifier
Gain X

fast shaper
Gain XX
T_p= XX ns

4-bit DAC adjustment

Trigger out

- Still under study …

10-bit dual DAC – common to 36 channels

SKIROC 2 specs

- 64 Channels

- Dynamic Range :
  - from ½ MIP up to 2500 MIP

- ADC Discriminator from Parisroc
  - better performance

- New Digital Part :
  - new layout (easier interconnections with analogue part)
  - minor modifications concerning some timings (allowing more latency to analogue signal)

Conclusion

- Engineering run, we should have 1250 sample

- Then, SPIROC2 will be integrated in FEV8 boards

- We still have lot of work to do …