

# TPC Parameters for ILD Integration/Optimization for the DBD Status 22 April 2010



# Optimization: software model

There will be some changes in 2010...

**Table 5**

from the Addendum

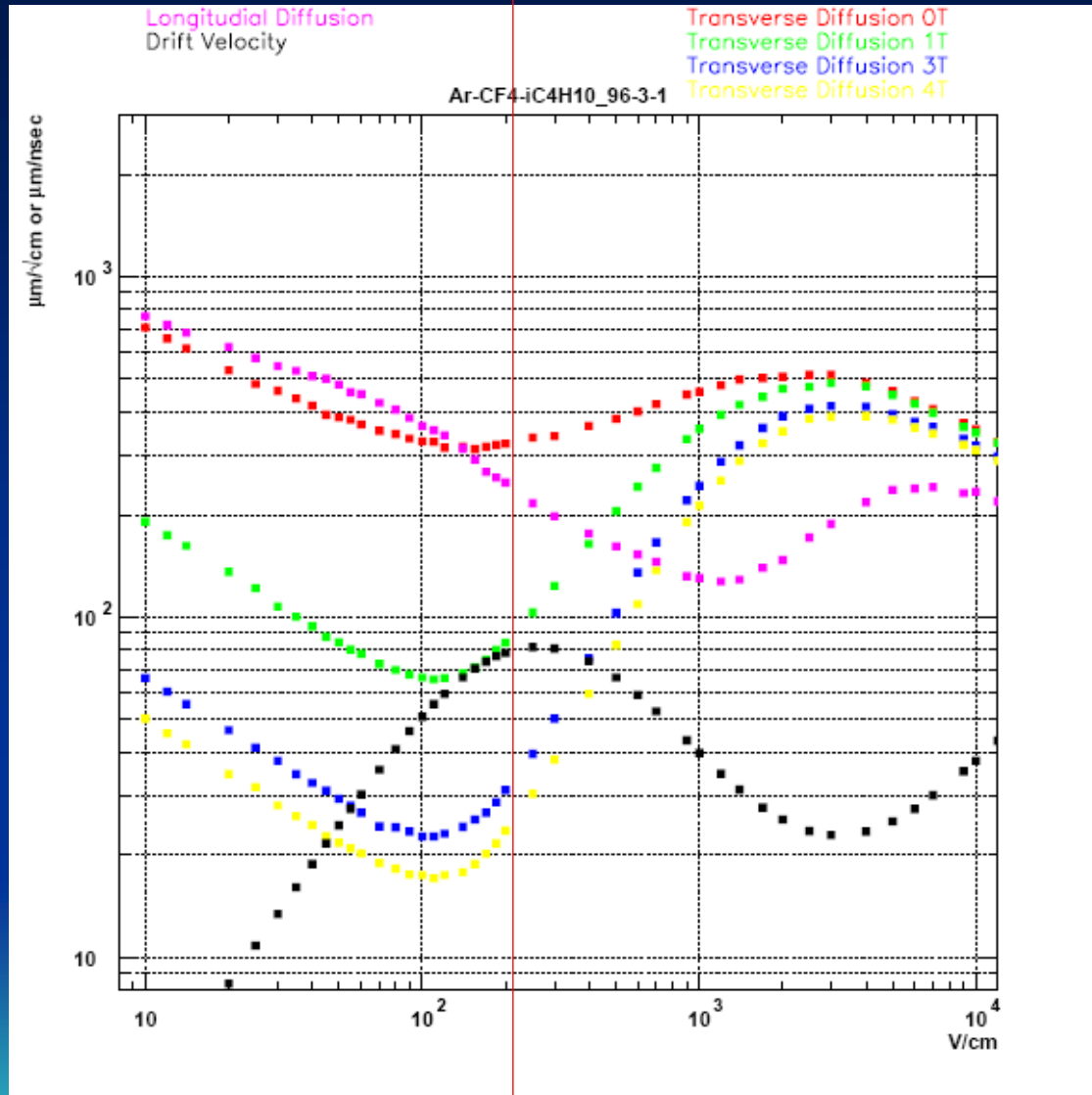
Performance/Design

Size	$\phi = 3.6\text{m}$ , $L = 4.3\text{m}$ outside dimensions
Momentum resolution (3.5T)	$\delta(1/p_t) \sim 9 \times 10^{-5} / \text{GeV}/c$ TPC only ( $\times 0.4$ if IP incl.)
Momentum resolution (3.5T)	$\delta(1/p_t) \sim 2 \times 10^{-5} / \text{GeV}/c$ (SET+TPC+SIT+VTX)
Solid angle coverage	Up to $\cos\theta \simeq 0.98$ (10 pad rows)
TPC material budget	$\sim 0.04X_0$ to outer fieldcage in $r$ $\sim 0.15X_0$ for readout endcaps in $z$
Number of pads/timebuckets	$\sim 1 \times 10^6 / 1000$ per endcap
Pad size/no.padrows	$\sim 1\text{mm} \times 4\text{--}6\text{mm} / \sim 200$ (standard readout)
$\sigma_{\text{point}}$ in $r\phi$	$< 100\mu\text{m}$ (average over $L_{\text{sensitive}}$ , modulo track $\phi$ angle)
$\sigma_{\text{point}}$ in $rz$	$\sim 0.5\text{ mm}$ (modulo track $\theta$ angle)
2-hit resolution in $r\phi$	$\sim 2\text{ mm}$ (modulo track angles)
2-hit resolution in $rz$	$\sim 6\text{ mm}$ (modulo track angles)
dE/dx resolution	$\sim 5\%$
Performance	$> 97\%$ efficiency for TPC only ( $p_t > 1\text{GeV}/c$ ), and $> 99\%$ all tracking ( $p_t > 1\text{GeV}/c$ )
Background robustness	Full efficiency with 1% occupancy
Background safety factor	Chamber will be prepared for $10 \times$ worse backgrounds at the linear collider start-up

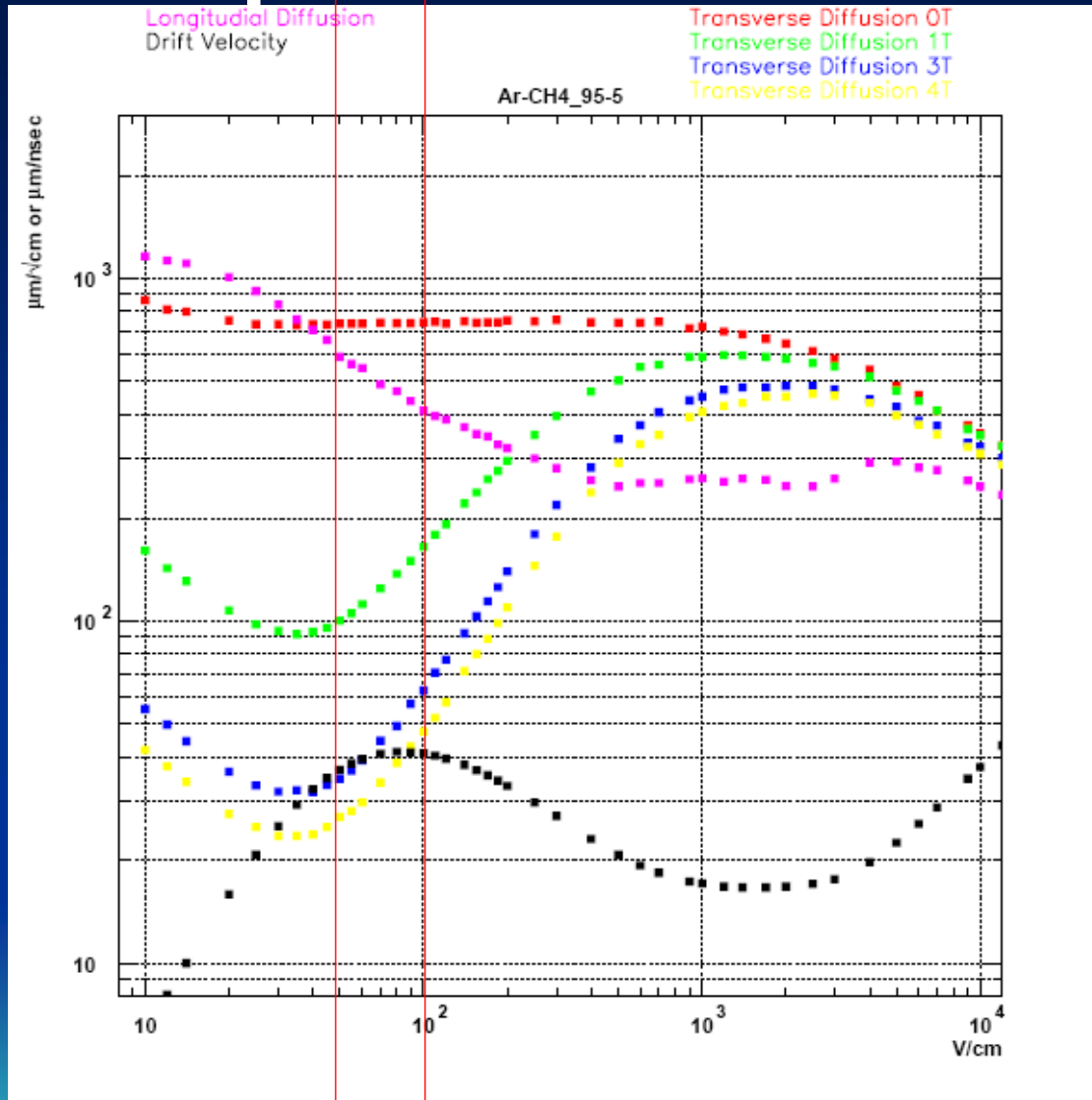
# « Gas »

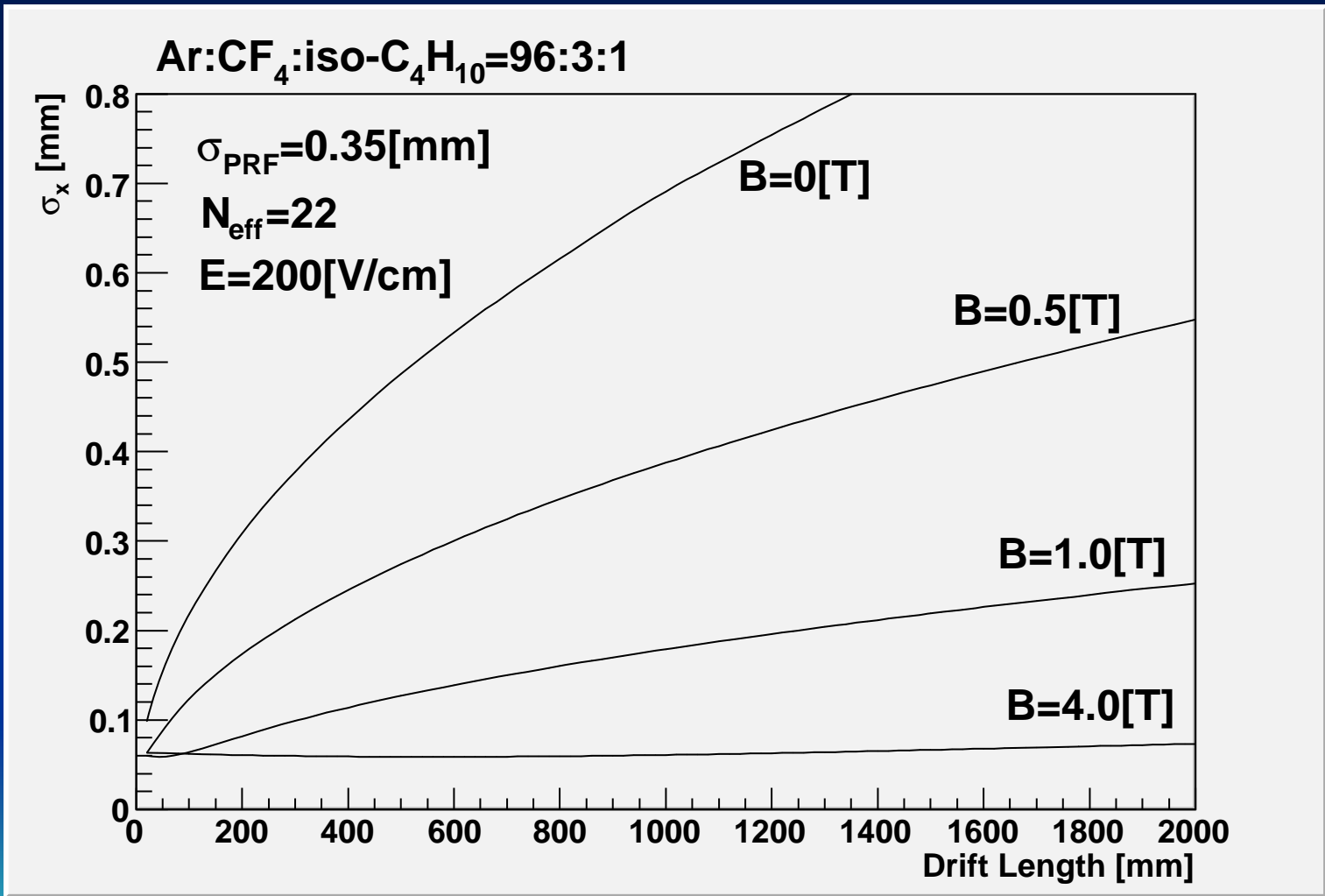
Fundamental decision: we should keep options open for at least two alternative gases, a 'preferred' gas and a 'back-up' gas...

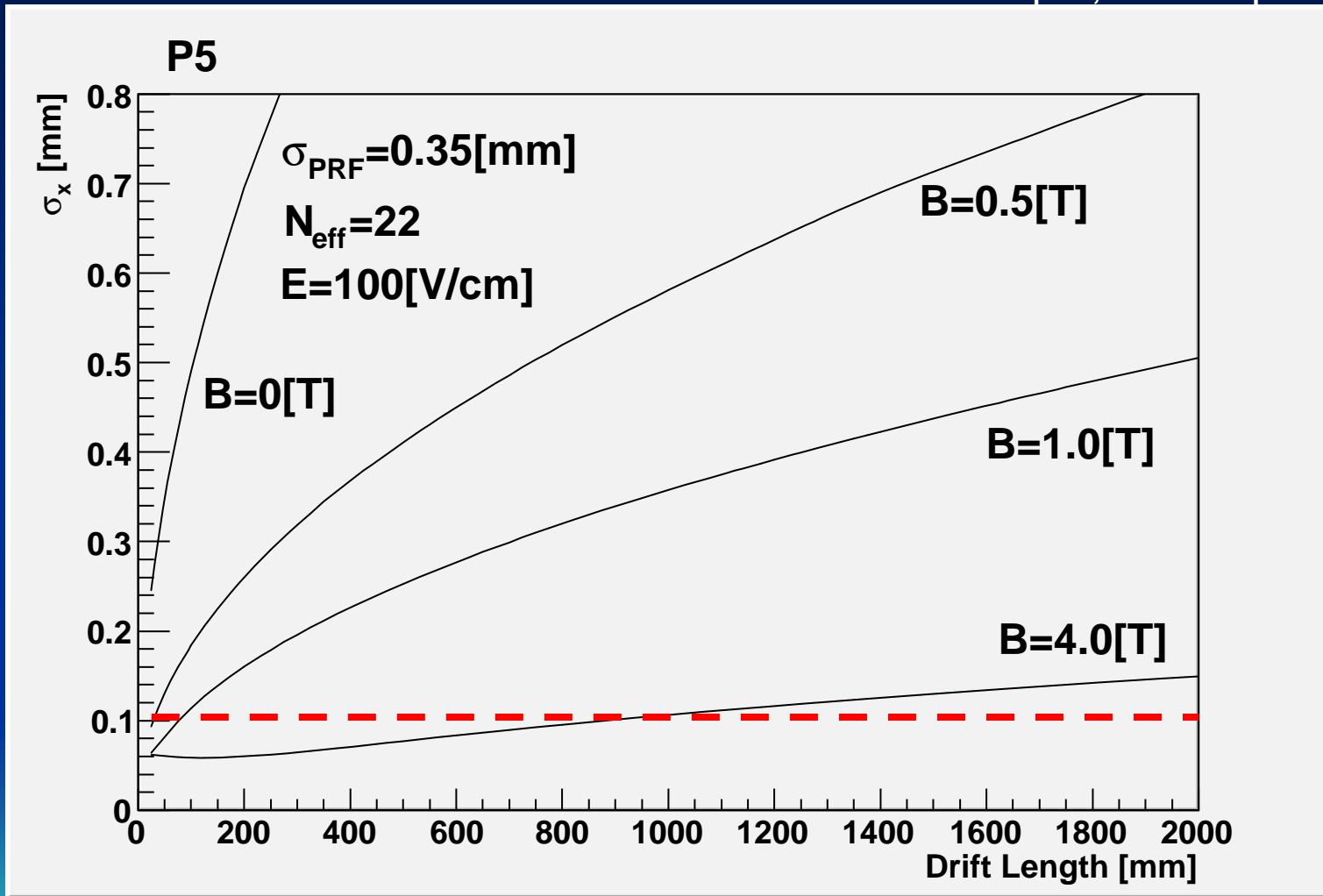
# Ar-CF4-iC4H10 (96-3-1%) 'preferred'



# P5 'back up'







# Performance

- **Momentum precision for the TPC**  
→ **What is the best we can do?**

Neff=22

w=1.27 [mm]

sigma\_PRF = 350 [microns]

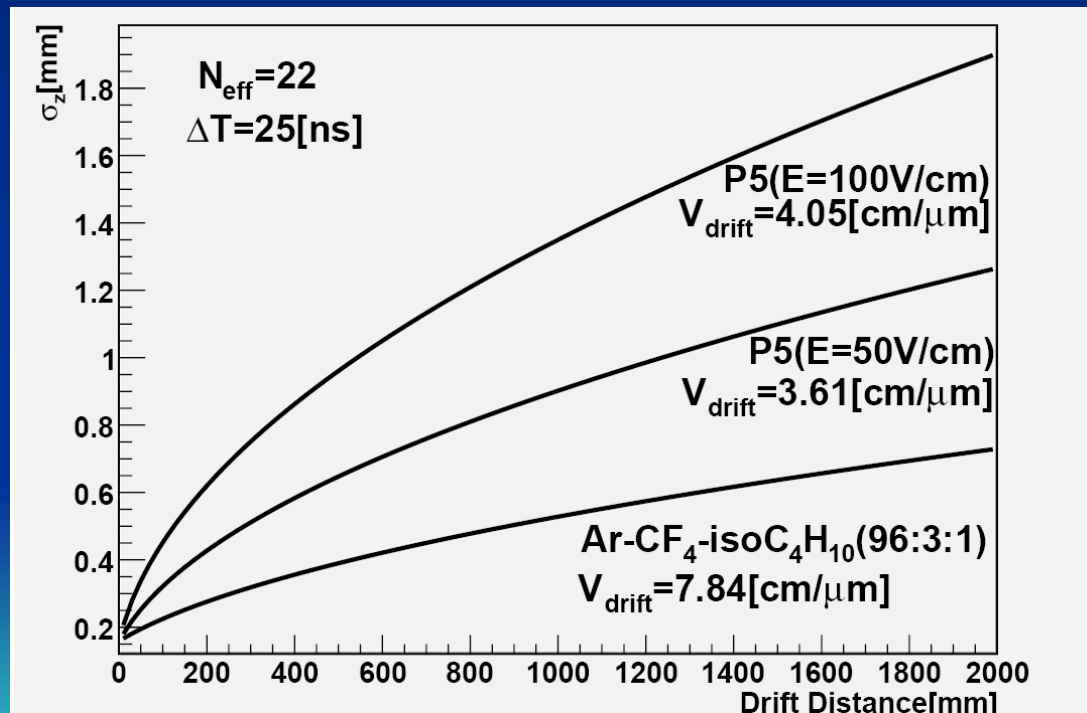
Gas	E [V/cm]	Cd [microns/sqrt(cm)]	sigma_0 [microns]
P5	50	25	25
	100	49	23
Ar-CF4-iso (96:3:1)	200	23	25



# Point Resolution $\sigma_z$ (z)

R. Yonamine

$$\sigma_z = \sqrt{\frac{1}{N_{eff}} \left( \frac{(\Delta T \cdot v_{drift})^2}{12} + (C_D^L)^2 \cdot z \right)}$$



## Part 1 of 2008 eMail to ild-optimization maillist:

There will be some changes in 2010...

1) As a function of drift-distance  $L_{\text{drift}}$ , the expression for the  $r_{\text{phi}}$  point resolution is, as you know,

$$\sigma_{\text{point}}^2 = \sigma_0^2 + Cd^2/N_{\text{eff}} * L_{\text{drift}}$$

Proposal 1) on point resolution:

$$\Rightarrow \sigma_0^2 = (50\text{micron})^2 + (900\text{micron} * \sin(\phi))^2$$

(where  $\phi$  is the local azimuthal angle of track wrt the padrow)

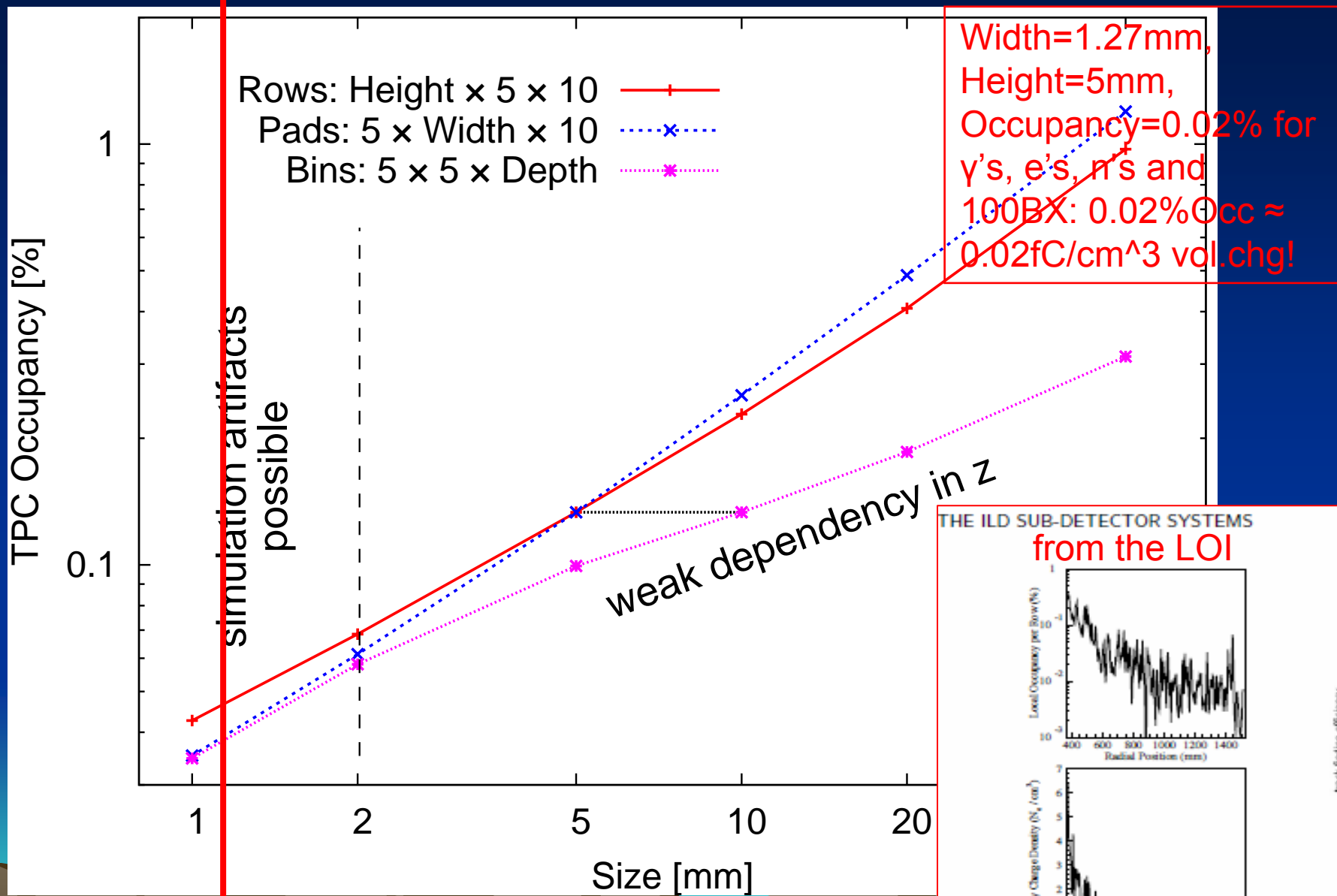
$$\Rightarrow Cd^2/N_{\text{eff}} = 25^2 / (22 / \sin(\theta) * h / 6\text{mm}) = (5.3\text{micron} / \sqrt{\text{cm}})^2 * (6\text{mm} / h) * \sin(\theta)$$

(this is for  $B=4\text{T}$  which we favor,  $h$  is the pad height=pad-row pitch in mm,  $\theta$  is the polar angle)

$$\Rightarrow \sigma_z(z) = \sqrt{400\text{micron}^2 + z(\text{cm}) * (80\text{micron} / \sqrt{\text{cm}})^2}$$

# Integration: hardware model iteration #1

# Adrian's simulation: bottom line $\Rightarrow$ want small pads



THE ILD SUB-DETECTOR SYSTEMS  
from the LOI

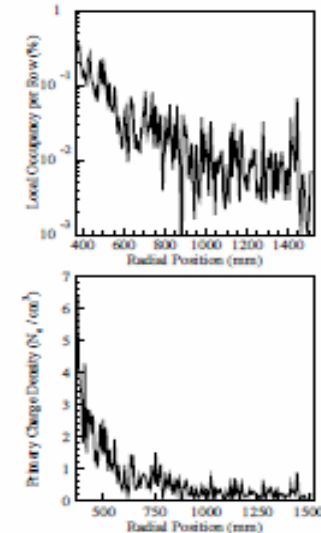


FIGURE 4.3-4. Occupancy for  $xyz = 1 \times 5 \times 5$

# « MDI parameters »

Request from MDI/Integration group (Catherine Clerc) to the TPC contact persons (Takeshi and me). We propose, not as Catherine started, → to define one "generic" TPC (not two) for MPGD (neither  $\mu$ gas nor gem specific)...



[TPC interface parameters]

Ref	ILD-000-xxxx
Issue	
Date	20/01/2010
Page	2

## 1. Technological description

Each endplate  $\approx 10 \text{ m}^2$

Pads :

- ✓  $\mu$ egas  $7 \times 3 \text{ mm}^2$  i.e.  $0.55 \text{ Mch/endplate}$
- ✓ Gems :  $1 \times 5 \text{ mm}^2$  i.e.  $2.3 \text{ Mch/endplate}$

## 2. Overall dimensions

400 KG/endplate,  $\approx 2 \text{ t}$  full TPC

## 3. Support

3 tie rods from each endplate face to HCal barrel

## 4. Services

Cabling (  $\mu$ egas)

- 80 modules each side.
- For each module (6800 channels) :
- 1 HV cable
- 1 double optical fibre
- 1 low-voltage 32A cable

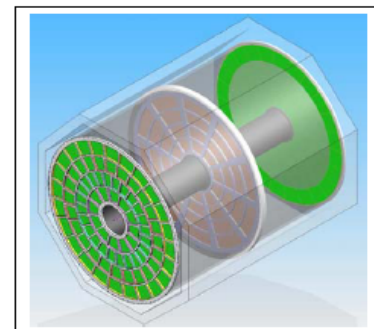
Each side : 80 HV+80 Double Fibres+80 LV(32A) = 240 cables

Cooling :

160 W to remove (becomes negligible is power pulsing can be fully implemented.)

But to be checked

With power pulsing  $0.5 \text{ mW}$  per channel



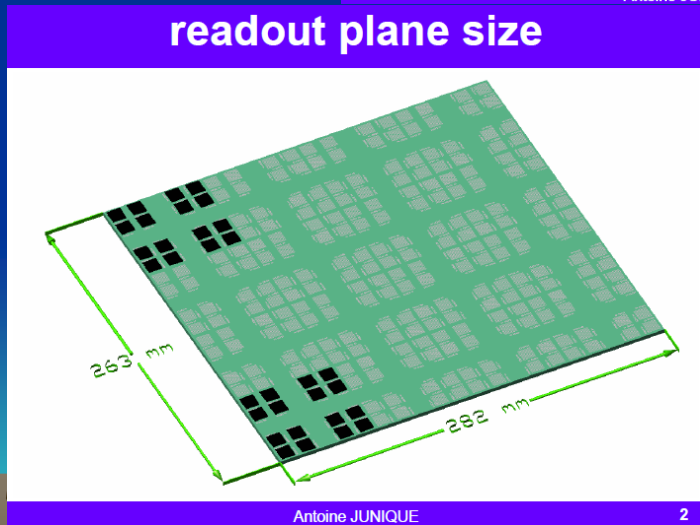
Electronics: since both micromegas and gem have agreed to use S-Altro

--Basic unit: Antoine Junique's "new readout plane", labeled in what follows here "ROB" (this is the basis for Takahiro's talk on the test set-up at the March 4 WP meeting):



Antoine JUNIQUE

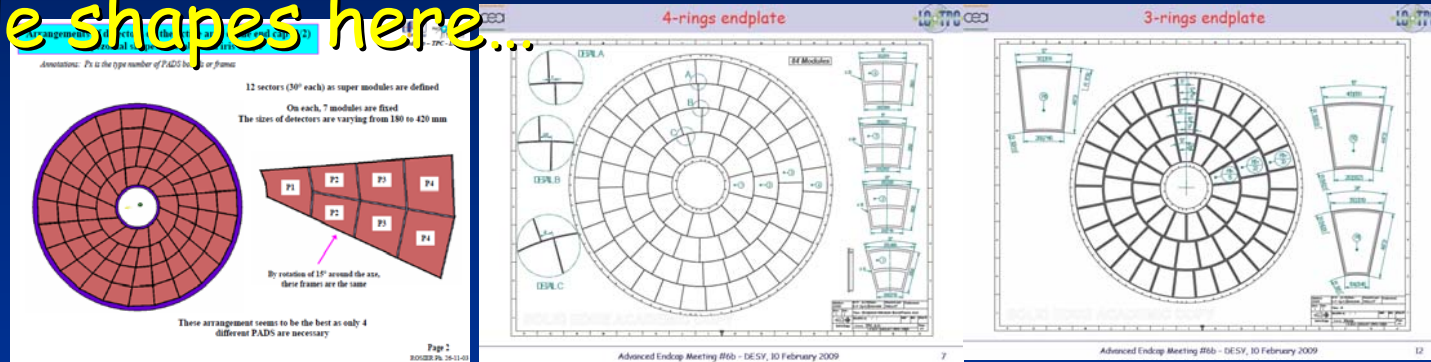
--ROB size:  
 $263 \times 282 \text{mm}^2 \approx$   
2 x LP1 "ROB"



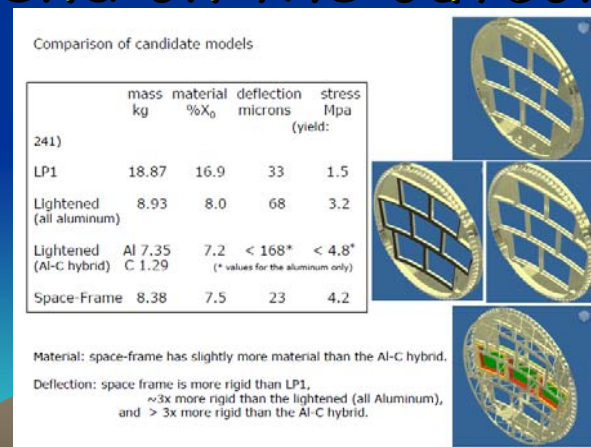
Antoine JUNIQUE

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Exercise: let's see what the endcap thickness with this S-Altro will be (ie, as thin as possible but with as small of pads as possible: 1 mm x 4 mm in this case). "Generic" also means don't worry about sector/module shapes here...



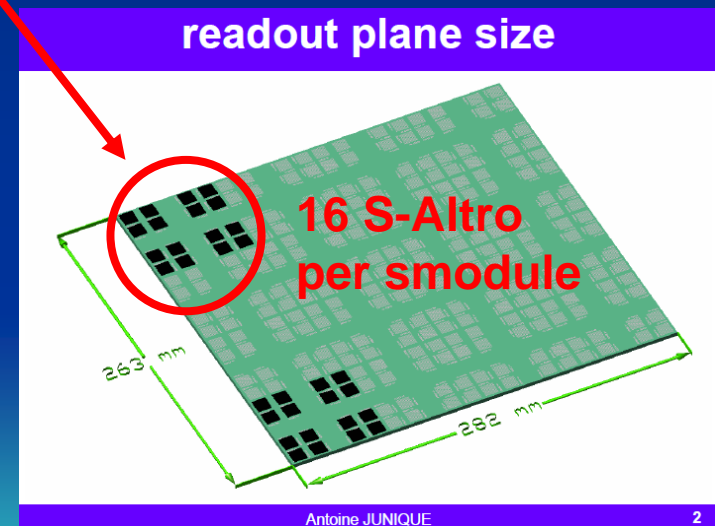
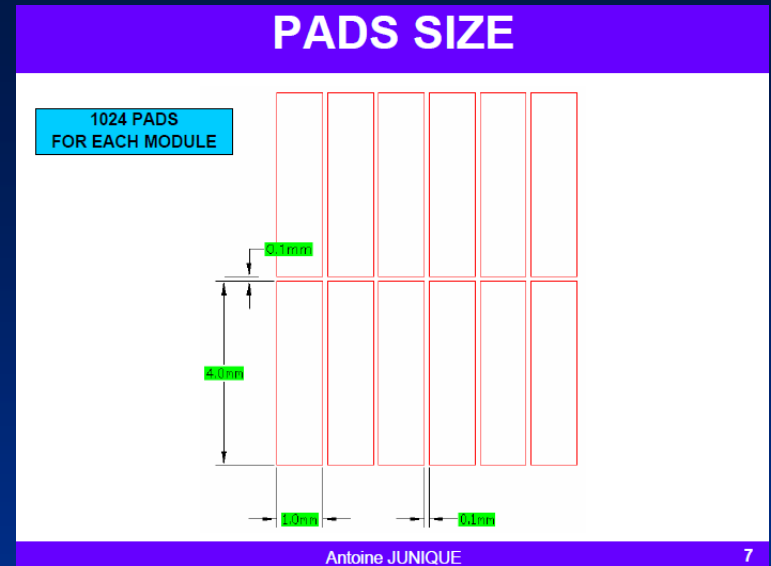
...because these will depend on the outcome of Dan's studies...





# Sizes

- pad pitch:  $4.1 \times 1.1\text{mm}^2$
- 1024 pads/smodule
- $\Rightarrow 4618 \text{ mm}^2/\text{smodule}$
- 16384 pads/ROB
- $\Rightarrow 73892 \text{ mm}^2/\text{ROB}$
- R\_endcap  $\sim 329\text{mm}$  to  $1818\text{mm}$
- $\Rightarrow 10049290 \text{ mm}^2/\text{endcap}$
- $\Rightarrow 136 \text{ ROB}/\text{endcap}$



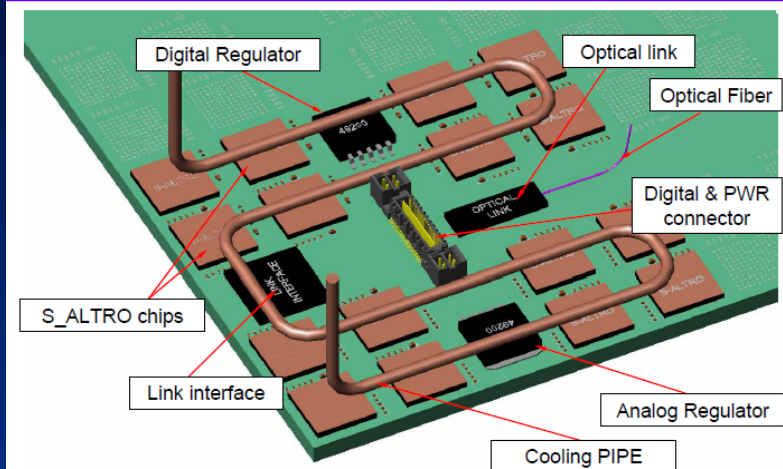


# # Channels

- 64 pads/S-Altro
- 16 S-Altro/smodule
- 16 smodules/ROB

- ⇒
- 1024 pads/smodule
  - 16384 pads/ROB
  - 34816 S-Altro/endcap
  - 2226897 pads/endcap

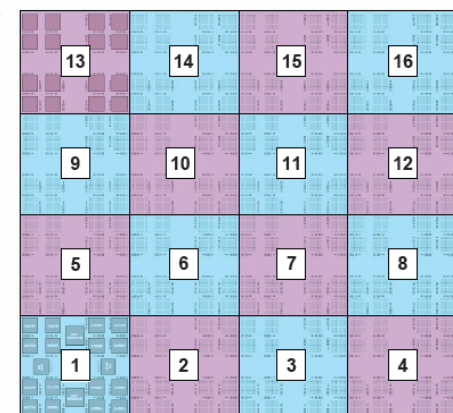
## MODULE DETAILS



Antoine JUNIQUE

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## PCB DISTRIBUTION



The readout plane is  
Divided in 16 Modules

Antoine JUNIQUE

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## # sections: "8 ways-out"

Catherine is proposing an "8-fold way" (which agrees with the Ecal segmentation) for extracting cables, cooling pipes etc, it seems logical to subdivide the TPC endcap into 8 sections:

--136 ROB/endcap  $\Rightarrow$  17 ROB/section

Remember, we don't worry about ROB or section shapes for this exercise (the shapes will vary going from the inside to the outside and are details we can work out later, as soon all the information is available). Here we want to derive the endcap thickness, heatload, etc using Antoine's "new readout plane" for the detector integration...

# Cooling

Tests of power delivery,  
power switching and cooling  
for S-ALTRO End-Plate

22/Sep/2009  
LCTPC Collaboration Meeting  
Takahiro Fusayasu  
Nagasaki Institute of Applied Science

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## 1. Cooling

### Considerations on power dissipation

#### Power consumption

- amplifier 8 mW / channel
- ADC 12mW/channel (10MSPS), 34 mW / channel (40MSPS)
- Digital Proc 2 mW / channel
- data links 2 mW / channel
- power regulation efficiency: 75%
- Total power 32mW/channel (10MSPS), 60mW/channel (40MSPS)

Owing to the bunch-train time structure (**beam duty cycle 0.5%**), for the ILC and CLIC TPCs electronics a basic ingredient is power pulsing

In principle a factor  $\sim 70$  ( $\sim 1.5\%$  **electronics duty cycle**) can be achieved

What can be achieved in practice is an important R&D issue

- duty cycle 1.5%
- average power / channel 0.5 mW / channel (at 10 MSPS)
- average power / m<sup>2</sup> 100 W (at 10MSPS)

**Power pulsing cannot be applied to PANDA.**

15 May 2009

Luciano Musa / CERN

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With 2226897 pads/endcap and 0.5mW/pad with power pulsing (1.5% duty cycle)

⇒ 1.1 kW/endcap = 111 W/m<sup>2</sup> at 10 MSPS = 8 W/ROB

or 2 kW/endcap = 200 W/m<sup>2</sup> at 40 MSPS = 15 W/ROB

**N.B. This is NOT negligible, even "if power pulsing can be fully implemented"!**

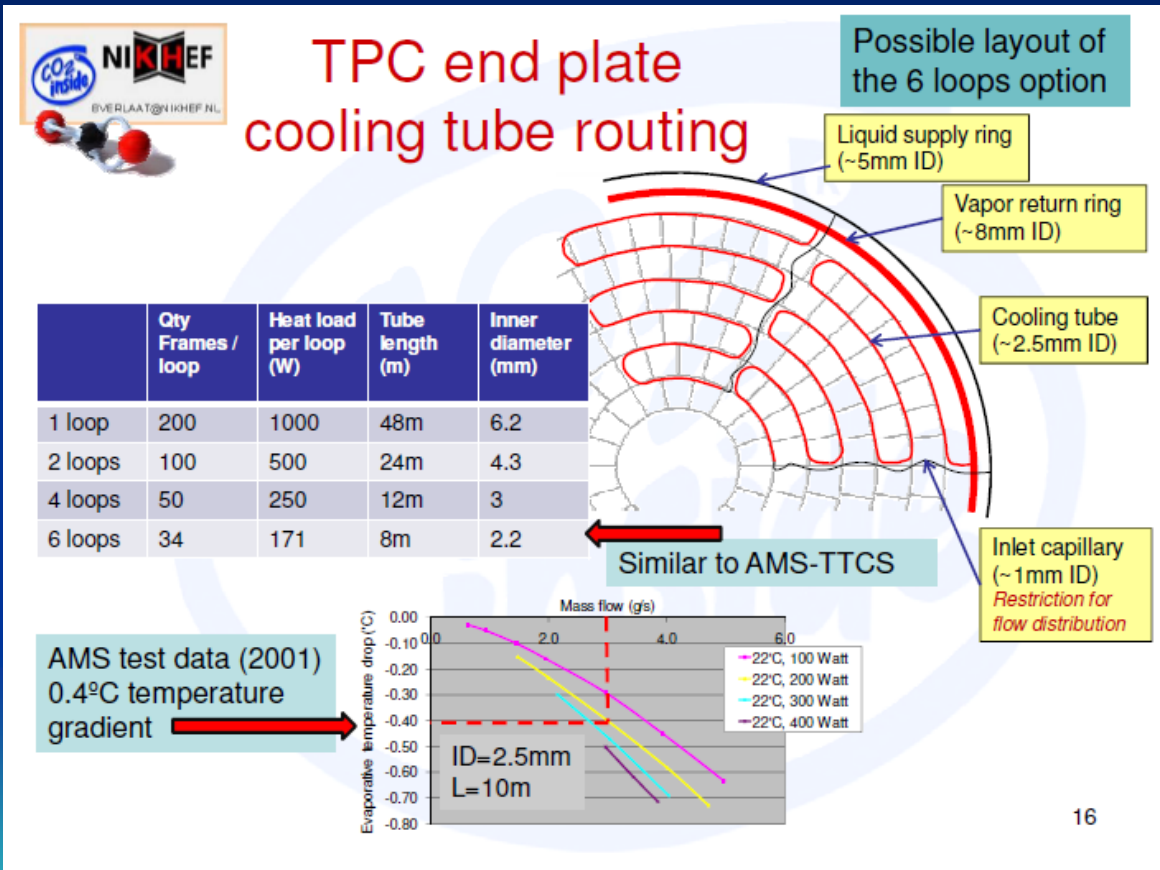
22/04/2010

Rolf Cottler MPI-Munich  
LCTPC integration model

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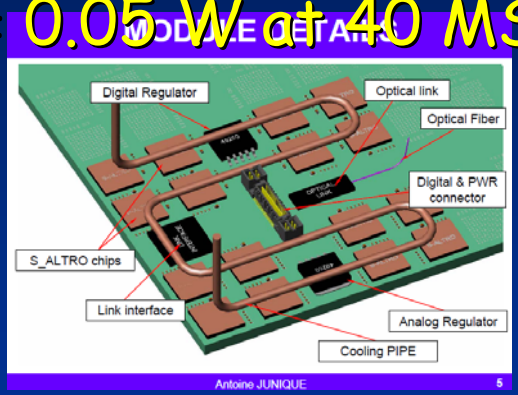
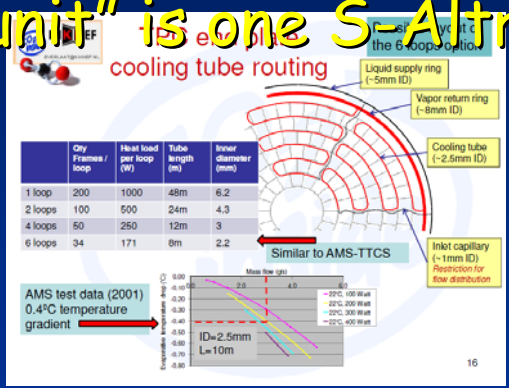
# Cooling

"8 ways-out" means that Bart should look into an "8-loop option" with a pad TPC...

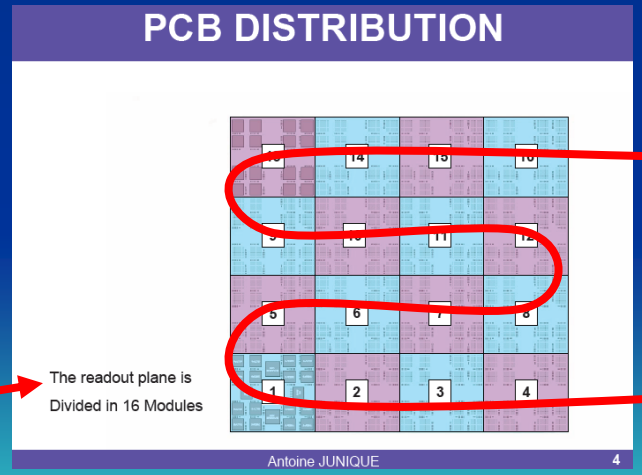


# Cooling

Also, these two strategies are different: Bart cools a timepix chip. This corresponds roughly to one ROB in a pad TPC (a "cooling unit") = 11 W at 40MSPS, where Antoine's "cooling unit" is one S-Altro = 0.05 W at 40 MSPS.



At Aleph, our "cooling unit" was ~ 1 W, roughly equivalent for this lctpc case 1 smodule = 16 S-Altros = 0.7 W at 40 MSPS or = 0.4 W at 10 MSPS. Should Bart use this?





# Cables

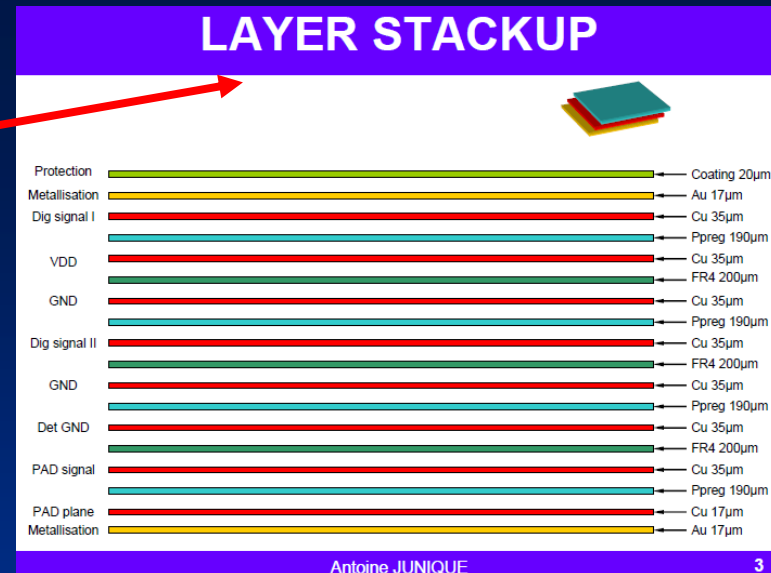
- Cable for fieldcage: one 70 kV cable,  $\phi \sim 15$  mm (Catherine's estimate is o.k.)
- Cables for MPGD, gating, clock, readout optical fibre, etc :  $\sim 10/\text{ROB}$  (depends on layout)  $\Rightarrow$  material small.
- Power cables  $\Rightarrow$  material large. This is important because the material is large and is very sensitive to the cable **layout** scheme, and we have not had enough time to work this out. We must try to do this within the next few weeks.

X<sub>0</sub> Thicknesses  
 Sum of these plus S-Altros  
 ~ 5 % X<sub>0</sub>

Dan estimated at last meeting  
 the space-frame thickness  
 ~ 8 % X<sub>0</sub> for the LP size. We  
 don't know yet how this  
 translates to the LCTPC size.

Cooling (my guess, needs  
 confirmation)  
 ~ 2% X<sub>0</sub>

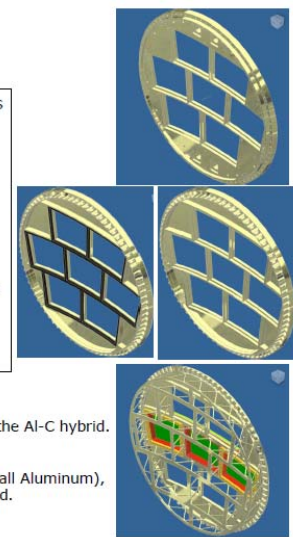
Cable layout --- work in  
 progress --- but it looks like  
 the above X<sub>0</sub> may be doubled.



Comparison of candidate models

	mass kg	material %X <sub>0</sub>	deflection microns	stress Mpa (yield:
241)				
LP1	18.87	16.9	33	1.5
Lightened (all aluminum)	8.93	8.0	68	3.2
Lightened (Al-C hybrid)	7.35 C 1.50	7.2	< 168*	< 4.8*
Space-Frame	8.38	7.5	23	4.2

(\* values for the aluminum only)



Material: space-frame has slightly more material than the Al-C hybrid.

Deflection: space frame is more rigid than LP1,  
 ~3x more rigid than the lightened (all Aluminum),  
 and > 3x more rigid than the Al-C hybrid.

# LCTPC milestones

- 2006-2012 Continue LCTPC R&D via small-prototypes and LP tests
- 2013 Decide on all parameters
- 2014 Final design of the LCTPC
- 2018 Four years construction
- 2019-20 Commission/Install TPC in the ILC Detector