

KLauS2v0

Uniformity and Noise Measurements

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Reminder

Input DAC
Uniformity

Charge
Conversion

Noise

Conclusion

Content:

- Reminder: KLauS Version 2.0
 - Design Features
 - Channel Description
- Uniformity of DAC Linearity
- Charge Conversion Factors
- Noise measurements
- Conclusion

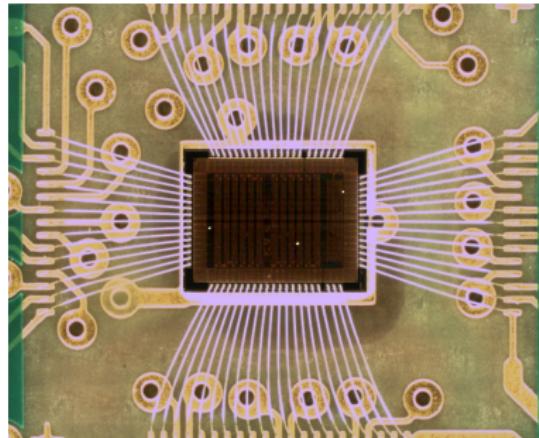
Reminder

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Reminder: KLauS2v0



- 12 channels
- Support for Power Gating
- Low Power Consumption
- High Dynamic Range
- Signal to Noise Ratio > 10
- SiPM Bias Tunable
- SiGe $0.35\ \mu\text{m}$ Technology
- Implementation into SPIROC planned

KLauS2v0

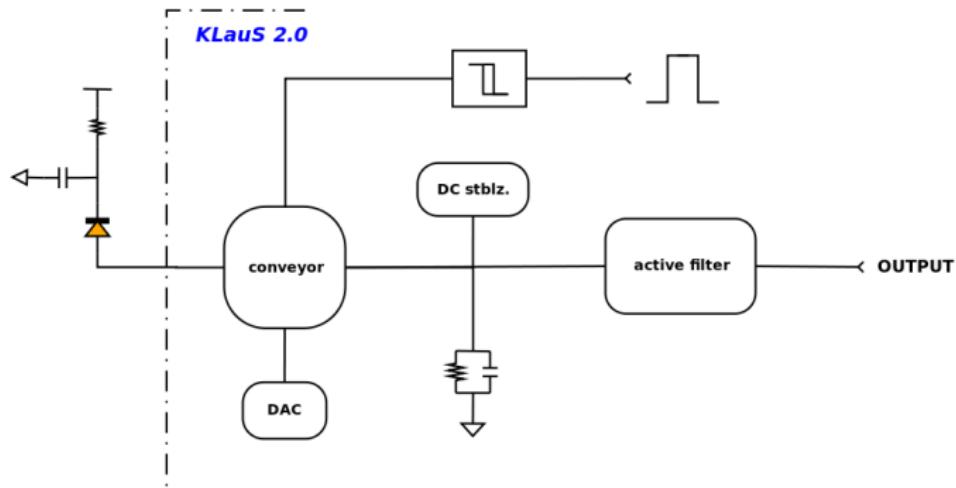
Channel Diagram of KLauS2v0

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Analog chain: passive integration + DC stabilization + active filter
Modified current conveyor supports power gating

Conducted Measurements

- Channel Uniformity → DAC Linearities
- Detector Capacitance → Charge Conversion Factors
→ Noise Measurements for different Detector Capacitances

An automated characterization is being developed to perform the needed measurements.

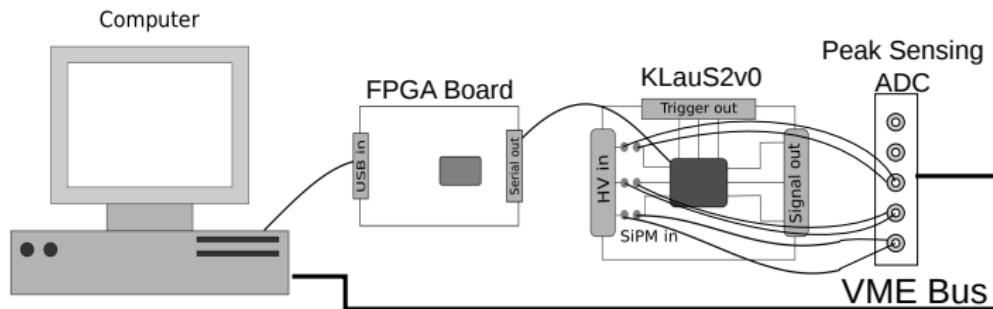
Input DAC Uniformity Setup

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The voltage at the input terminals is measured for every DAC configuration.

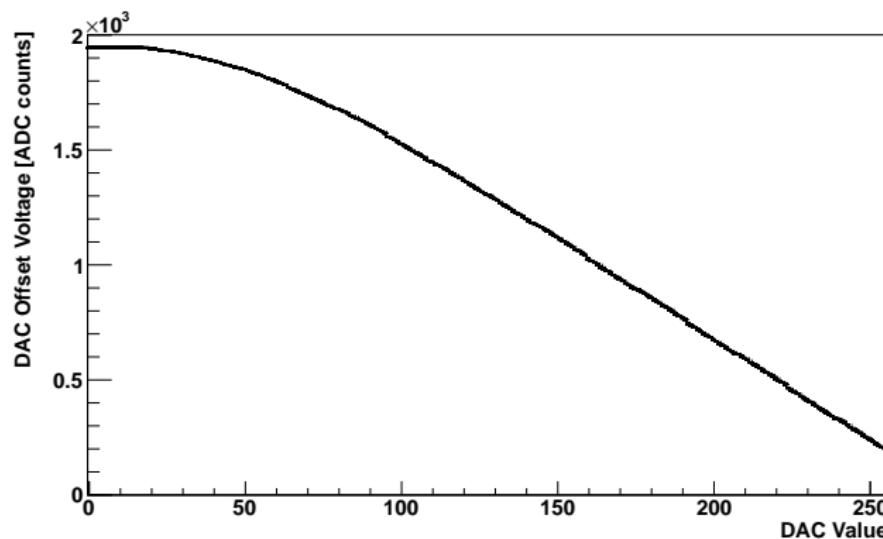
Raw Data

Reminder

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Channel 0 DAC Measurement

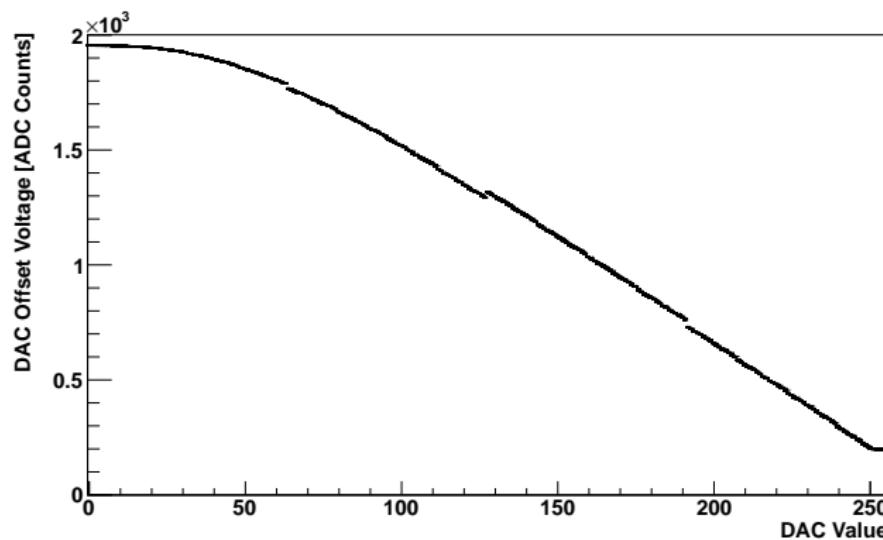
Raw Data

Reminder

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Channel 7 DAC Measurement

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Conducted Measurements

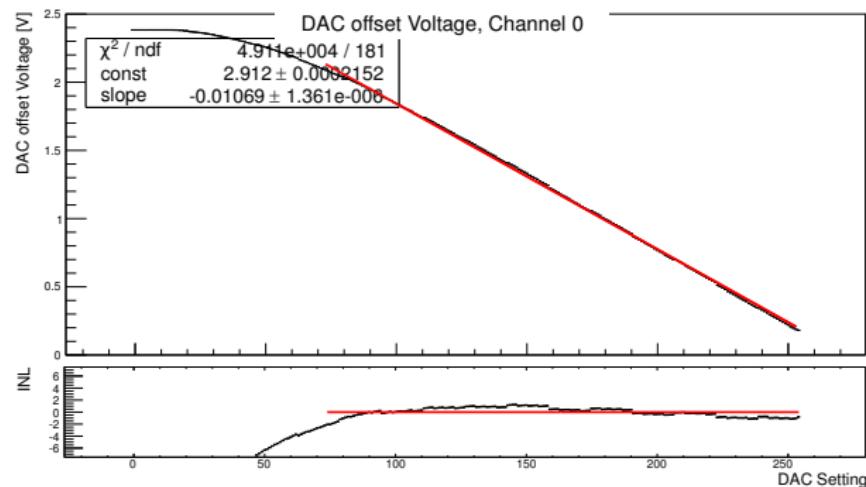
Reminder

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- DAC Resolution from the slope of the fit
- Linear Range at INL < 2.5 %

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Uniformity of the DAC Linearity

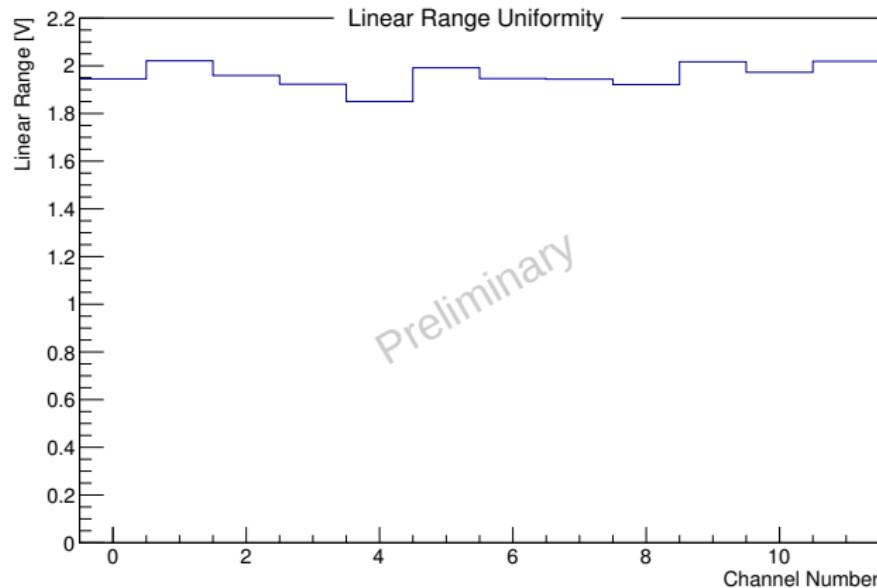
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Channel to channel variations < 5 %

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Uniformity of the DAC Resolution

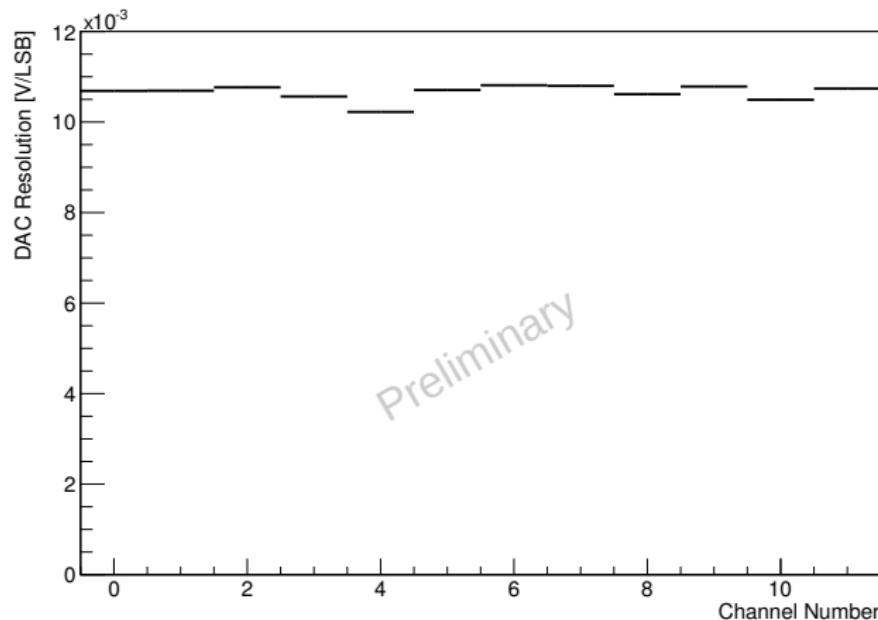
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Channel to channel variations < 5 %

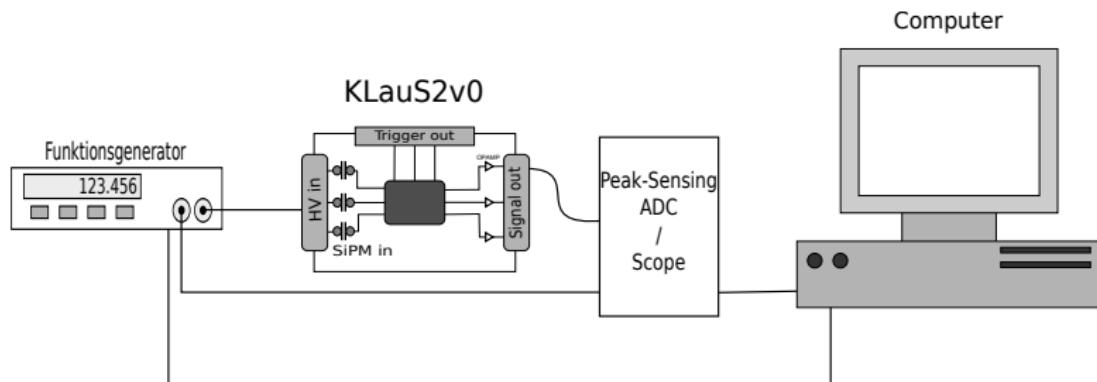
Charge Conversion Setup

Reminder

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Conclusion



The signal amplitude is measured for defined charge signals injected by different capacitances.

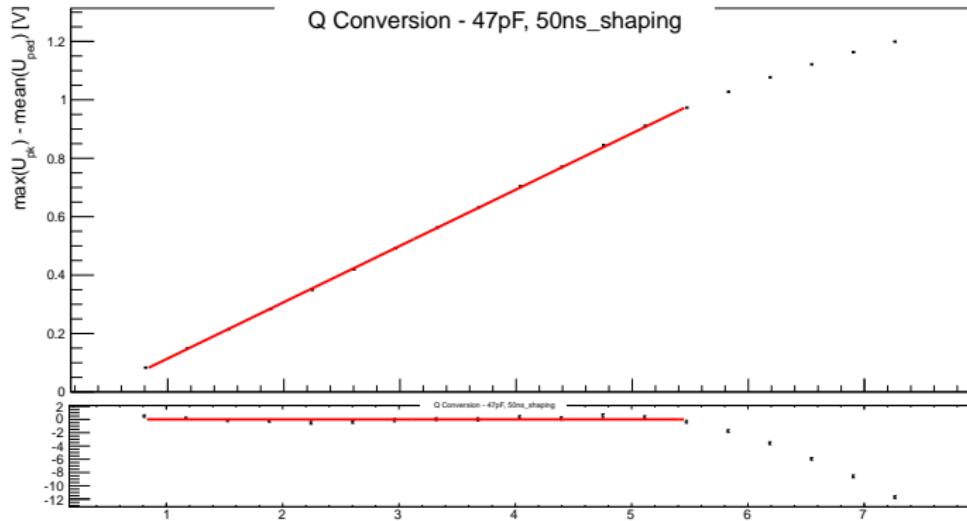
Conducted Measurements

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- Charge conversion for different detector capacitances
- Conversion factor for different DAC configurations

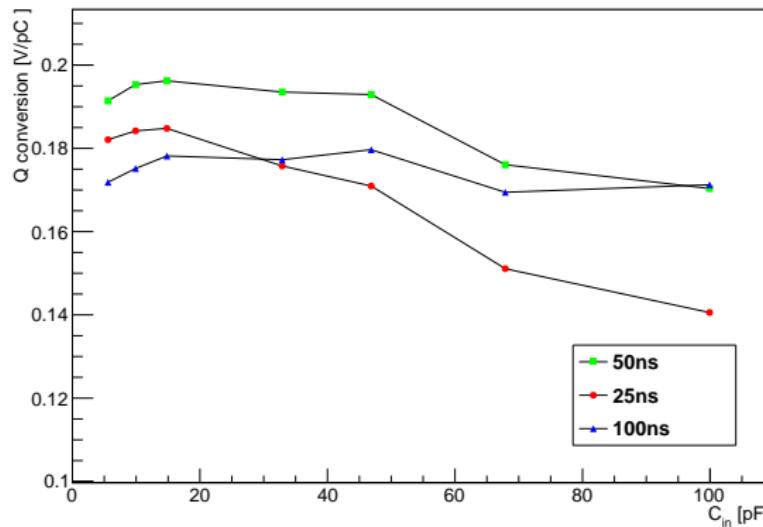
Influence of the Detector Capacitance

Reminder

Input DAC
UniformityCharge
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The conversion factor with 25 ns shaping time is reduced due to the external amplifier bandwidth

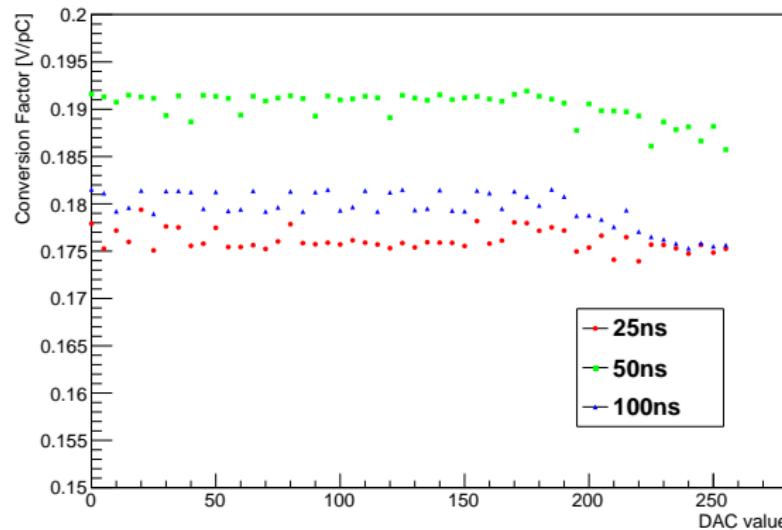
Influence of the DAC Configuration

Reminder

Input DAC
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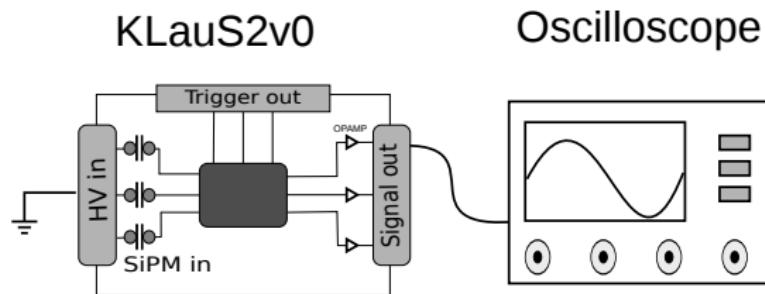
Noise Measurement Setup

Reminder

Input DAC
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The signal noise is measured for different detector capacitances.

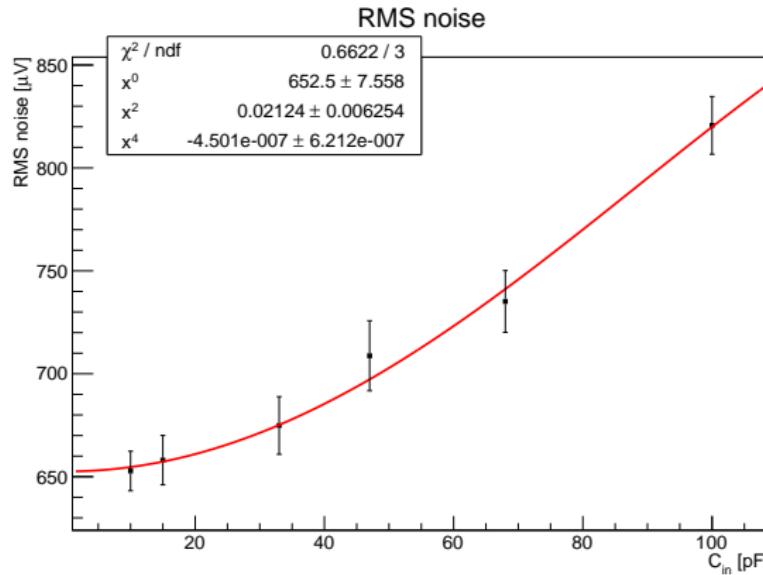
Channel Noise

Reminder

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The measured noise can be described analytically by:

$$\sigma_{\text{noise}} = \sqrt{ax^2 - 4ax^4 + b}$$

Equivalent Noise Charge

The ENC can be calculated from the gain and noise measurements

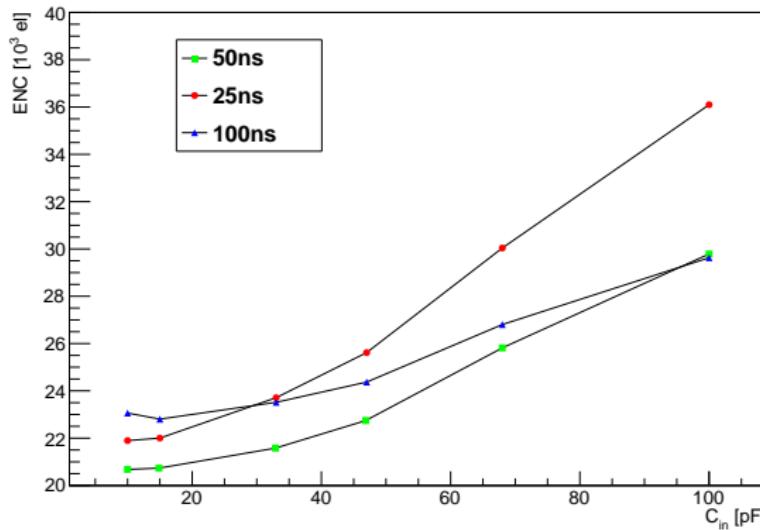
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For a MPPC with $C_d = 40 \text{ pF}$ and a gain of 2.75×10^5 we expect a $\text{SNR} > 10$

Conclusion

- An automated characterization setup is being developed
- Preliminary bias DAC results show a channel variation of $< 5\%$
- Charge conversion behaves as expected
- Bandwidth of external amplifier reduces the output signal at 25 ns shaping time
- Measured noise behaviour can be analytically described
- ENC is consistent with previous measurements

Outlook:

- Chip to chip uniformity measurements
- Further characterization of the power gating functionality