

Micromegas for sampling calorimetry, an update on prototyping and simulation studies



LCWS, 11-15 Nov. 2013, University of Tokyo, Japan

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Overview

- Introduction: Micromegas for sampling calorimetry
- *R&D* : prototypes and testbeam results
 - Large area Micromegas for a SDHCAL technological prototype
 - Spark protected resistive Micromegas
- Monte Carlo : DHCAL compensation & cell size
 - DHCAL performance with different cell size
 - Compensation based on hit density
- Conclusion and future plans

Micromegas for sampling calorimetry

We are studying Micromegas for a digital hadron calorimeter (SDHCAL)

In fact, it is applicable to **sampling calorimetry** in general (ECAL, HCAL, LC, HL-LHC) based on its intrinsic **linear response** that manifests e.g. as a **very high rate capability**



Large area Micromegas (1/3)

Today, we are capable of constructing 1x1 m² Micromegas chambers that <u>fulfil several technical requirements for a technological SDHCAL prototype</u>

XY-scalability (**board interconnects**), thickness (< 1cm), 1x1 cm² anode pads Embedded electronics with 3 readout thresholds, **power-pulsing, self-triggering** (MICROROC) Diode protection of ASIC against sparks (works), more elaborate solution with R-coatings studied





Large area Micromegas (2/3)

Lot learnt inside the CALICE RPC-SDHCAL: 4 Micromegas + 46 RPC

Efficiency > 95% at a low gas gain of 10^3 (= very low spark probability) Uniformity of a few % over whole chamber area but also from chamber to chamber \rightarrow easy (cell-to-cell & layer-to-layer) calibration in a sampling calorimeter







Large area Micromegas (3/3)

Lot learnt inside the CALICE RPC-SDHCAL: 4 Micromegas + 46 RPC

Measurement of longitudinal pion shower profile in Micromegas chambers Integral of profile \rightarrow leakage-corrected pion response of a virtual Micromegas SDHCAL

 \rightarrow "physics" results with a few layers



Similar measurements with electrons and small prototypes (16x16 cm²) described in the next slides



Spark protected Micromegas

Today's protections against sparks are *current*-limiting **diodes on-PCB** <u>and</u> in-ASIC

Seems (so far) sufficient to protect the electronics in hadron showers

Sparks, however, are allowed to develop with following consequences

- \rightarrow all 64 ASIC channels fire (sometimes all 1536 ASU channels) + drop of the HV of the mesh
- Wrong energy measurement + dead time in the HV section where the spark occur



Spark protection effectiveness (1/2)

July 2012 testbeam at DESY (1-5 GeV e=)

 \rightarrow Compare <u>standard and resistive</u> prototypes \rightarrow Spot <u>charging-up effects</u> in resistive prototypes

Spark study in Ar/CO₂ 90/10

Rate of 1.5 kHz (4 cm²), *increase* V_{mesh} *by small steps* 500-560 V \rightarrow Gas gain from a few 10³ to a few 10⁴ Monitor mesh current



Image: Strate Strate

Standard un-coated prototype

Current spikes & voltage drops At 510 V, during 10 minutes: 11 sparks & $\sim 10^6 e^-$ Spark probability of $\sim 10^{-5} / e^-$

Spark protection effectiveness (2/2)



Comparison standard/resistive prototypes

Small signal loss in R-prototypes \rightarrow lower efficiency at same V_{mesh}

No lateral spread of signals in R-prototypes (as expected from their design)

1-2% loss of efficiency from 0.25 to 50 kHz/cm² beam intensity in 1 R-prototype



(standard prototype HV lowered to avoid sparks at high rate; sparks can disturb the DAQ)

On-going study of charging-up effect (1/2)

RD51 lab. at CERN: X-ray gun (Cu target 8 keV line)

Beam collimated to 3 mm diameter (~ 7 mm²) \rightarrow one pad X-gun calibration performed with standard detector (*Igun* \rightarrow *X-rate*)

Readout1 at low rate (< 1 MHz) Counting hits (~ quanta conversion) in trigger-less mode with MICROROC

Readout2 at high rate (> 1 MHz) *Measure mesh current on power-supply*





On-going study of charging-up effect (2/2)

Only "resistive via" prototype tested so far (preliminary results, methodology not yet fully defined)

Current mode

- \rightarrow resistive prototype works at least up to 700 MHz/cm² but with a saturated response
- \rightarrow linear region suggested below 50 MHz/cm²

Counting mode

 \rightarrow linear region extends at least up to 300 kHz/cm²

Linearity limit between 0.3 & 50 MHz/cm² To be continued...

520 V

250

Xray intensity (kHz/cm²)

300

350

200



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MC simulation: DHCAL pion response & cell size

We don't have a Micromegas calorimeter but we have a model of it Model: 100 layers of 1x1 m² with 1x1 cm² pads and steel absorbers (10k pions/E, QGSP_BERT) (Pion response with 1x1 cm² compares well with testbeam data, so far only check)

Old DHCAL response & resolution, multi-threshold compensation with SDHCAL

New Effect of cell size + *hit density compensation*

Performance quickly degrades with cell size (as geometric saturation gets worse)



Hit density compensation of DHCAL response (1/2)



Hit density compensation of DHCAL response (2/2)

With 1x1 cm² cells, the "total number of neighbouring hit" response is only slightly saturated Using a logarithmic parametrisation, the energy can be reconstructed (non-linearity < 5%)

Above 30 GeV, energy resolution improved w.r.t. simple counting approach (sum of hits) Above 80 GeV, compensation as effective as the one based on multi-thresholds

 \rightarrow Worth testing on testbeam data (DHCAL, SDHCAL)



Future & conclusion

Large area Micromegas chambers can be built with high and uniform performance

Still, they are rooms for improvement such as *integrating resistive spark protections* inside the chamber during the *ASU* manufacturing process (instead of soldering 1 diode / channel on PCB)

Immediate step: understand charge-up effect of small resistive prototypes Next step: construct and test a *large area resistive prototype*

What we would like to do on the longer term: measure instead of simulate \rightarrow *Micromegas HCAL prototype* \rightarrow Proposal submitted to French funding agency, answer in 2014