

Digital Hadron Calorimetry using Gas Electron Multipliers

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Overview

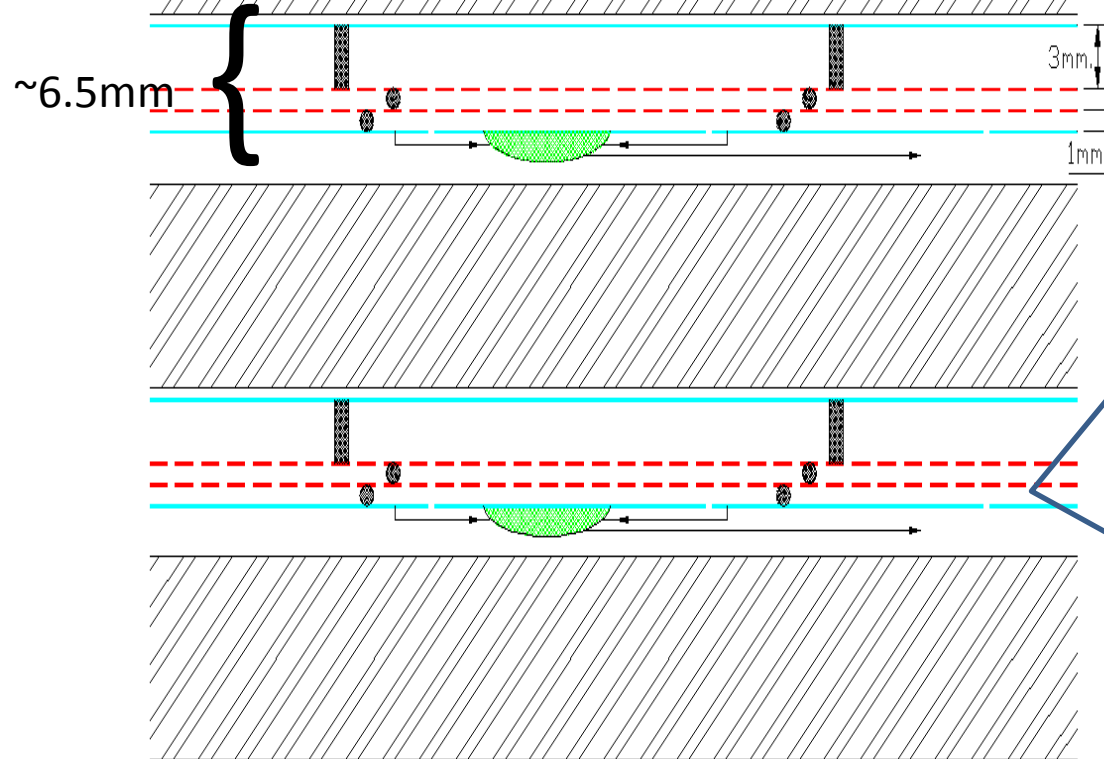
- GEM/DHCAL basics
- Review of GEM/DHCAL developments
- Results to date
- Plans

- ThickGEM/DHCAL
- THGEM structures
- Recent results/CERN test beam

GEM-based Digital Calorimeter Concept

GEM-BASED DHCAL CONCEPT

Use Double GEM layers to minimize gap size



NOT TO SCALE

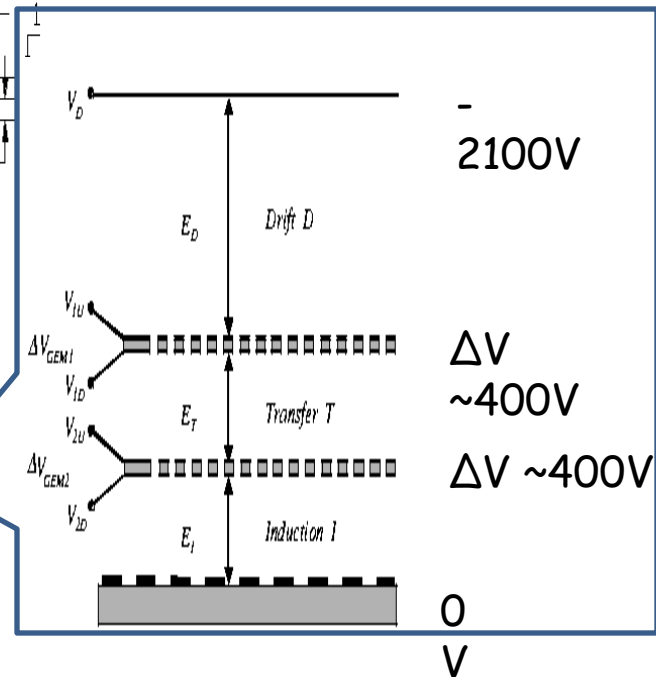
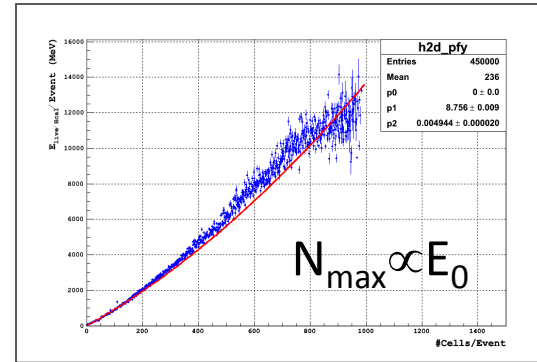


Fig. 1: Schematics of a double-GEM detector.

30X30 prototype GEM chamber and Readout Electronics

➤ GEM Foils(3M)

310x310 mm²

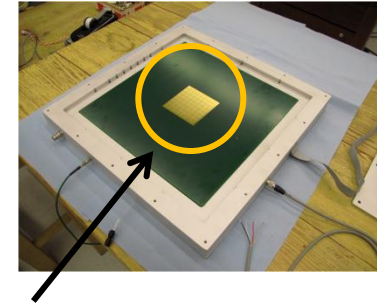
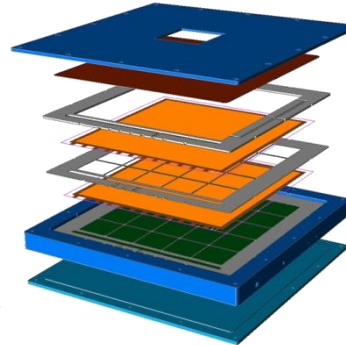
Active area : 280x280 mm²

➤ Active gas volume

350x350x6 mm³ → For 3/1/1 gaps

➤ Readout channels: KPIX(64), DCAL(256)

Chamber



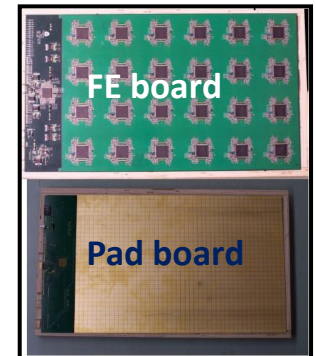
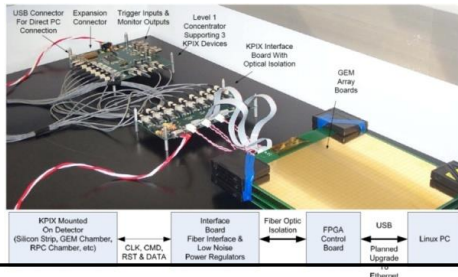
64-readout pads(KPIX)

❖ KPIX readout system/SLAC

- ✓ 13 bit resolution(ADC)
- ✓ Handles 1024 channels/chip,
- ✓ 3 gain ranges
- Normal gain

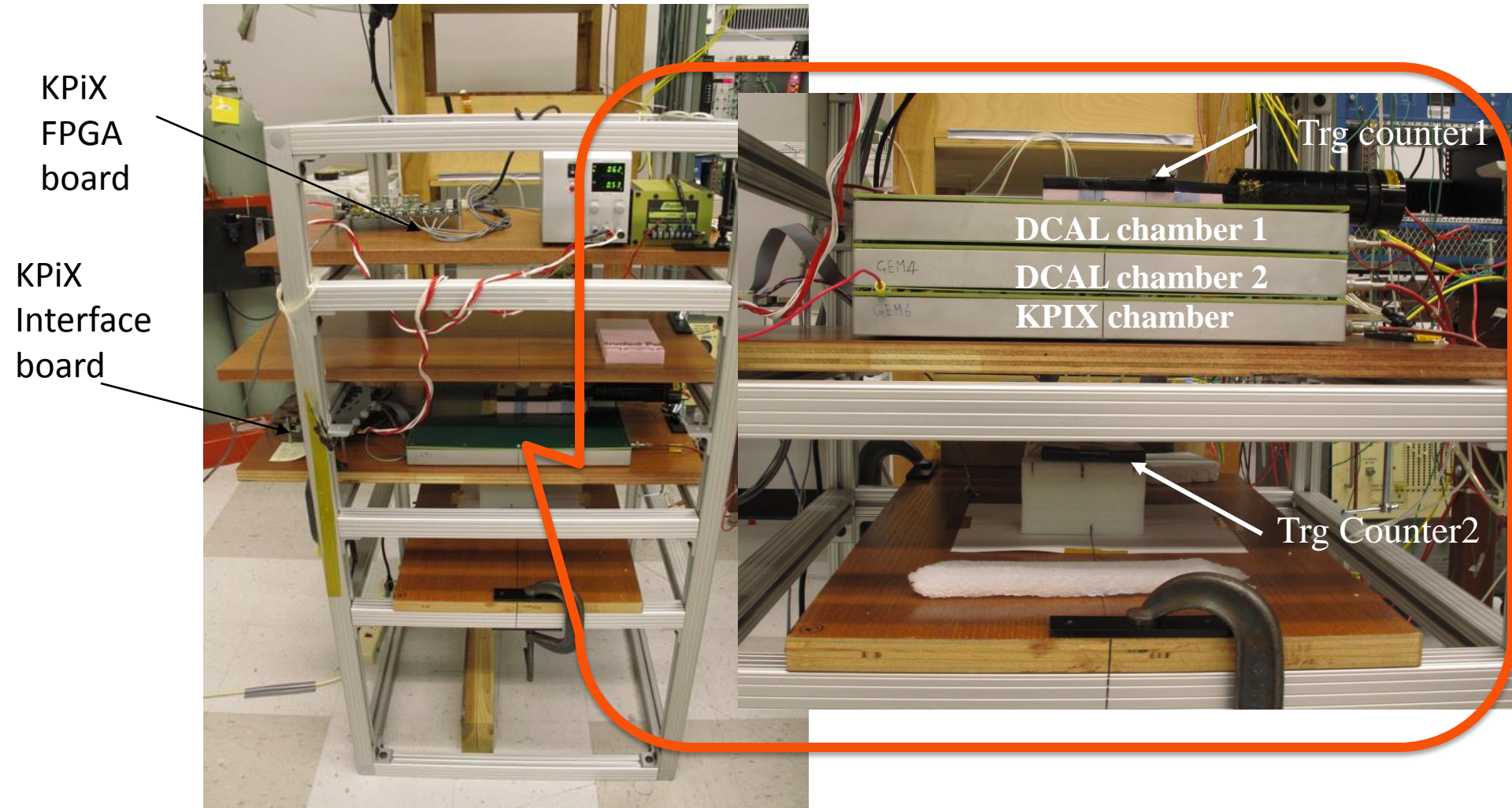
❖ DCAL readout system/ANL

- ✓ 1 bit resolution(ADC)
- ✓ 64 channels/chip
- ✓ 2 gain ranges
- High gain for GEMs (10 fC~200 fC signals)
- Low gain for RPCs (100 fC~10 pC signals)

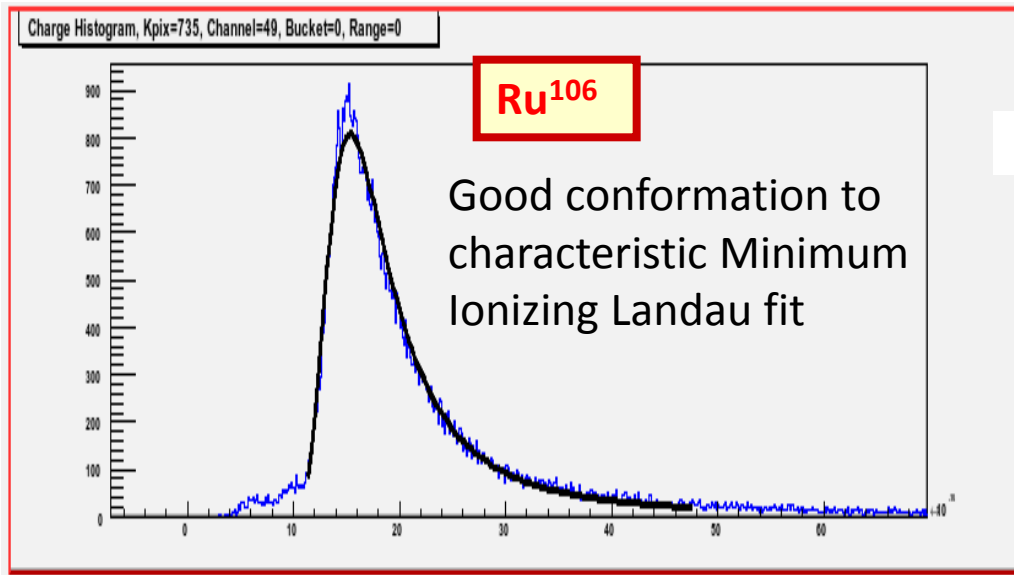
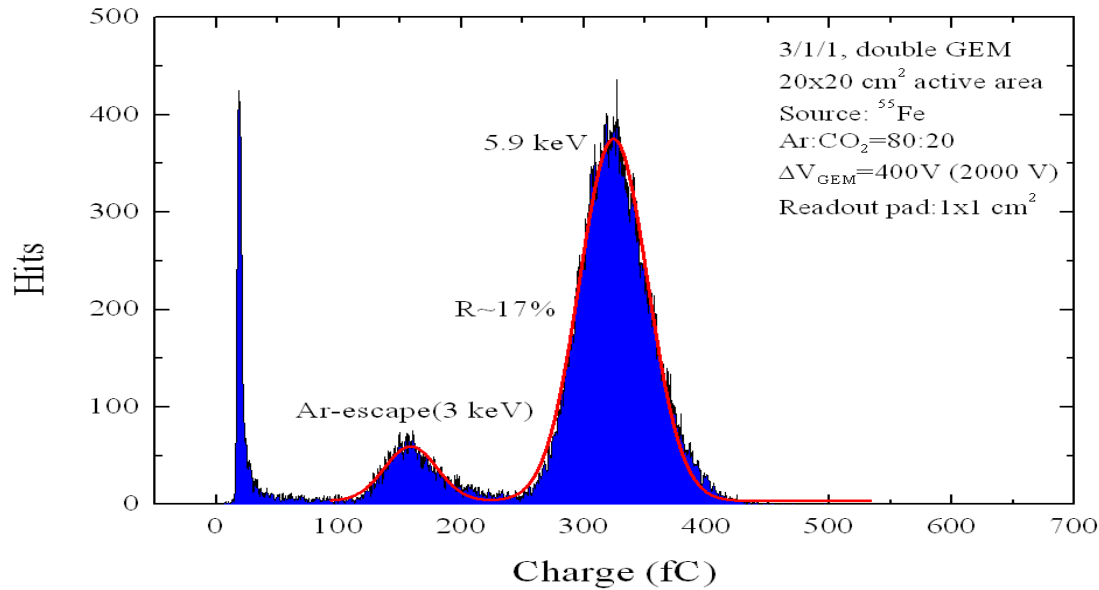


Readout systems

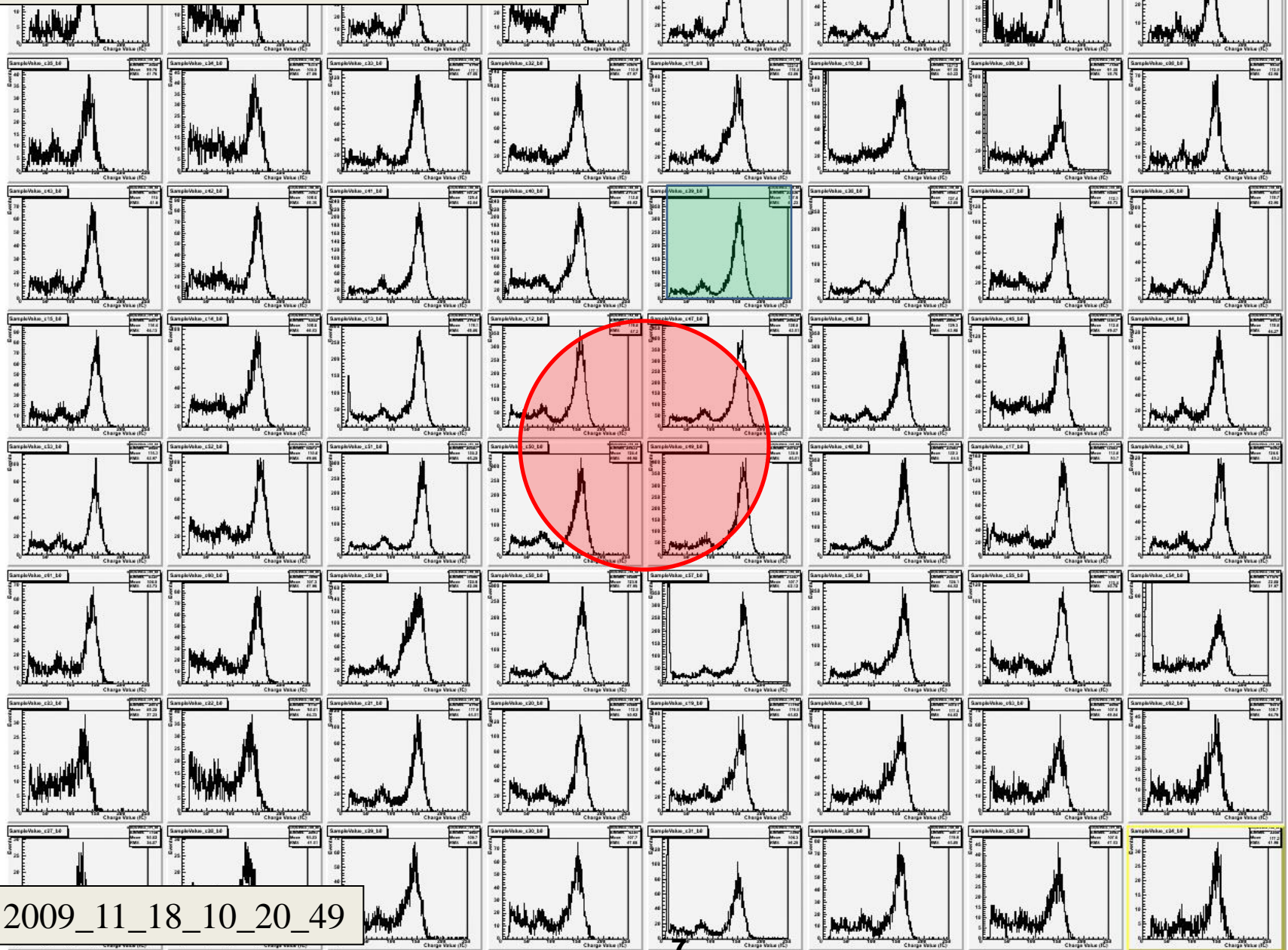
UTA GEM-DHCAL Cosmic Test Stand



GEM+KPiX7 Fe⁵⁵ and Ru¹⁰⁶ Spectra



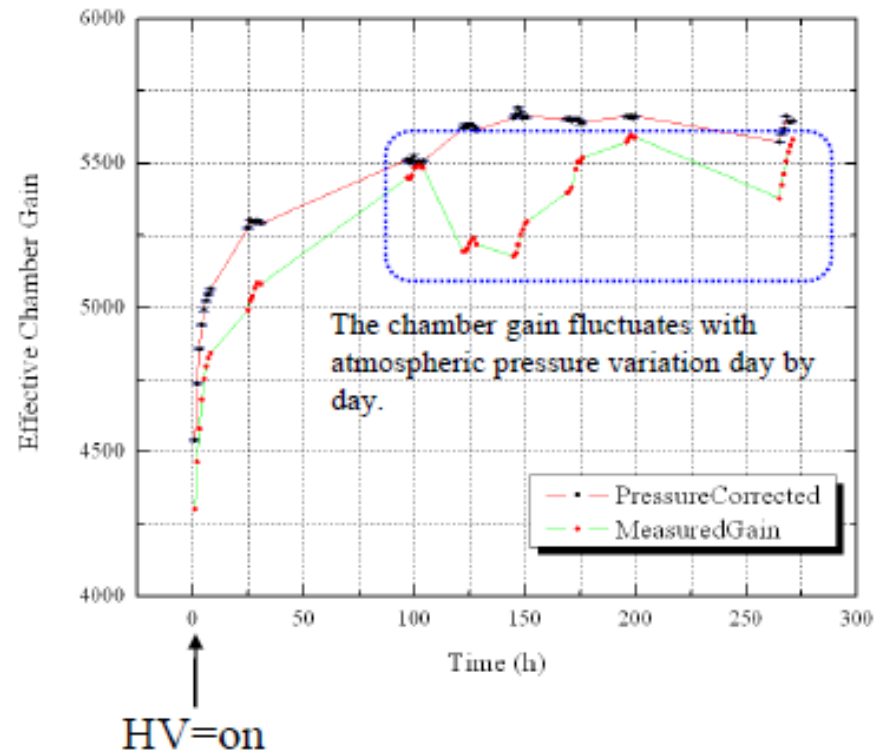
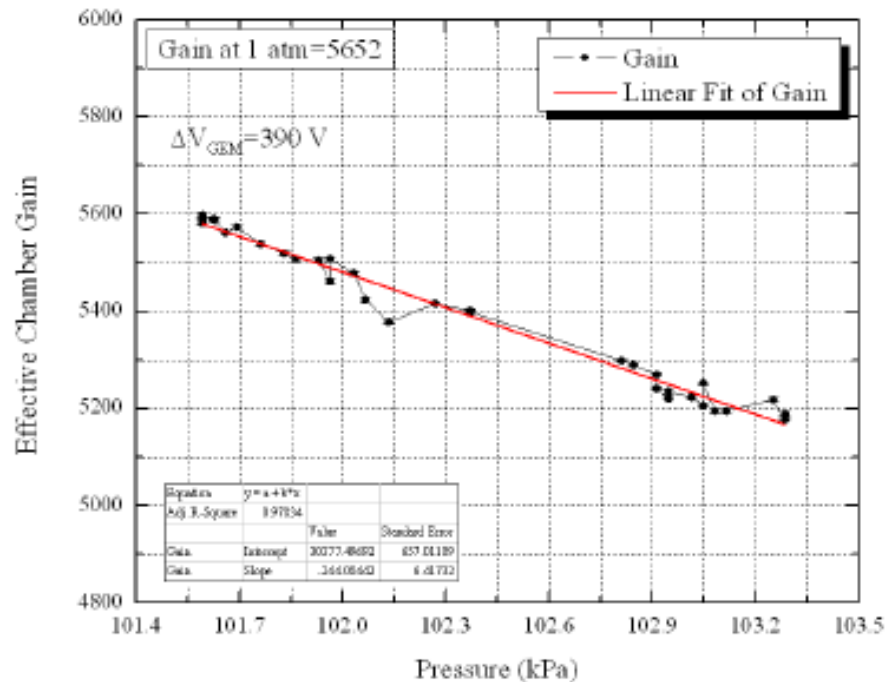
HV=1950V, Fe55, ST=1.8V=14 fC



2009_11_18_10_20_49

Pressure Dependence of Gain

HV = 1950V ($\Delta V_{\text{GEM}} = 390$ V)



We use an open gas system (gas flows at atmospheric pressure).

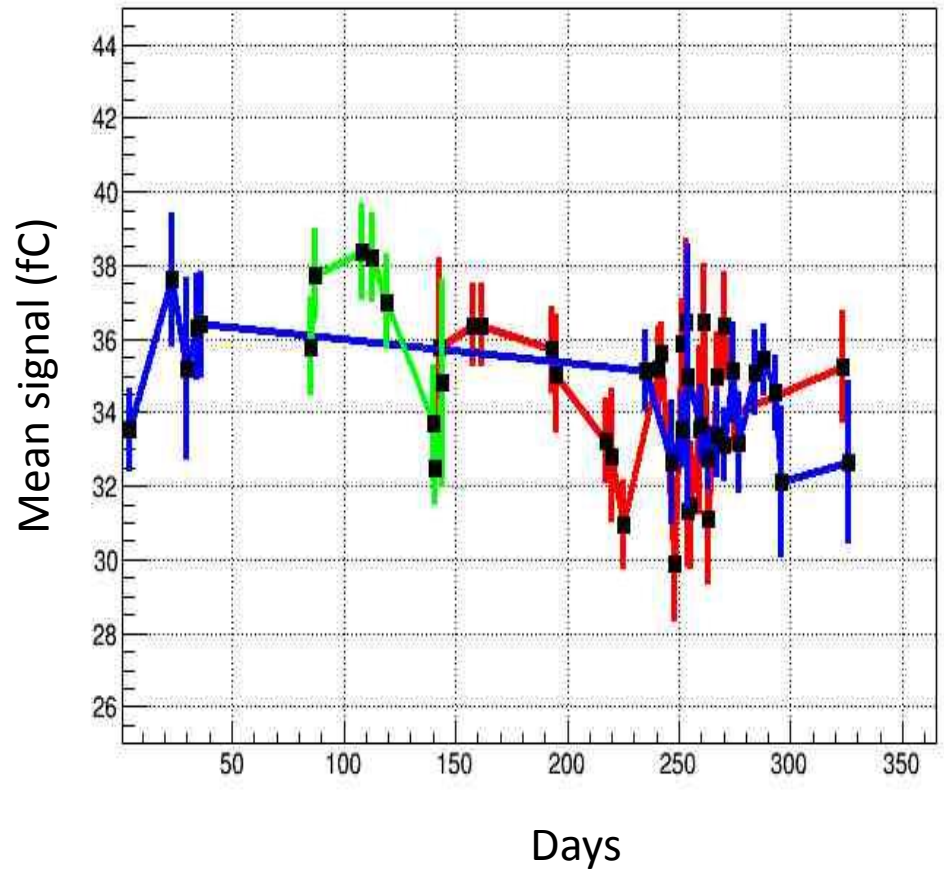
Thus, pressure inside chamber is affected by the atmospheric pressure directly.

This pressure change affects the chamber gain.

The chamber gains were recalculated to the values at 1 atm.

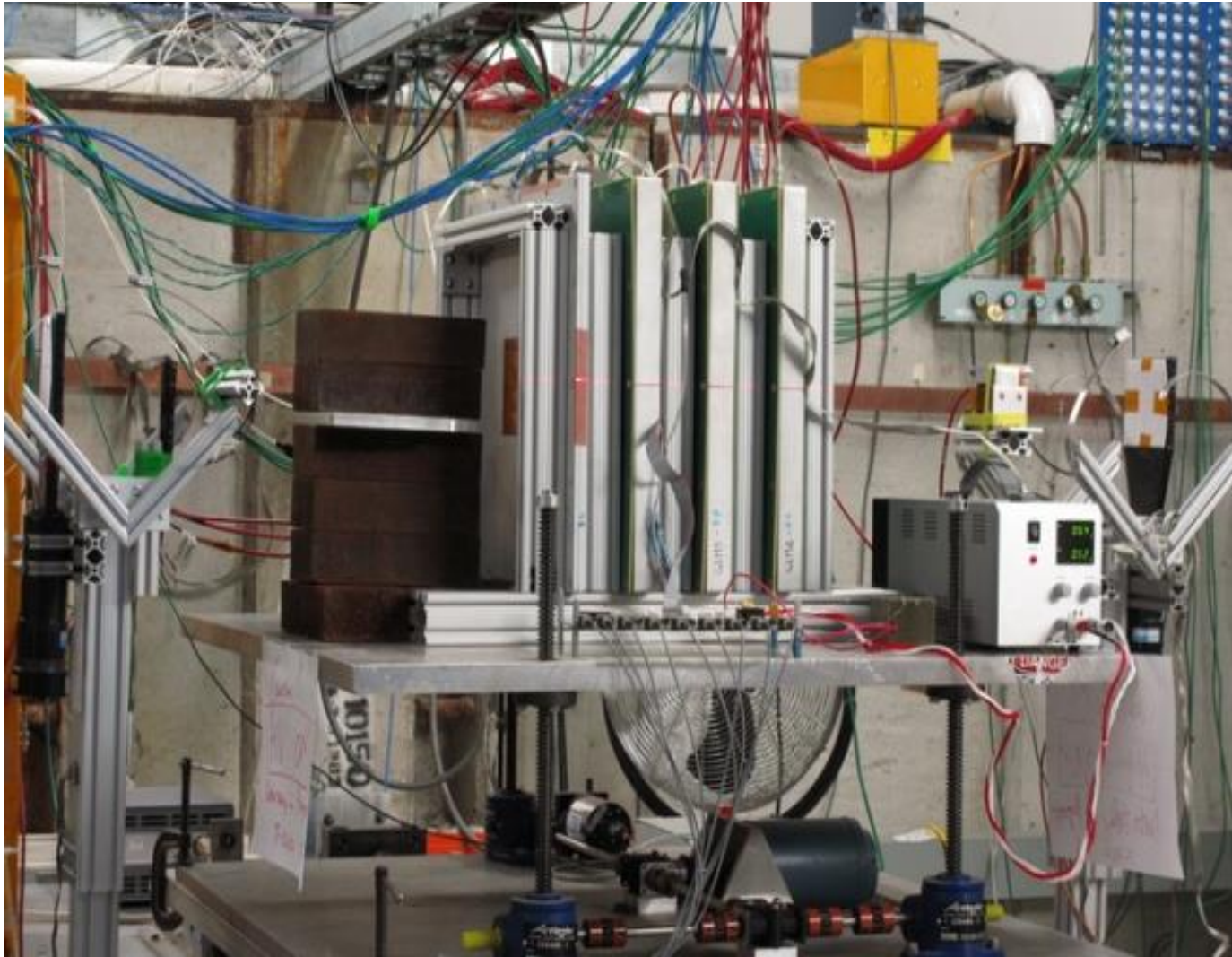
Study the seasonal change of GEM detector gain.

- Red: 2012, blue: 2013, green: 2014

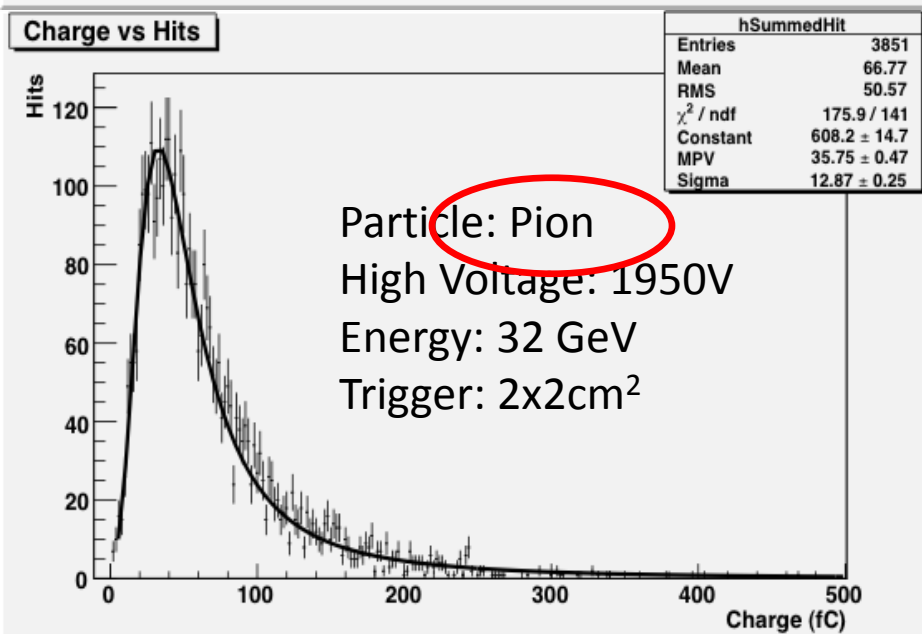
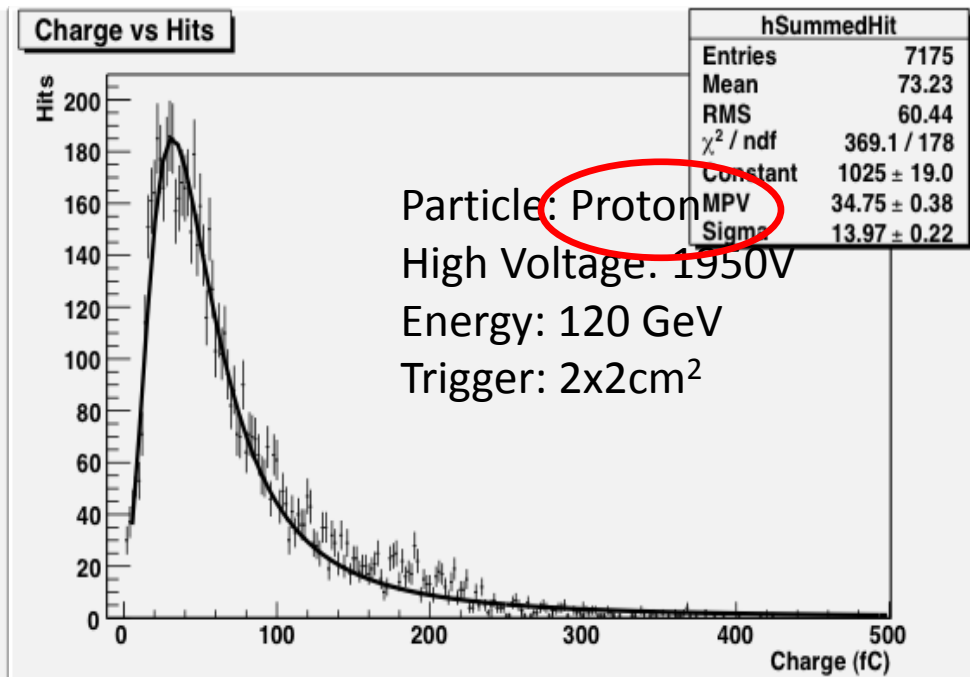
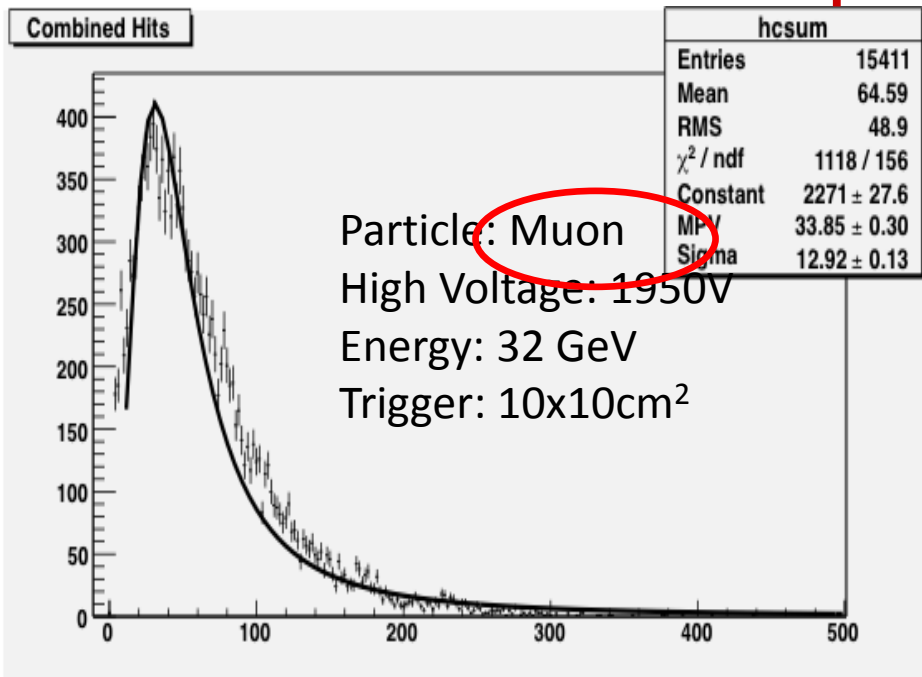


Yvonne Ng – UTA Student

T-1010 Experiment Setup

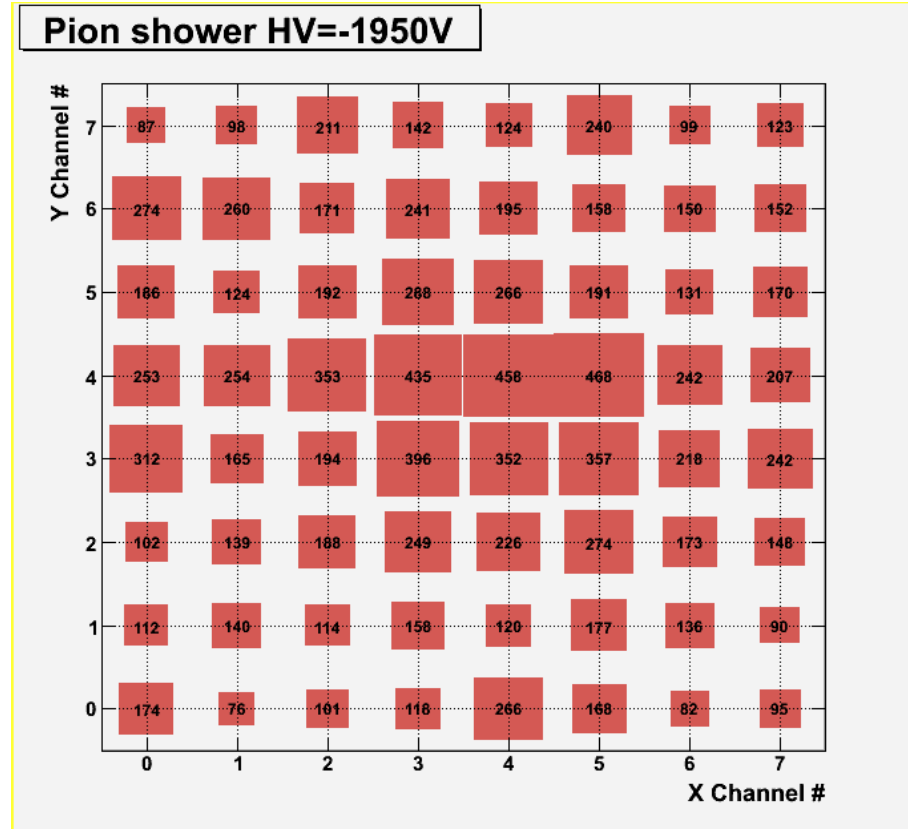
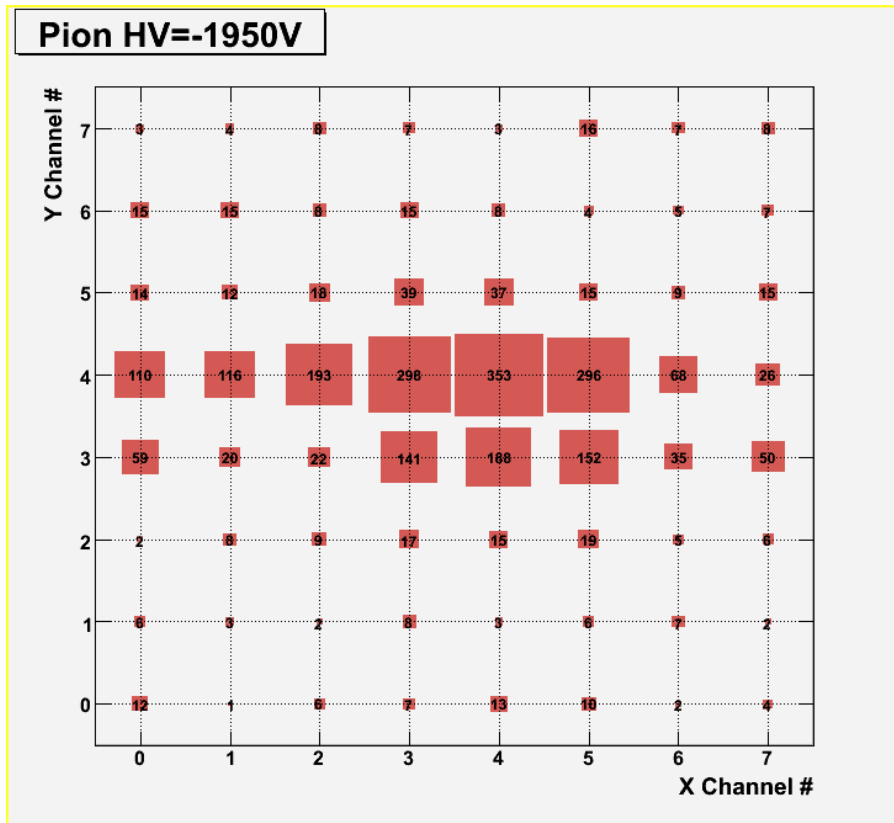


GEM Response with KPiX



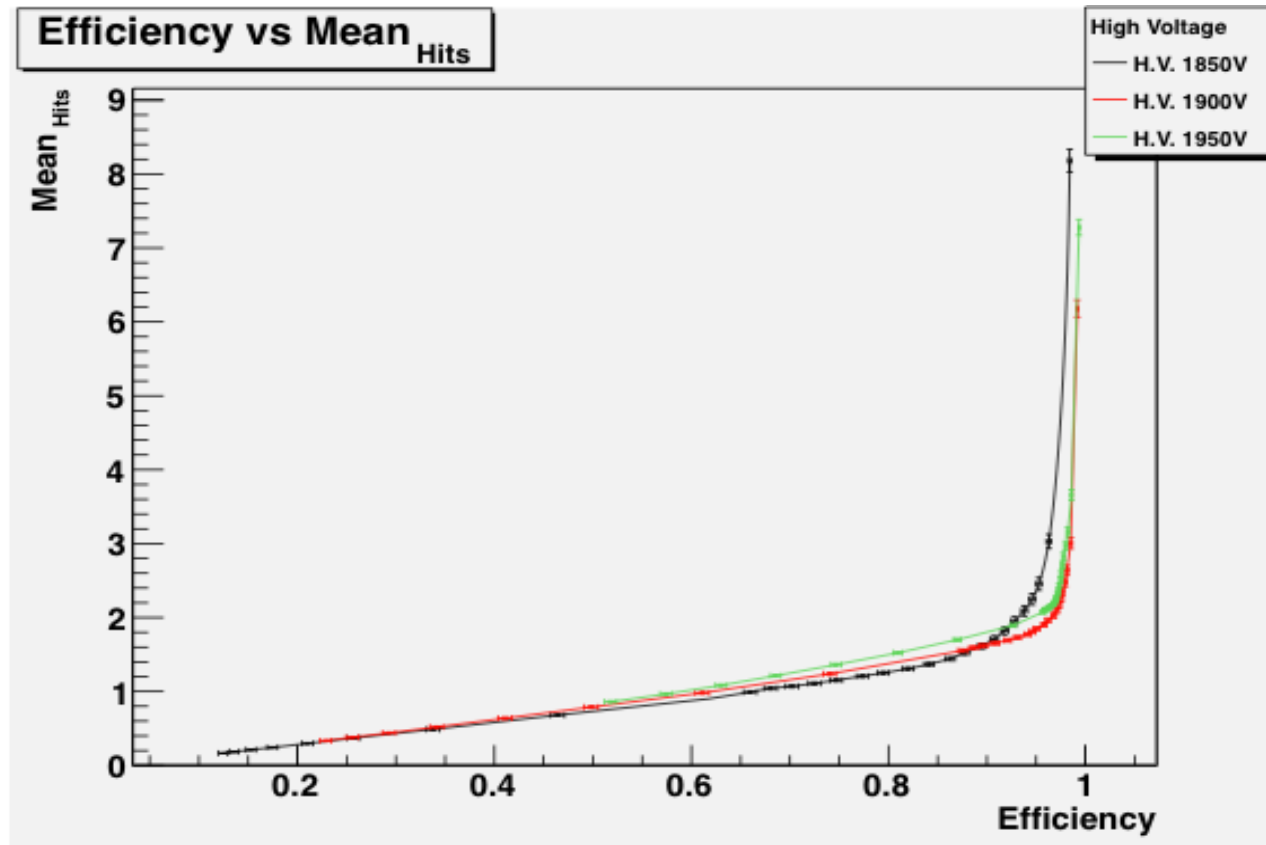
Results pressure corrected

Hit Map for Pions vs Pion Showers (KPiX)



Hits above 5fC were counted and normalized to 1000
Demonstrates the KPIX capability to take many hits simultaneously

Efficiencies and Hit multiplicities (KPiX)

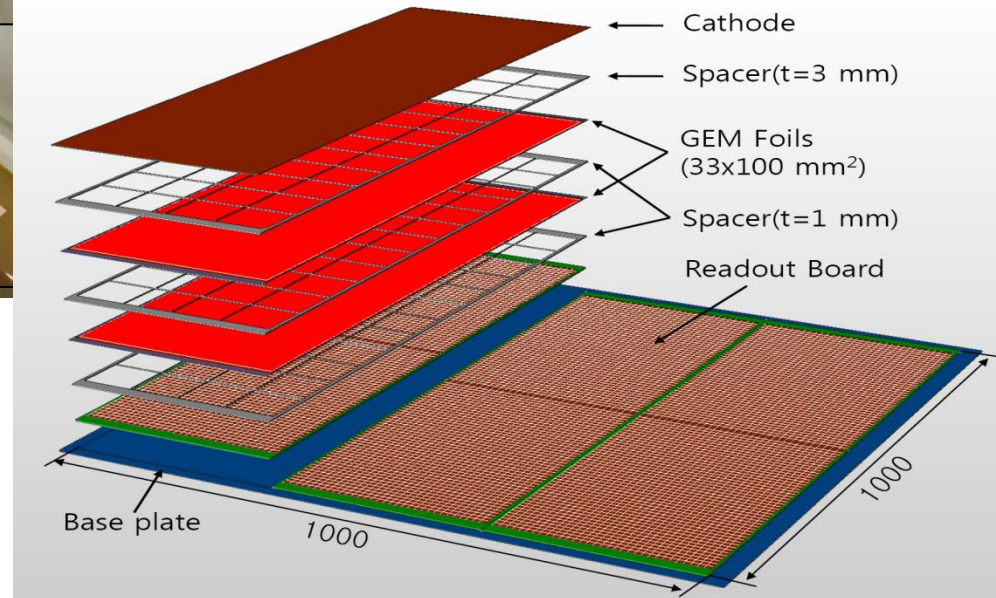


Results pressure corrected

Toward 100cmx100cm GEM Planes



Each of the GEM 100cmx100cm planes will consist of three 33cmx100cm unit chambers



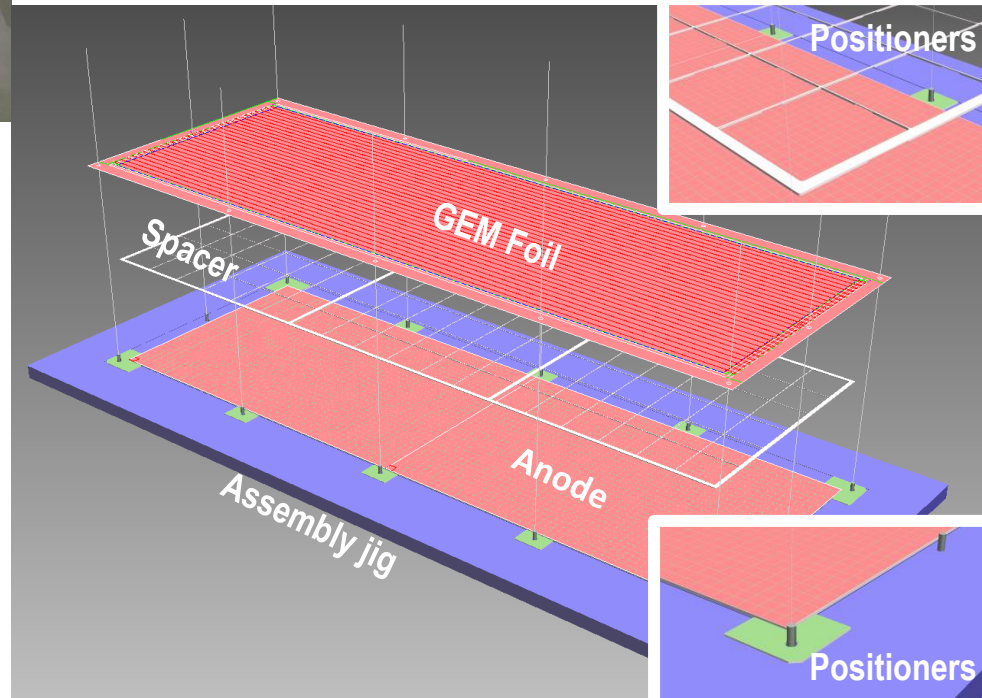
Toward 100cmx100cm GEM Planes!!



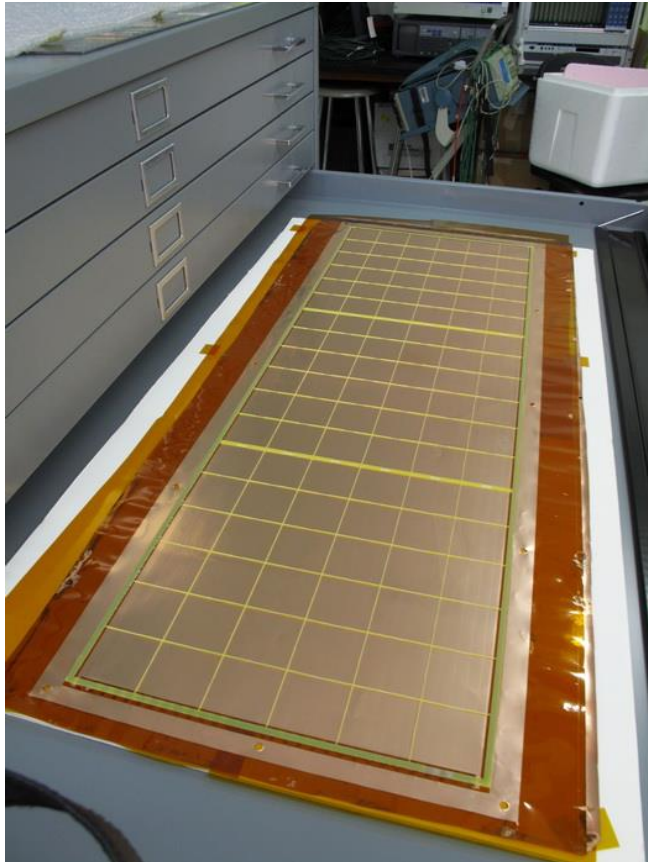
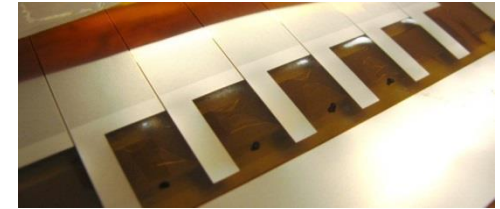
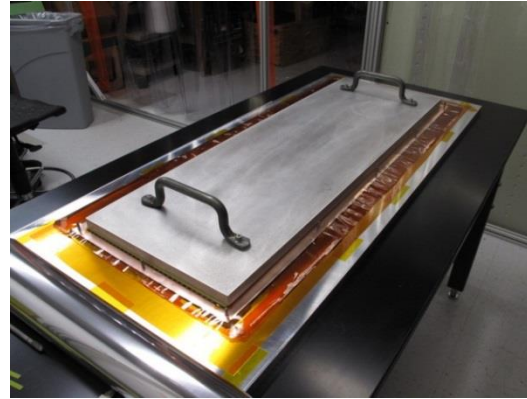
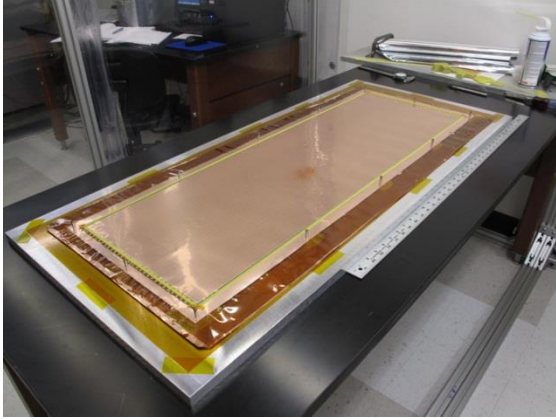
Class 10,000 clean room (12'x8') construction completed

Two 33cmx100cm chamber parts delivered

Jig for 33cmx100cm chamber being procured



Preparation for LGEM Assembly



Current situation:

- Waiting resumption of detector R&D support
- Characteristics of GEM chambers (Eff. vs. mult, long term stability) show suitability for DHCAL application.

Next:

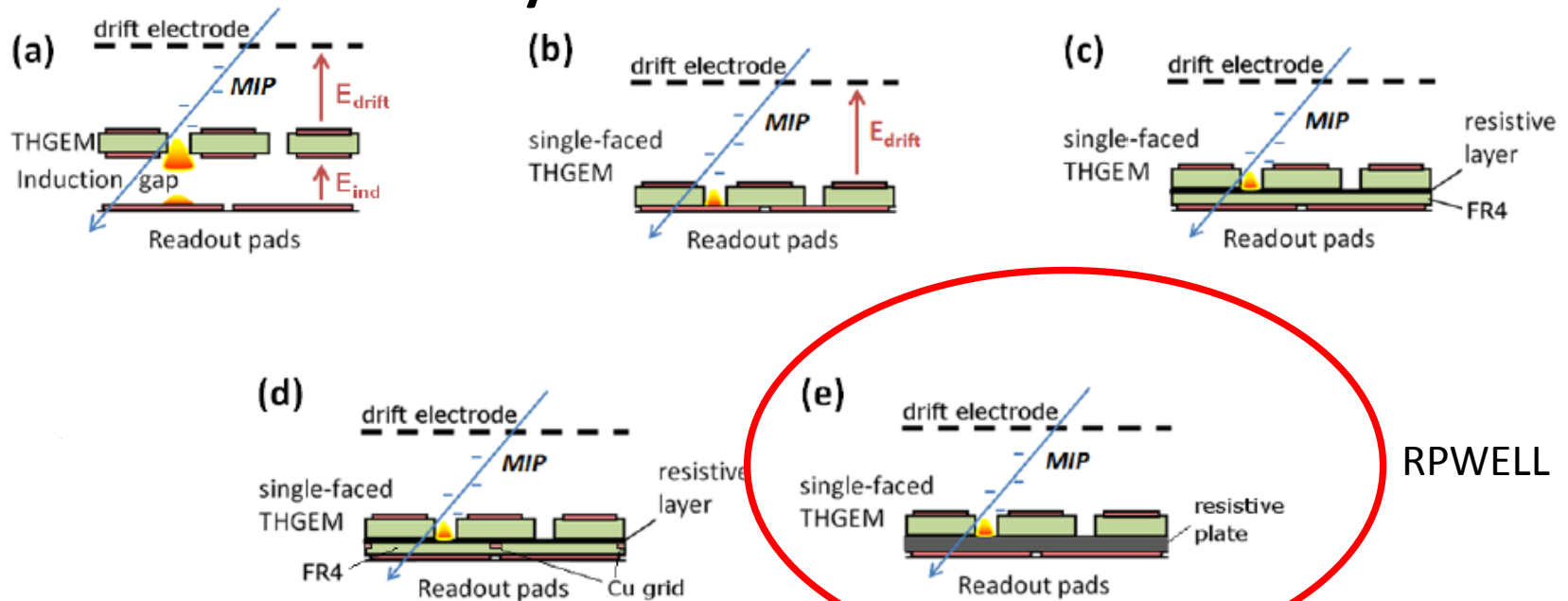
- Complete one 33cm x 100cm chamber
- Test chamber in lab
- Build two more chambers
- Beam test with KPIX/synchronous beam at SLAC ESTB

DHCAL – Thick GEM

ThickGEM technology developed at Weizmann Institute of Science, Israel
Tested in collaboration with Portugal (Aveiro, Coimbra), US(SLAC,UTA)

Slides courtesy Dr. Shikma Bressler (WIS)

From very recent CERN test beam



Recent advances with THGEM detectors

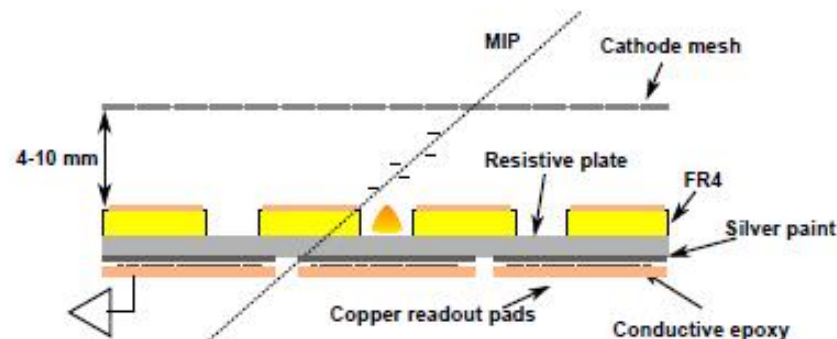
S. Bressler, L. Arazi, L. Moleri, M. Pitt, A. Rubin and A. Breskin

2012 JINST 7 C05011

The RPWELL

Resistive Plate WELL:

- WELL coupled to materials with large bulk resistivity
- The charge is induced on the readout pads
- The avalanche charge flows through the plate to the anode (doesn't propagate sideways)
- Less cross talk ?
(under study)



A. Rubin et. al, JINST 8 P11004 2013



Tested materials

Material	Dimensions [mm]	Bulk resistivity [Ωcm]
VERTEC 400 glass	36×31×0.4	8×10^{12}
HPL Bakelite	29×29×2	2×10^{10}
Semitron ESD 225	30×30×2	2×10^9
Semitron ESD 225	30×30×4	3×10^9

RPWELL

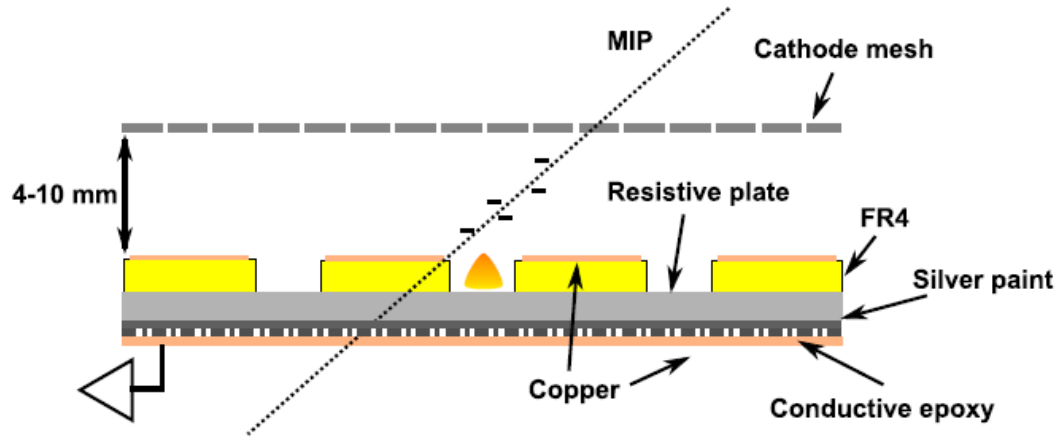
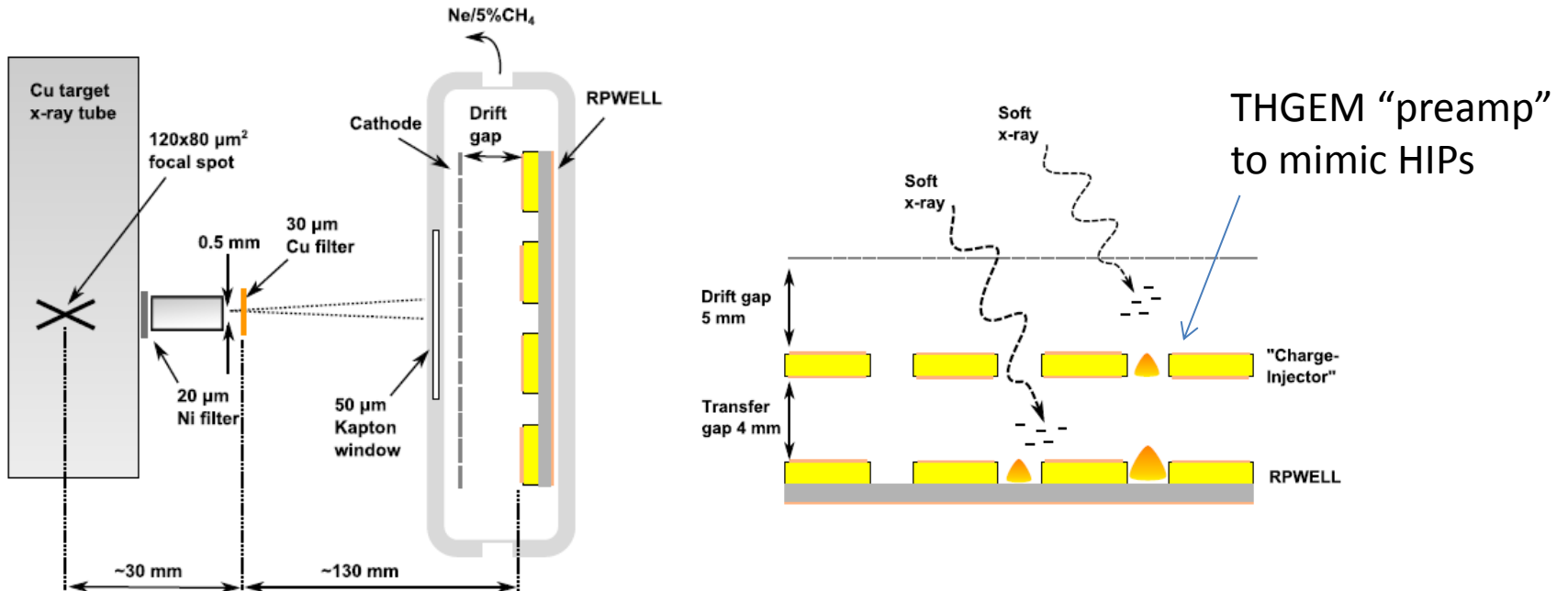


Figure 1. The Resistive-Plate WELL (RPWELL) configuration with a resistive anode and a readout electrode. The WELL, a single-faced THGEM, is coupled to a copper anode via a resistive plate. Charges are collected from the copper anode. In some experiments the WELL was directly coupled to the metal anode.

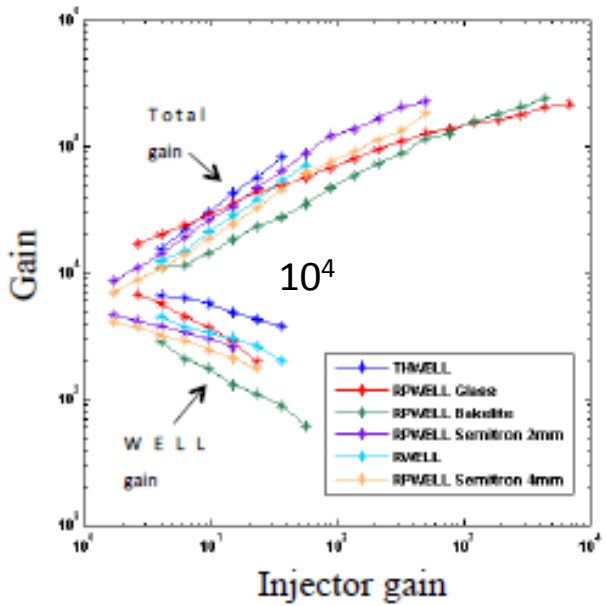
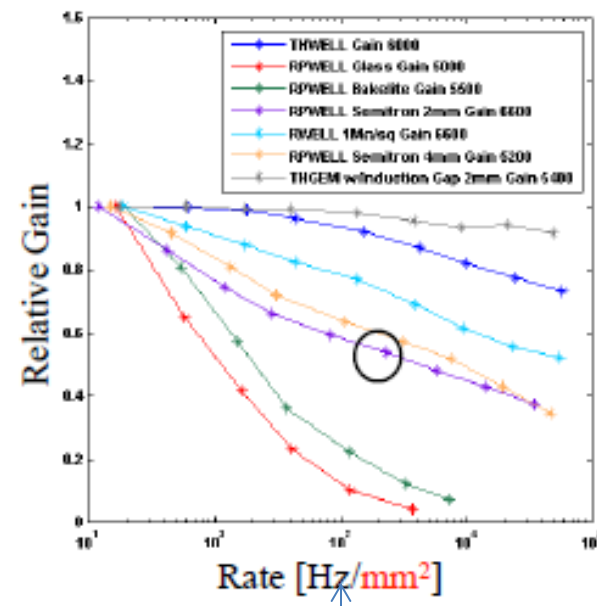
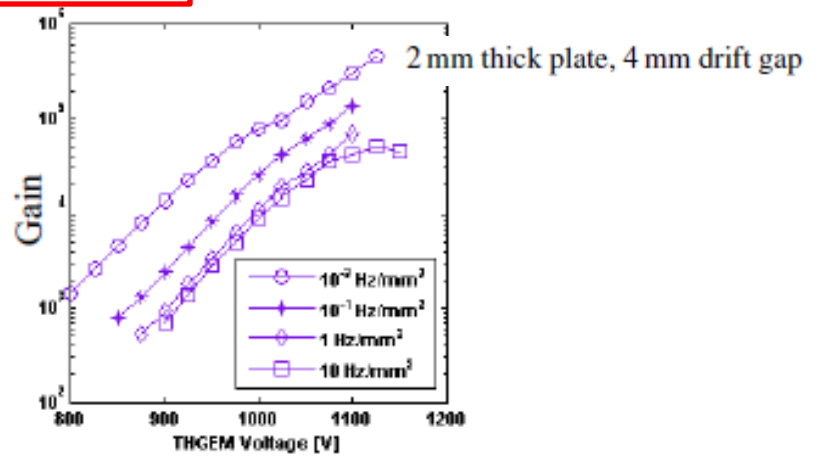
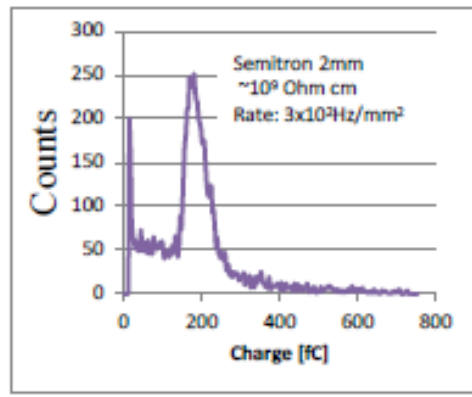
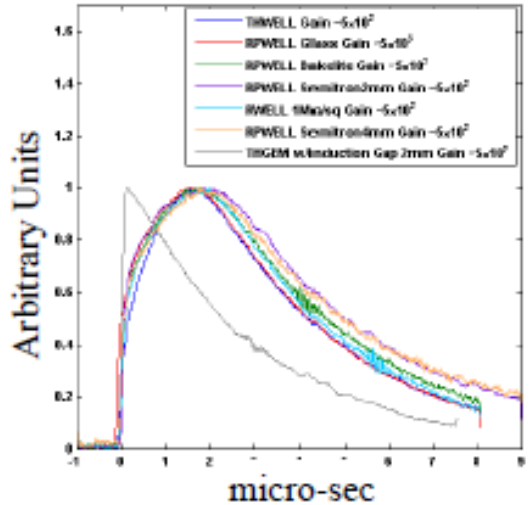


THGEM "preamp" to mimic HIPs

Focus on thin Semitron ESD 225 layers

A. Rubin et. al, JINST 8 P11004 2013

RPWELL $10^9 \Omega\text{cm}$ - 2 mm layer



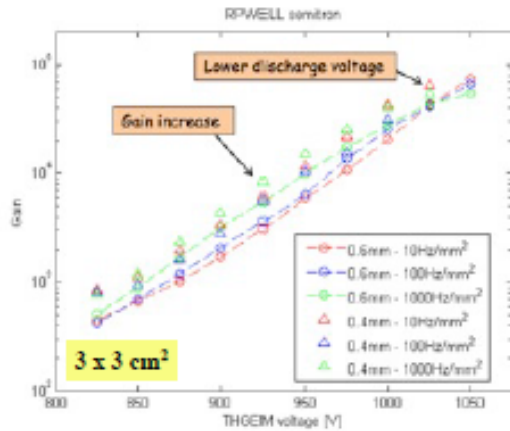
- Same pulse shape as standard well
- $\sim 20\%$ Energy resolution
- Gain saturation at high irradiation rate
- $< 50\%$ gain drop over 4 orders of rate magnitudes
- **No discharges at high rate of HIPs**

10^3

Laboratory studies with X-Rays

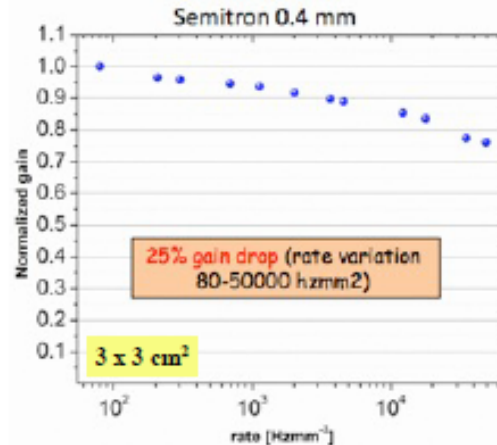
Focus on thin Semitron ESD 225 layers

Improved performance with thinner (0.4 & 0.6 mm) layers - $R \sim \#10^8 \Omega\text{cm}$



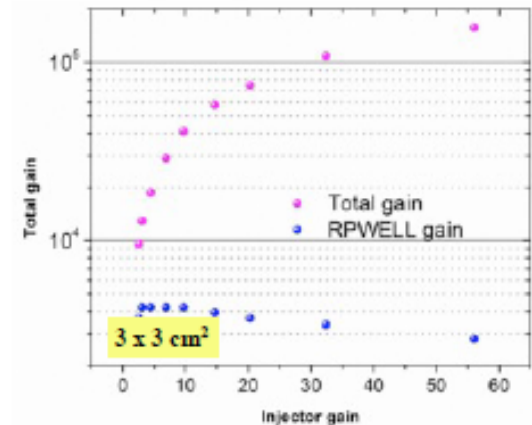
Higher gain for the same voltage

- Smaller anode-cathode gap





Gain drops slower with rate

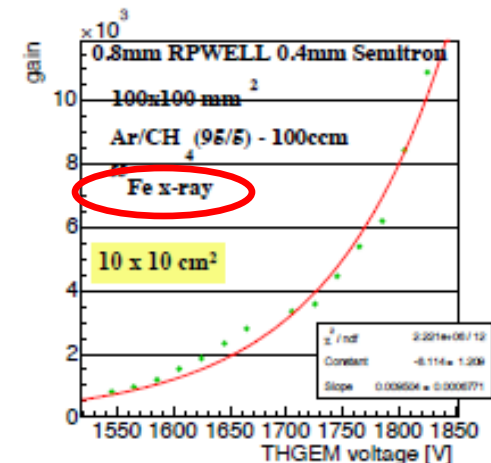
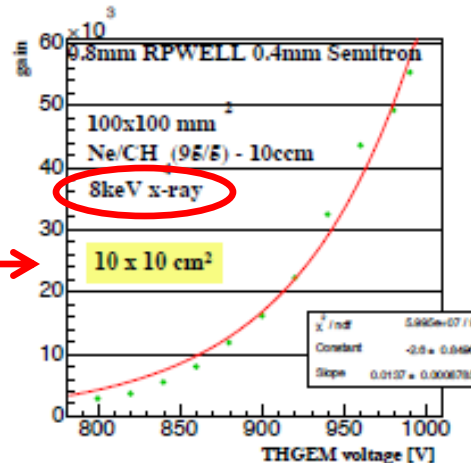
- Lower resistivity



Stable with HIPs

- Observe gain saturation

Decided: 
 Build 10 x 10 cm² proto.
 with 0.4 mm Semitron layer
 Test in the lab 
 Test in the beam

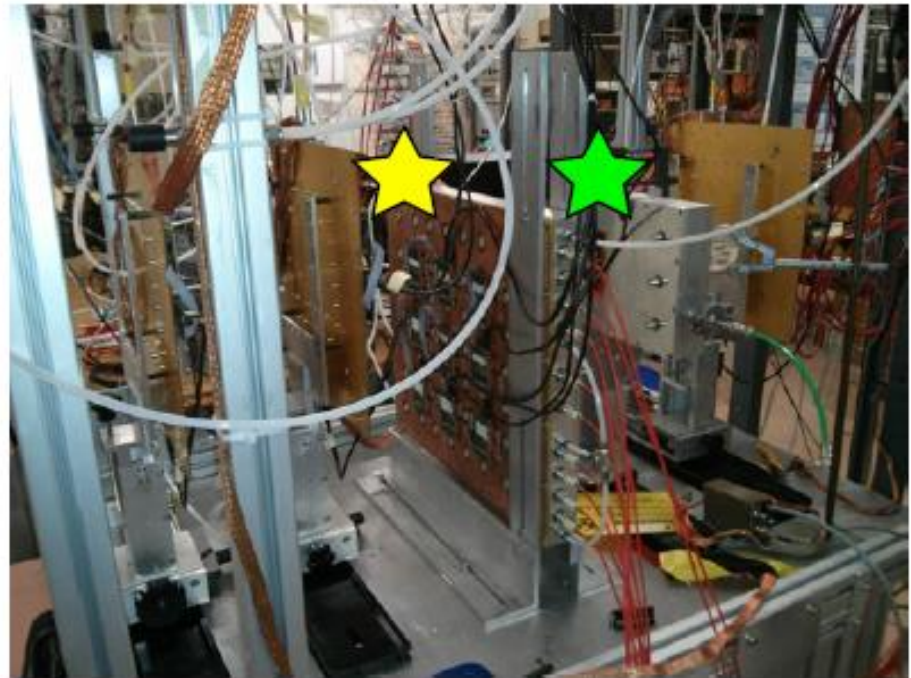


Laboratory studies with X-Rays

Tests in the beam - happening **now**

2 detectors setup + telescope installed in SPS/H4 beam area:

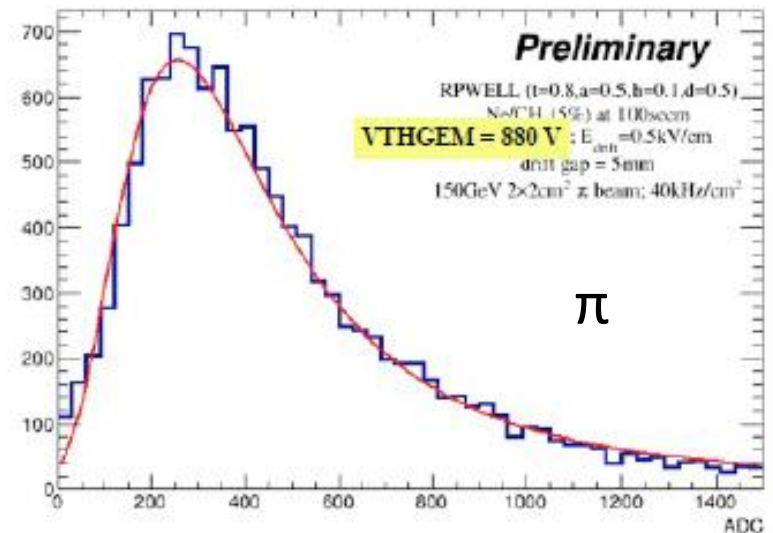
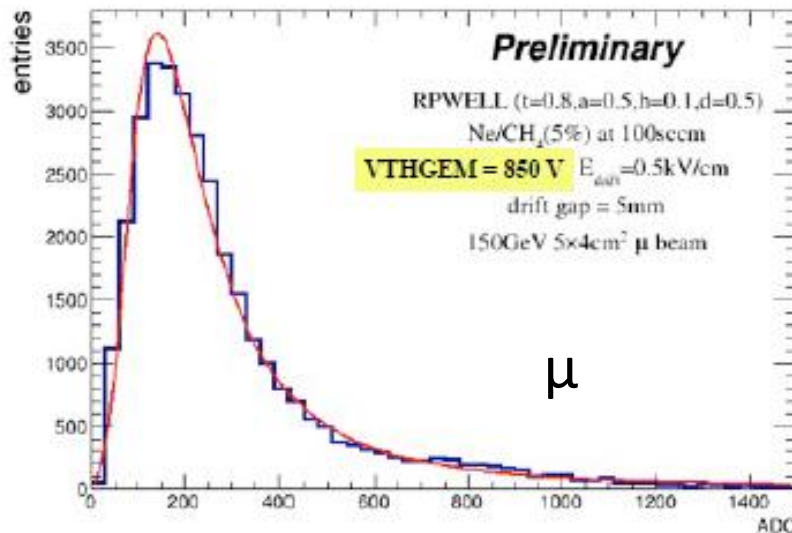
- ★ 0.4 mm 30 x 30 cm² RPWELL 0.4 mm Semitron layer
- ★ 0.8 mm 10 x 10 cm² RPWELL 0.4 mm Semitron layer



Results - 10 x 10 cm² 0.8 mm RPWELL

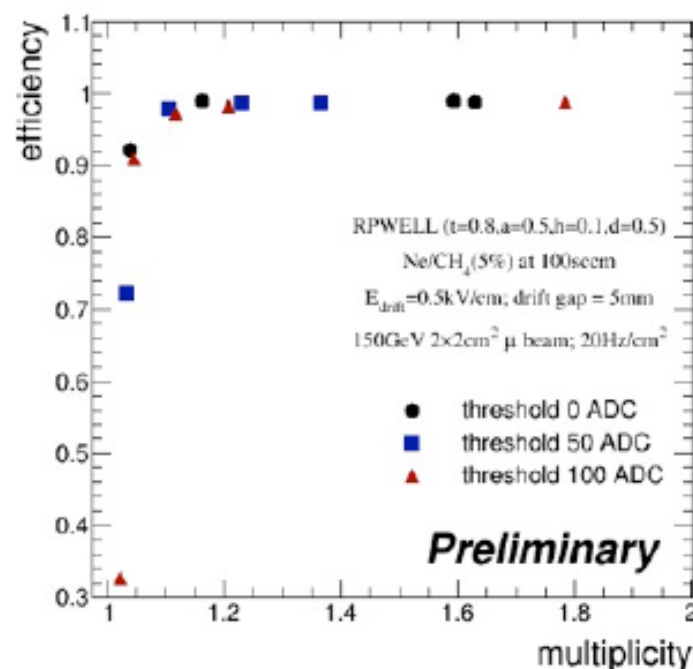
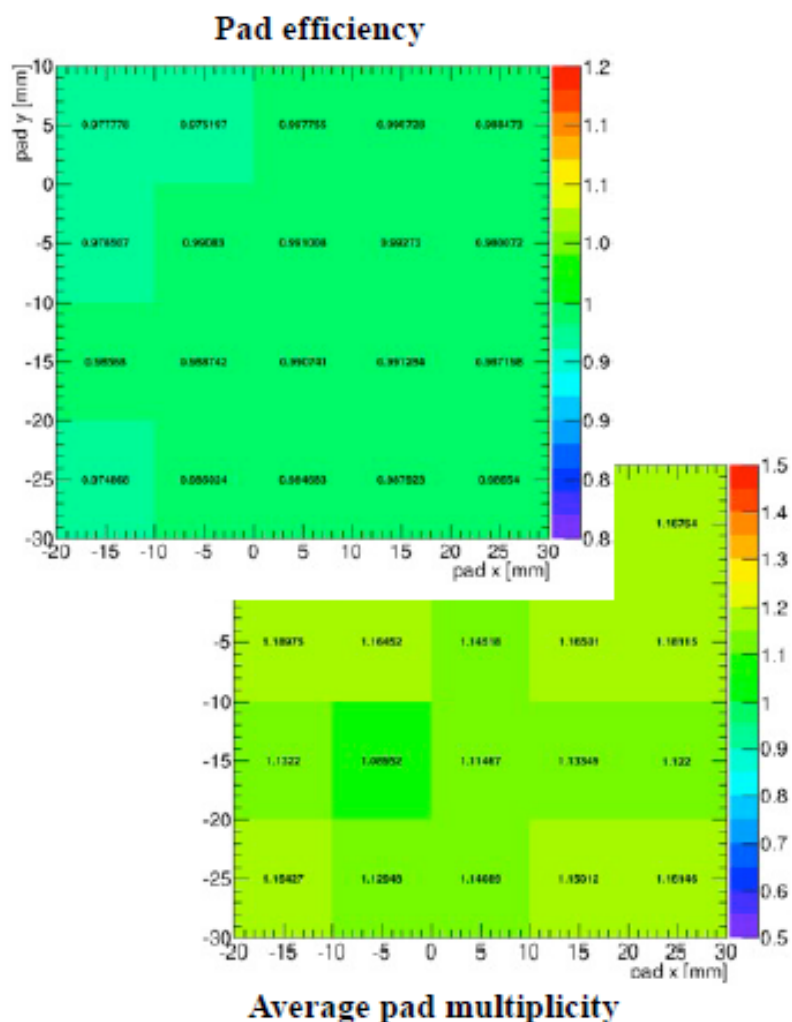
Clear Landau distribution

Excellent signal to noise separation in low and high rate beams



200 ADC counts $\sim Q = 4 \text{ fC} \sim \text{Effective gain} = 3000$

Results - 10 x 10 cm² 0.8 mm RPWELL



High efficiency (>98%) at reasonably low multiplicity (1.1) - preliminary!

Uniform response

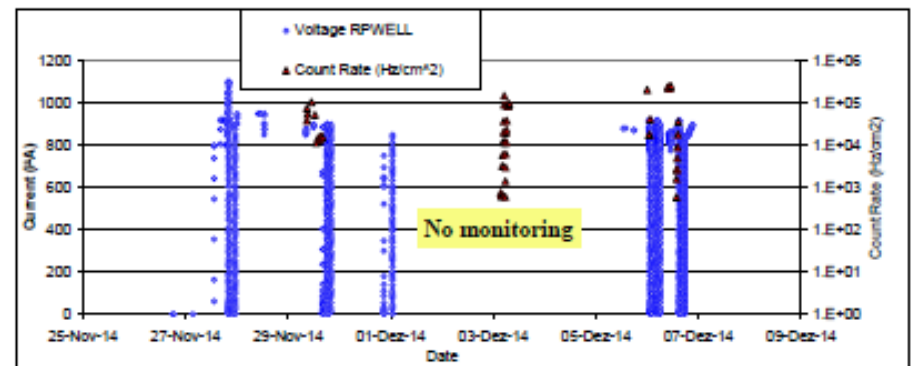
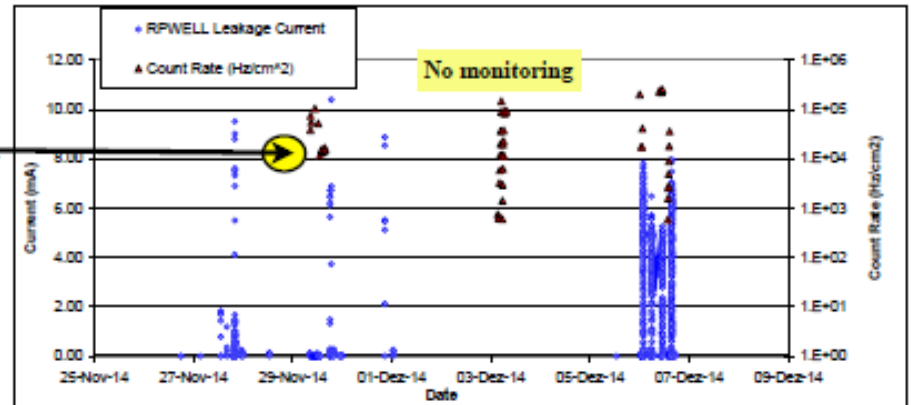
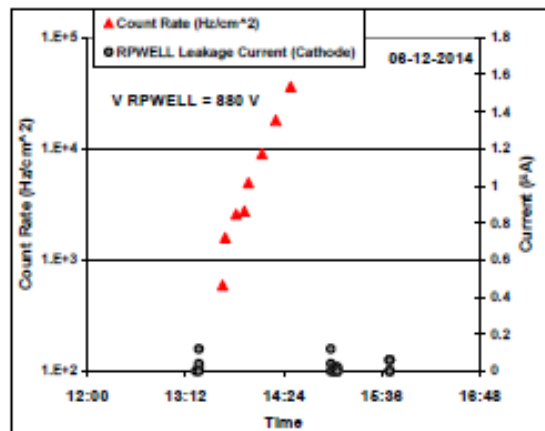
Results - 10 x 10 cm² 0.8 mm RPWELL

Discharge free*!! Discharge prob < 10⁻¹⁰ (first approximation - Preliminary)

Long Pi run. No current activity

Trips - long term high current - occur rarely

- At very high rates and high gains (at the same time)
- Recovery takes time
- Cause has to be understood



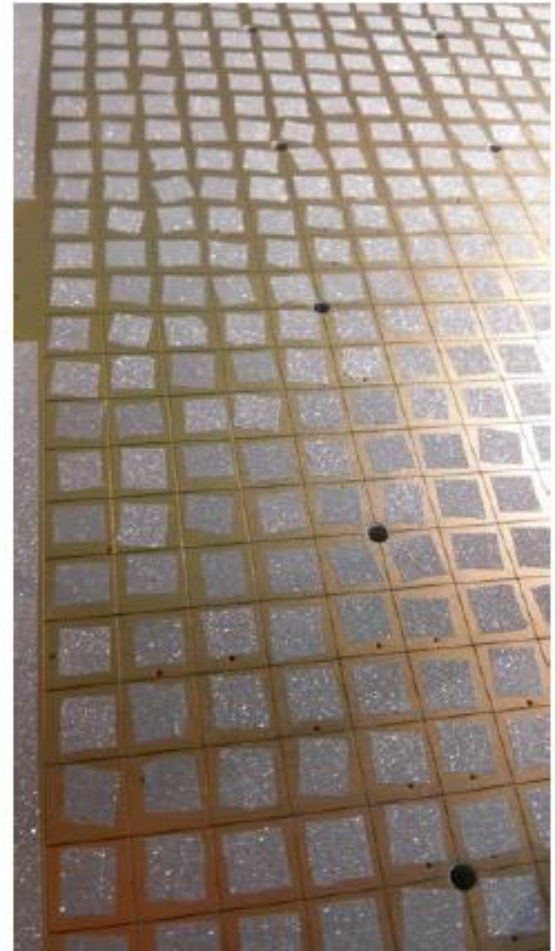
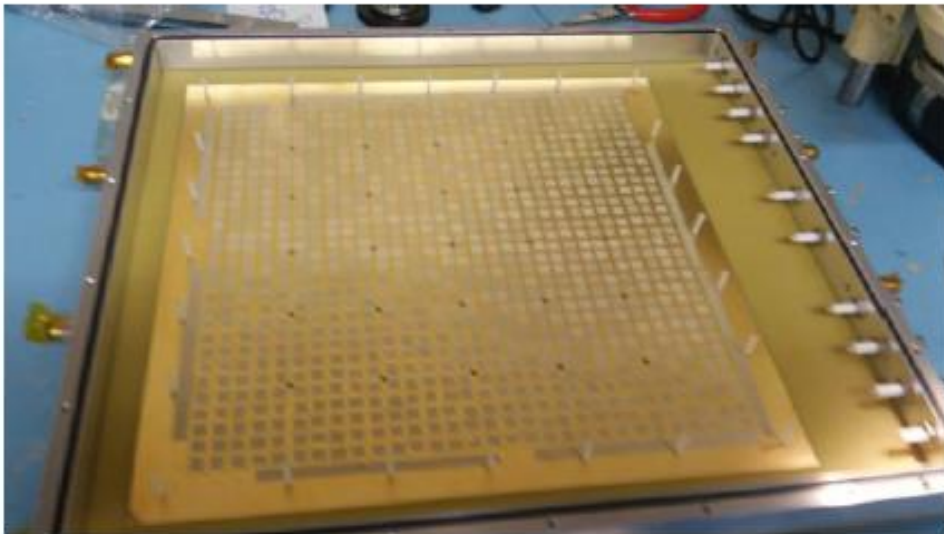
Analysis is on going

Hand made 30 x 30 cm² RPWELL

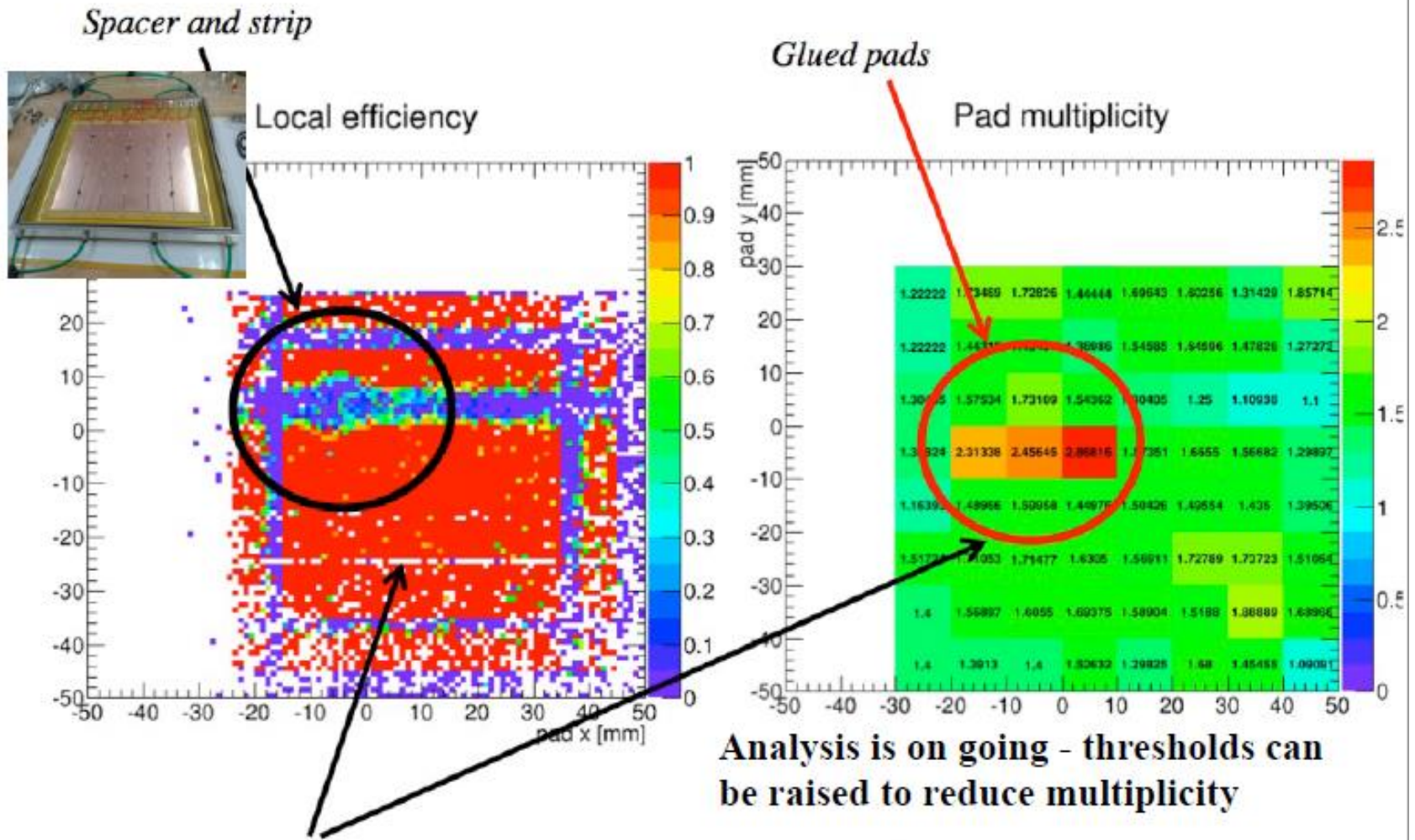
Two RPWELL 0.4 mm Semitron configurations:

- 10 x 10 cm² - 0.8 mm electrode; 3 mm drift
 - Operation in Ne/CH₄ and maybe also Ar/Co₂
- 30 x 30 cm² - 0.4 mm electrode; 5 mm drift
 - Electrode of bad quality (not intentionally)

30 x 30 cm²



Results - 30 x 30 cm² 0.4 mm RPWELL



High pad multiplicity in viducial volume → ~99% eff

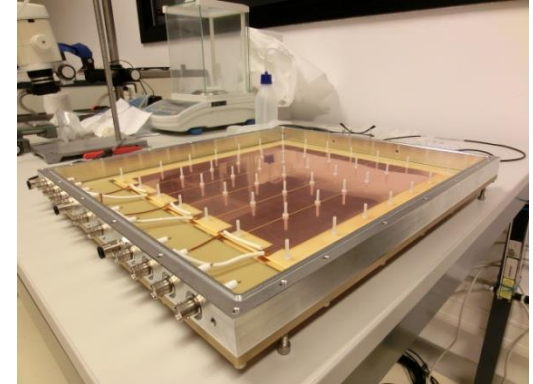
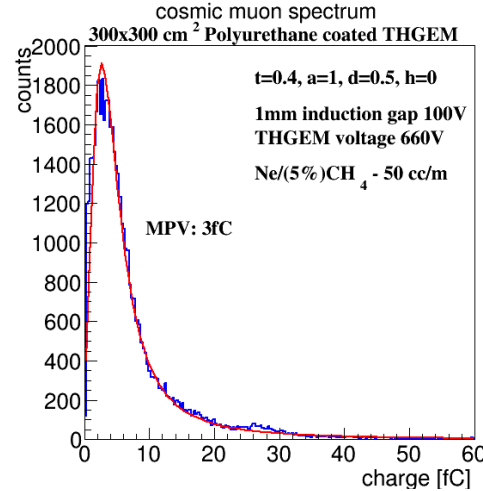
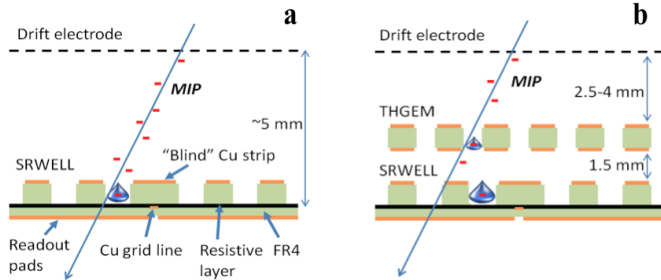
DHCAL – Thick GEM

Summary

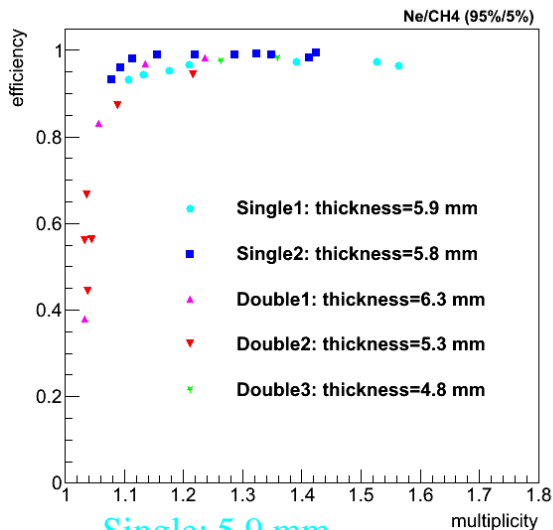
- Three prototypes were tested
 - 0.8 mm thick 10 x 10 cm² RPWELL; 5 mm drift
 - 0.8 mm thick 10 x 10 cm² RPWELL; 3 mm drift
 - 0.4 mm thick 10 x 10 cm² RPWELL; 5 mm drift
- Stable operation at high incoming pion flux
- High efficiency (the detectors can be operated stably at very high gain)
- Low average pad multiplicity
- ~30% gain drop over 4 orders of magnitude of incoming particle flux
- ~10% increase in pad multiplicity at 45 degrees
- The numbers are preliminary!!! Analysis is ongoing.

DHCAL – Thick GEM technology

ThickGEM



The DHCAL requirements were met



Single; 5.9 mm
 Single; 5.8 mm
 Double; 6.3 mm
 Double; 5.3 mm
 Double; 4.8 mm

(Near) future plans

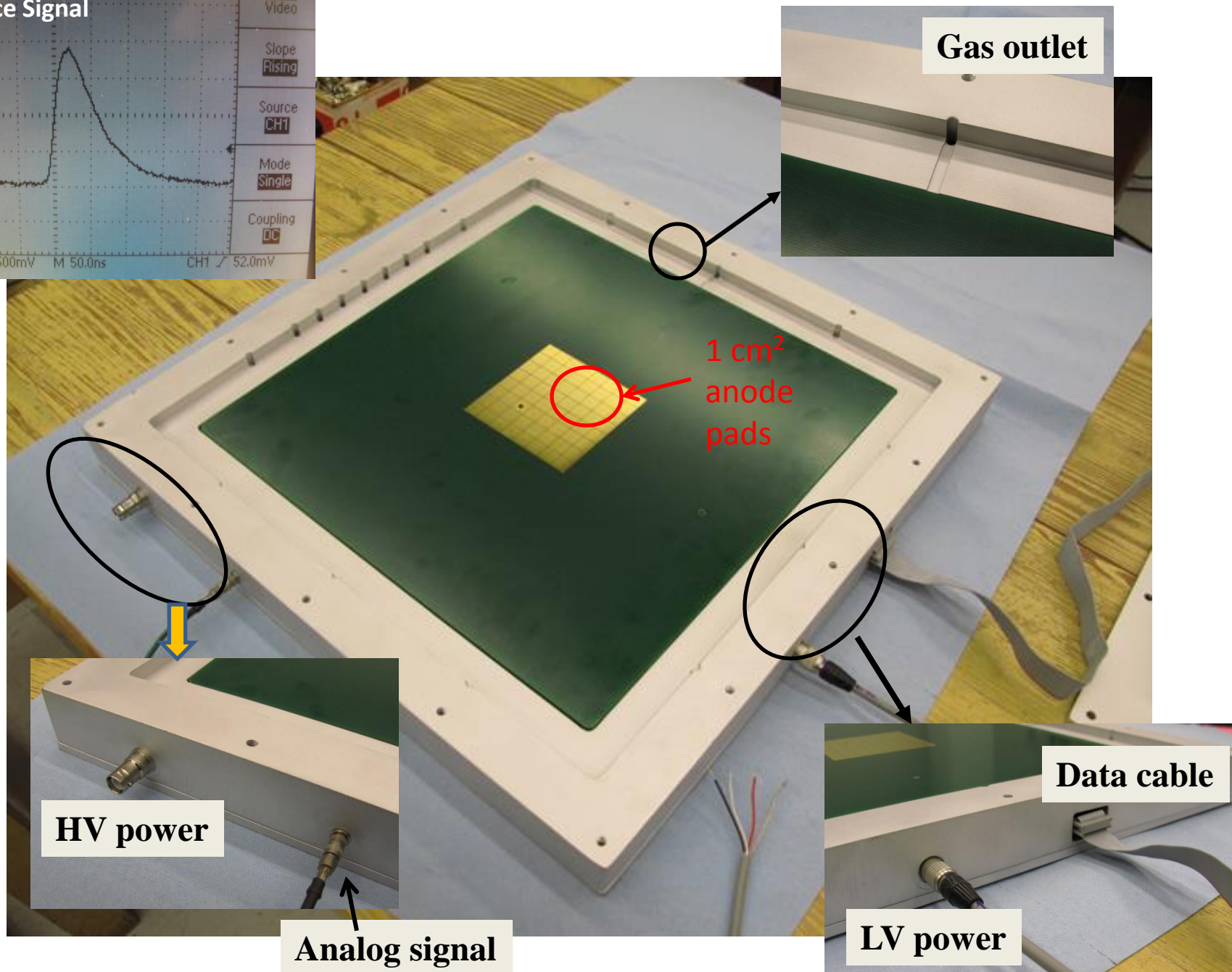
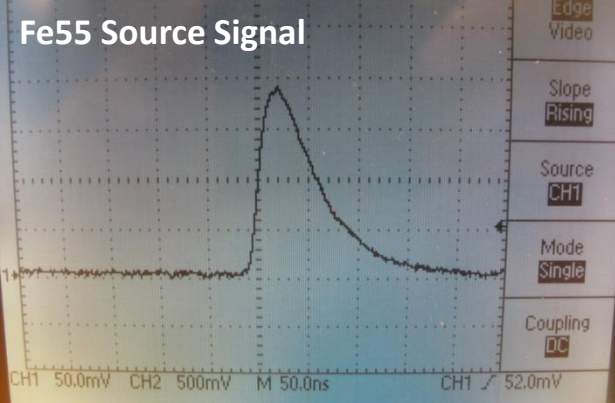
Build meter-scale single-stage detector

- One prototype in collaboration with Israeli industry
- Prototype(s) to test within CALICE's DHCAL module

Study extensively THGEM concepts with broader dynamic range

- RPWELL [Rubin 2013 JINST 8 P11004](#)
- Using newly developed techniques [Bressler 2014 JINST 9 P03005](#)
- Test in beam of 100 x 100 mm² prototype

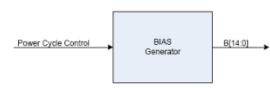
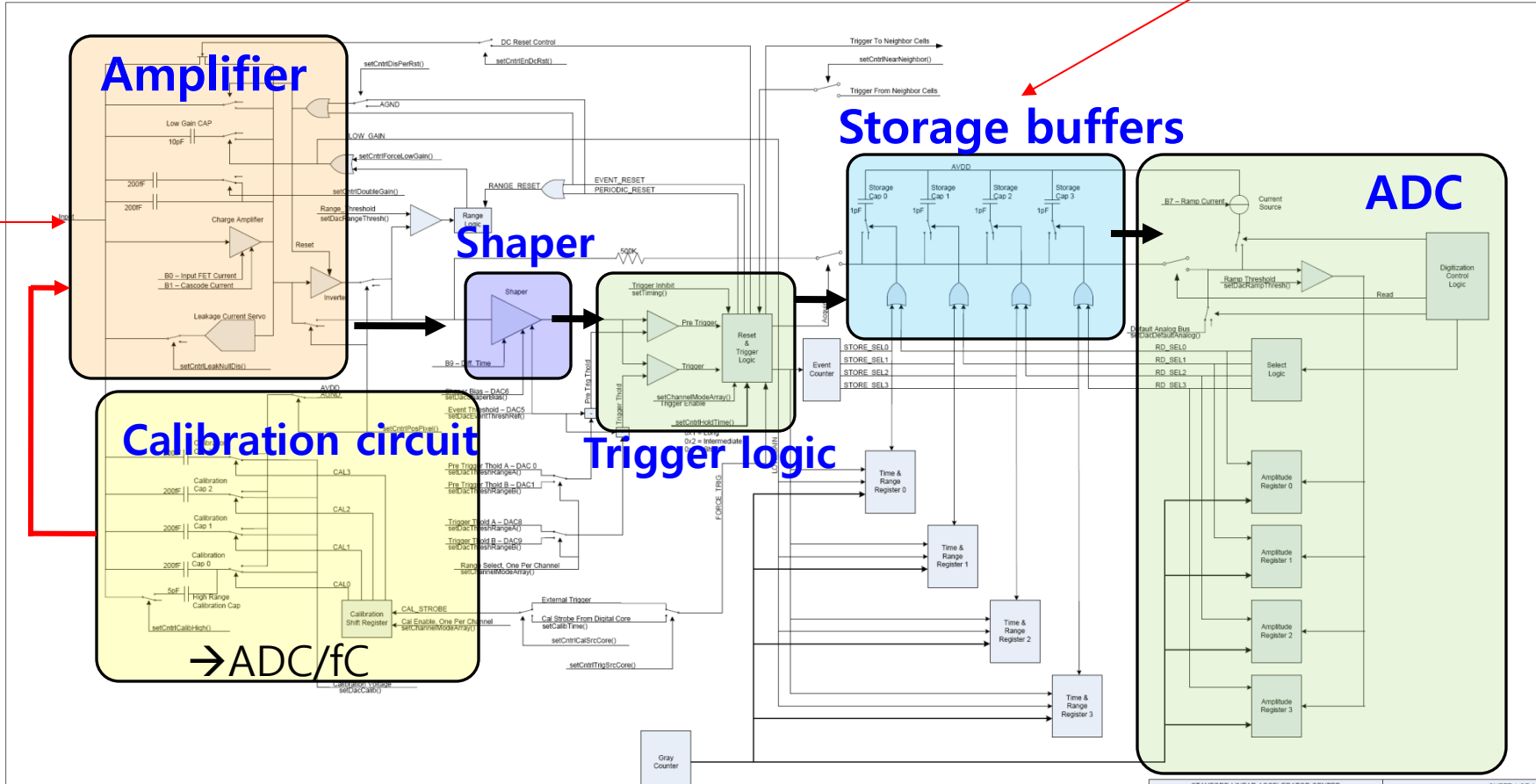
EXTRA



DHCAL
anode
pad

KPiX Readout scheme (SLAC)

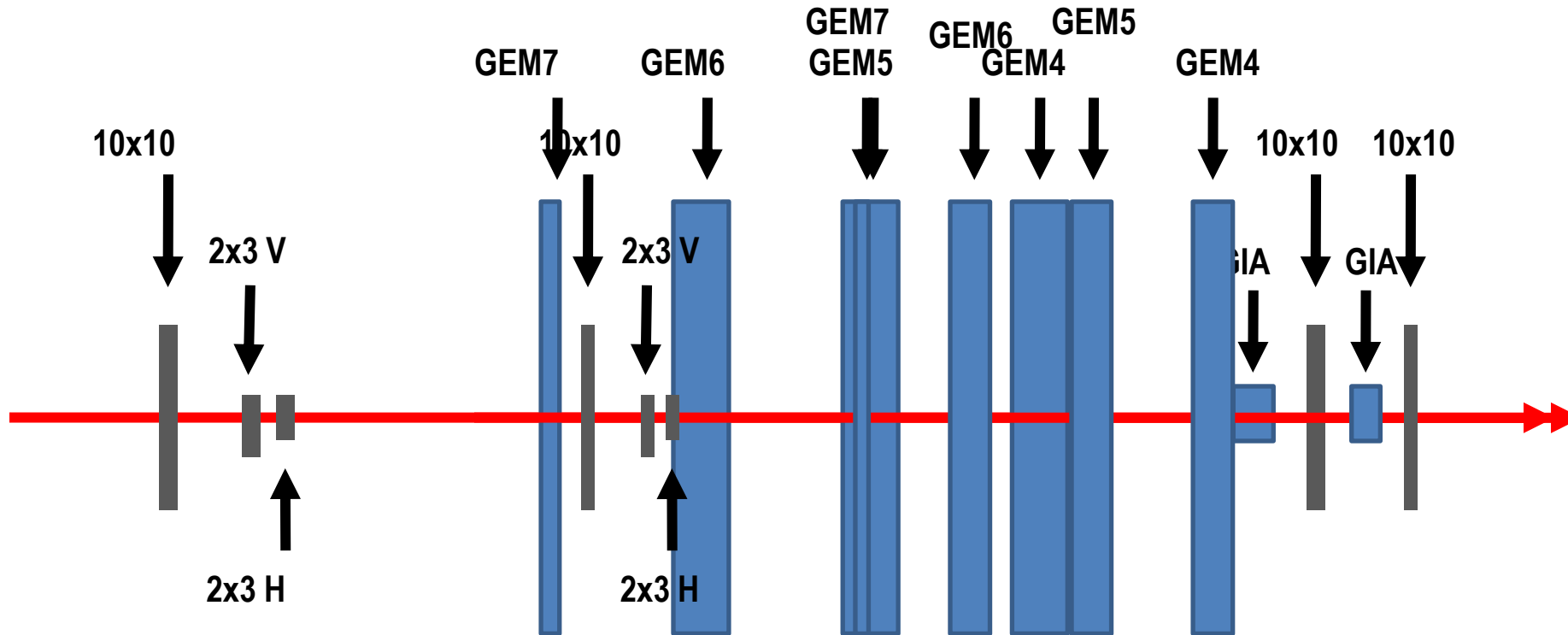
4-deep
"pipeline"



Switches are all shown in their state when control signal is de-asserted (0 Volts)

STANFORD LINEAR ACCELERATOR CENTER U.S. DEPARTMENT OF ENERGY		KPiX ASIC Analog Section High Level Block Diagram
STANFORD UNIVERSITY STANFORD, CALIFORNIA		
Proprietary data of Stanford University and/or U.S. Department of Energy. Recipient shall not publish the information within unless granted specific permission of Stanford University.		
ENGR Ryan Herbst	DATE 06-03-2008	APPROVALS
DFTR Ryan Herbst	DATE 06-03-2008	
CHKR		
		Rev 4.1 - 06/03/2008

T-1010 Experiment Setup



GEM6: Read out by 13bit KPiX designed for the ILC time line

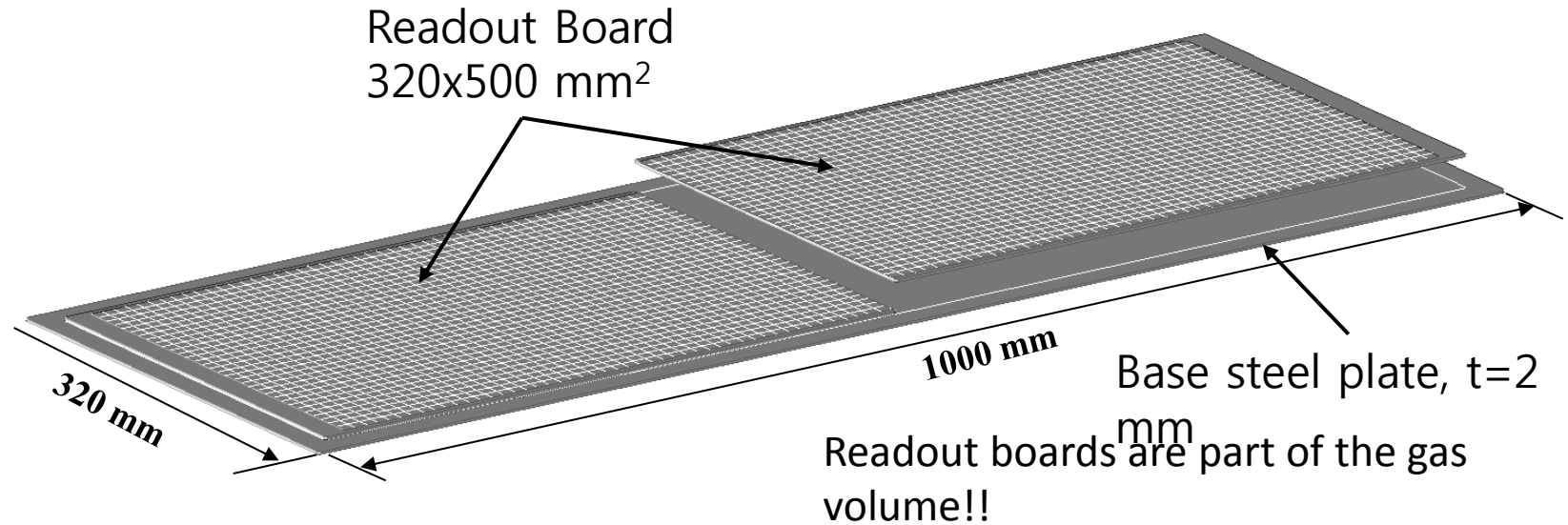
GEM7, GEM5, GEM4: Read out by 1bit DCAL chip by ANL and FNAL

GIA: Medical image intensifier prototype with 12 bit ADC in-house readout

Triggers formed off the motion table:

1. 10×10 coincidences for guaranteed beam penetration through the detector array
2. 2×3 coincidences arranged perpendicular to each other for 2×2 coverage in the center of the detector array
3. Coincidence of 1×2 : Guaranteed beam penetration with center 2×2 coverage (efficiency $\sim 95\%$)

33cmx100cm DHCAL Unit Chamber Construction



GEM foils with 31 HV strips delivered
2mm steel strong-back + thin cathode layer

3mm

G10 spacers will be used without aligned dead areas.

1mm

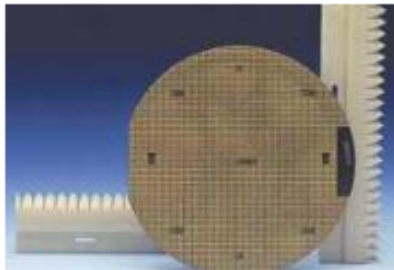
Readout boards will be glued in the seam

1mm

1mm pad board

2mm FE board

1mm assistive strong back



Links:

- [Semitron ESd 225 Data Sheet](#)
- [Semitron 410C - Conductive Ultem](#)
- [Semitron ESd 420 - ESD Ultem](#)
- [Semitron ESd 480 - ESD PEEK](#)
- [Semitron ESd 500HR - ESD PTFE](#)
- [Semitron ESd 520HR - ESD PAI](#)
- [Semitron ESd 225 Acetal](#)

Semitron® ESd 225 - Acetal

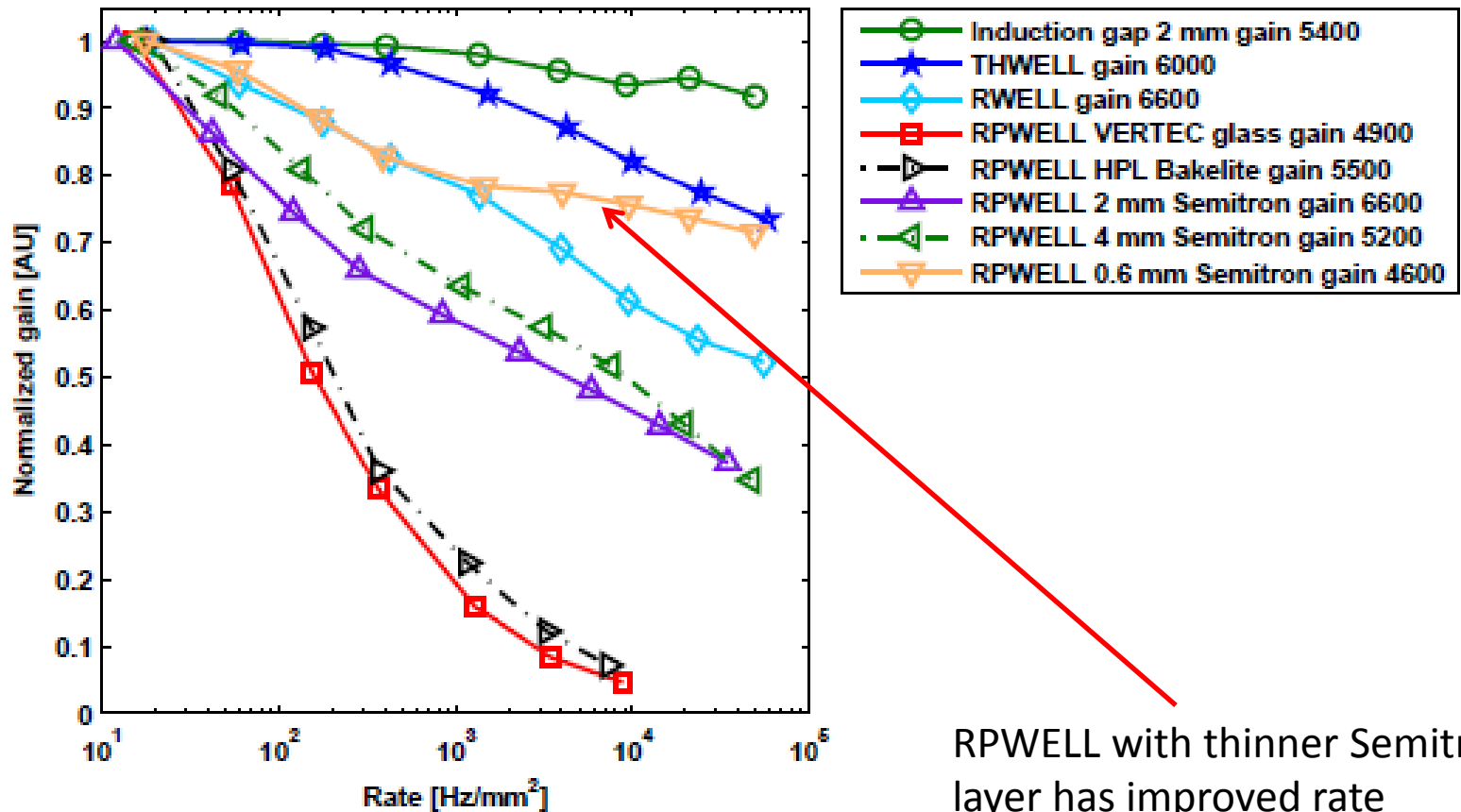
Semitron® ESd 225 Acetal Sheets & Rods

Semitron® ESd products are inherently dissipative and electrically stable unlike many other "dissipative" plastic shapes. They do not rely on atmospheric phenomena to activate, nor are surface treatments used to achieve dissipation. Static electricity is dissipated through these products as readily as it is dissipated along the surface. All of these products dissipate 5 KV in less than 2 seconds per Mil-B-81705C.

- Surface Resistivity of 10^9 to 10^{10} Ohms per square
- Note: Maximum Operating Temperature is 190 F (82 C)
- For higher operating temperatures, consider other materials in the Semitron family. (links on left-side of page)

FEATURES AND BENEFITS

Semitron® ESd 225 is ideal for fixturing used in the manufacturing of hard disk drives or for handling in-process silicon wafers.



RPWELL with thinner Semitron layer has improved rate performance