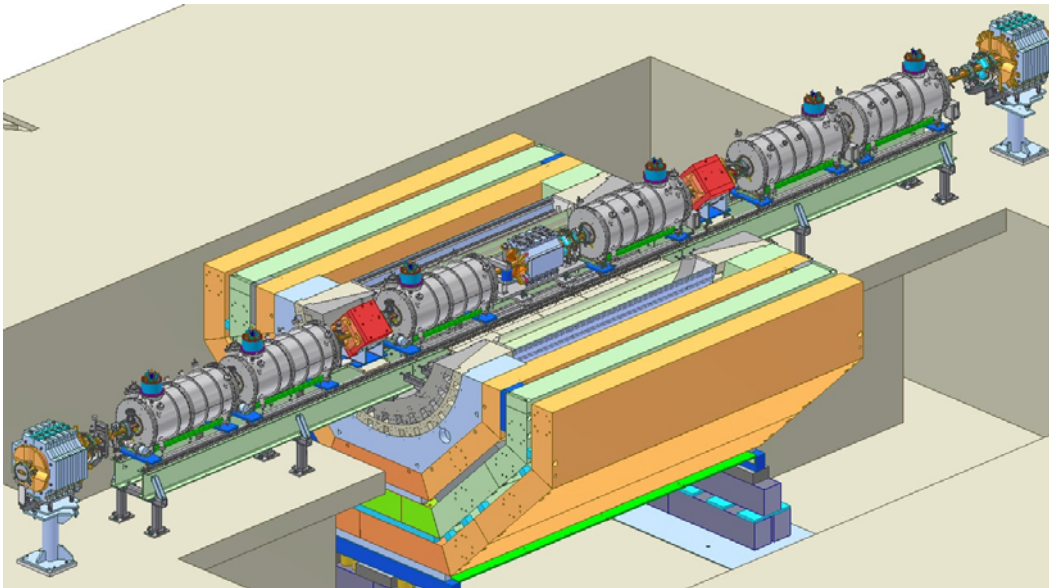


ILC / GDE Report



Electron Cloud at CEsrTA

Barry Barish
TILC09 – Tsukuba, Japan
17-April-09



What are we doing?

- Updated version R&D Plan: Plug Compatibility concept fleshed-out; SCRF test facilities;
- R&D Demonstrations – Progress on CsrTA (electron cloud); ATF-2 (final focus); and SCRF cavity gradient
- We are beginning the process of a cost-performance optimization of the design leading toward “re-baselining”
- Developing a Project Implementation Plan
 - Governance study; Siting activity & strategy



- 3

- The document has two parts:
 - **A summary of the primary goals and schedules for the Technical Design Phases (TDP-1 and TDP-2)**
 - **Appendices which contain detailed information on world-wide resources and the complete project work-package structure**



Major Milestones for TDP 1

SCRF

- High Gradient R&D - globally coordinated program to demonstrate gradient by 2010 with 50% yield;

ATF-2 at KEK

- Demonstrate Fast Kicker performance and Final Focus Design

Electron Cloud Mitigation – (CesrTA)

- Electron Cloud tests at Cornell to establish mitigation and verify one damping ring is sufficient.

Minimum Machine Studies (Cost/Performance)

- Studies of possible cost reduction designs and strategies for consideration in a re-baseline in 2010

The ILC SCRF Cavity



Figure 1.2-1: A TESLA nine-cell 1.3 GHz superconducting niobium cavity.

- Achieve high gradient (35MV/m); develop multiple vendors; make cost effective, etc
- Focus is on high gradient; production yields; cryogenic losses; radiation; system performance



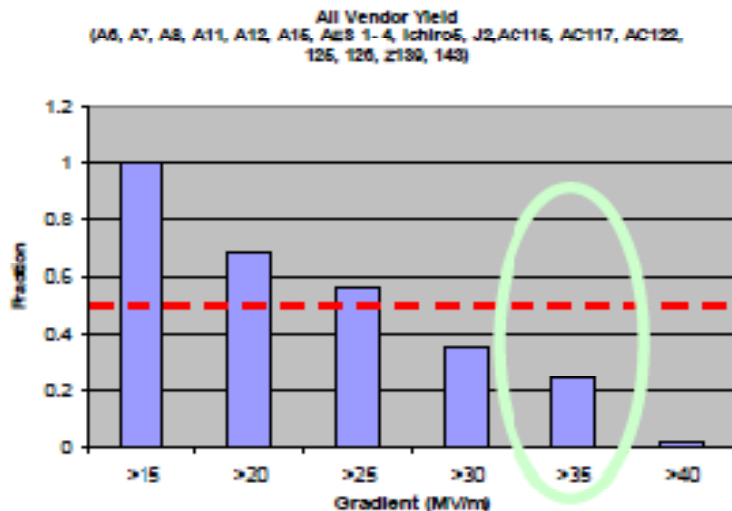
Status of 9-Cell Cavity R&D

48 Tests, 19 cavities

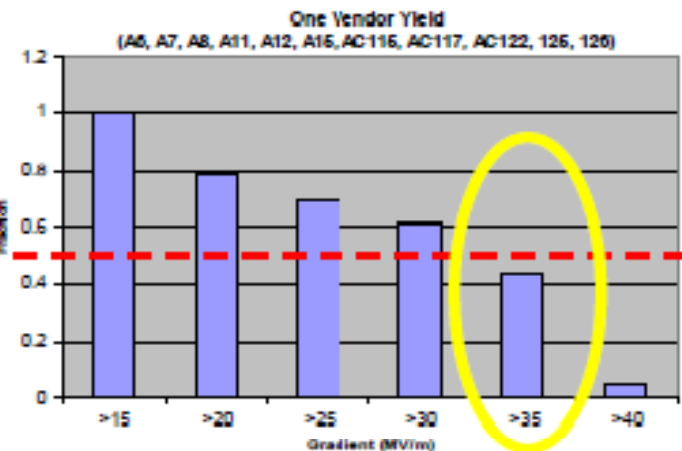
ACCEL, AES, Zanon, Ichiro, Jlab

23 tests, 11 cavities

One Vendor



50%

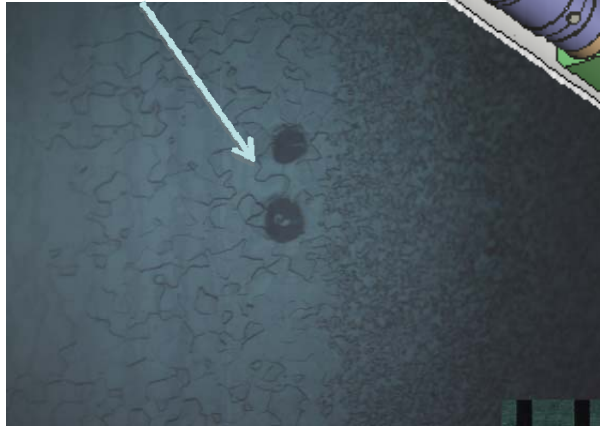
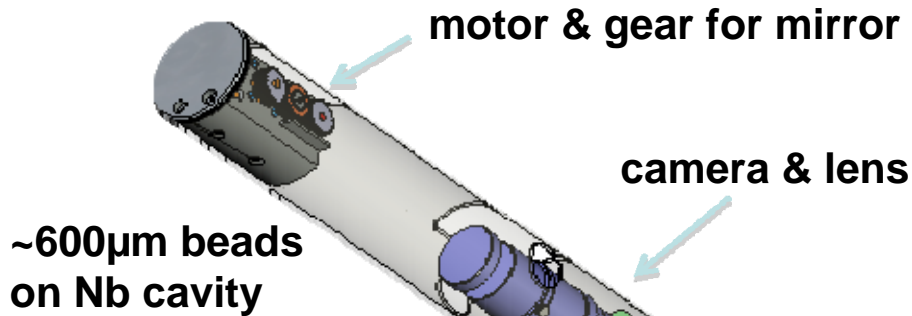


Yield **45 %** at **35 MV/m** being achieved
by cavities with a qualified vendor !!

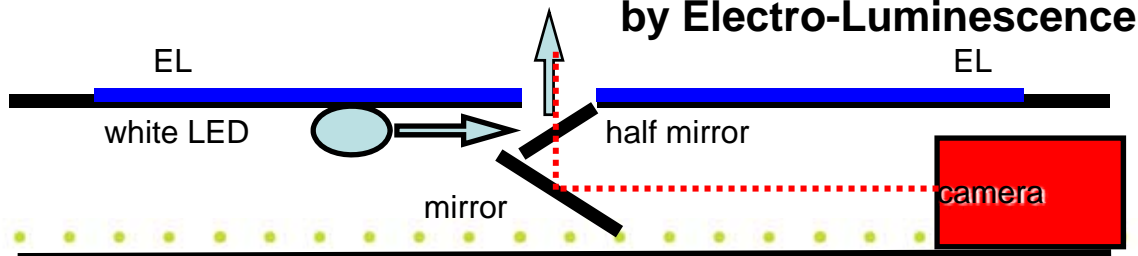


Diagnostics -- Optical Inspection

For visual inspection of cavity inner surface.



perpendicular illumination by LED & half mirror



Camera system (7µm/pix) in 50mm diameter pipe.

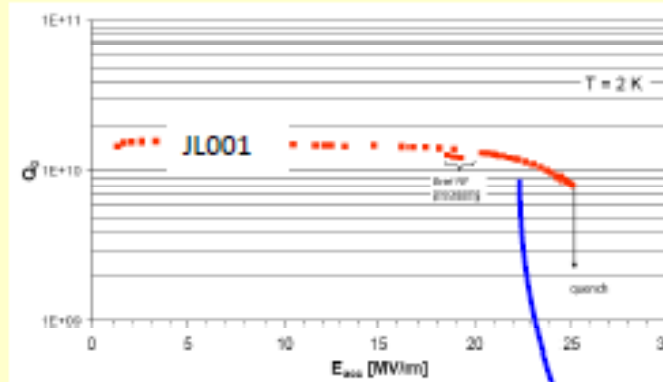
sliding mechanism of camera

DESY starting to use this system in cooperation with KEK

Thermometry for Local Hotspots

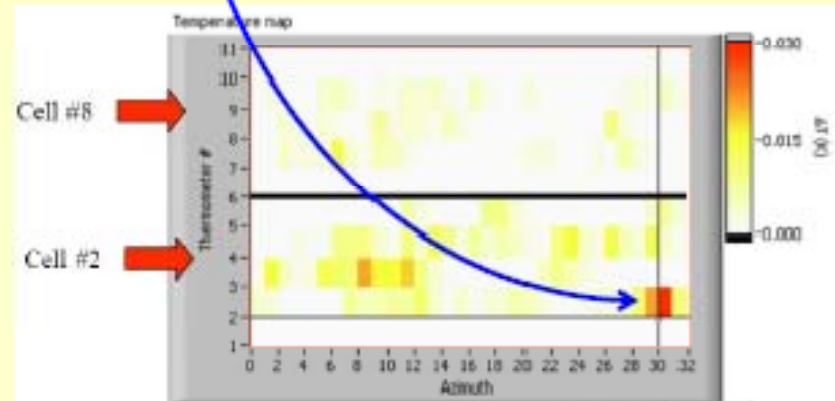


A 2-cell thermometry system for ILC 9-cell cavity was designed, built and commissioned at JLab to study defects



“Hot-spots” were detected near the equator weld of one of the cells, causing the Q-drop

JLab



Cavity in
cryogen
tank



Eight in
a string



Hang string
from support
tube



Slide into
cryostat



Completed Cryomodule in Fermilab ICB,
November 2007

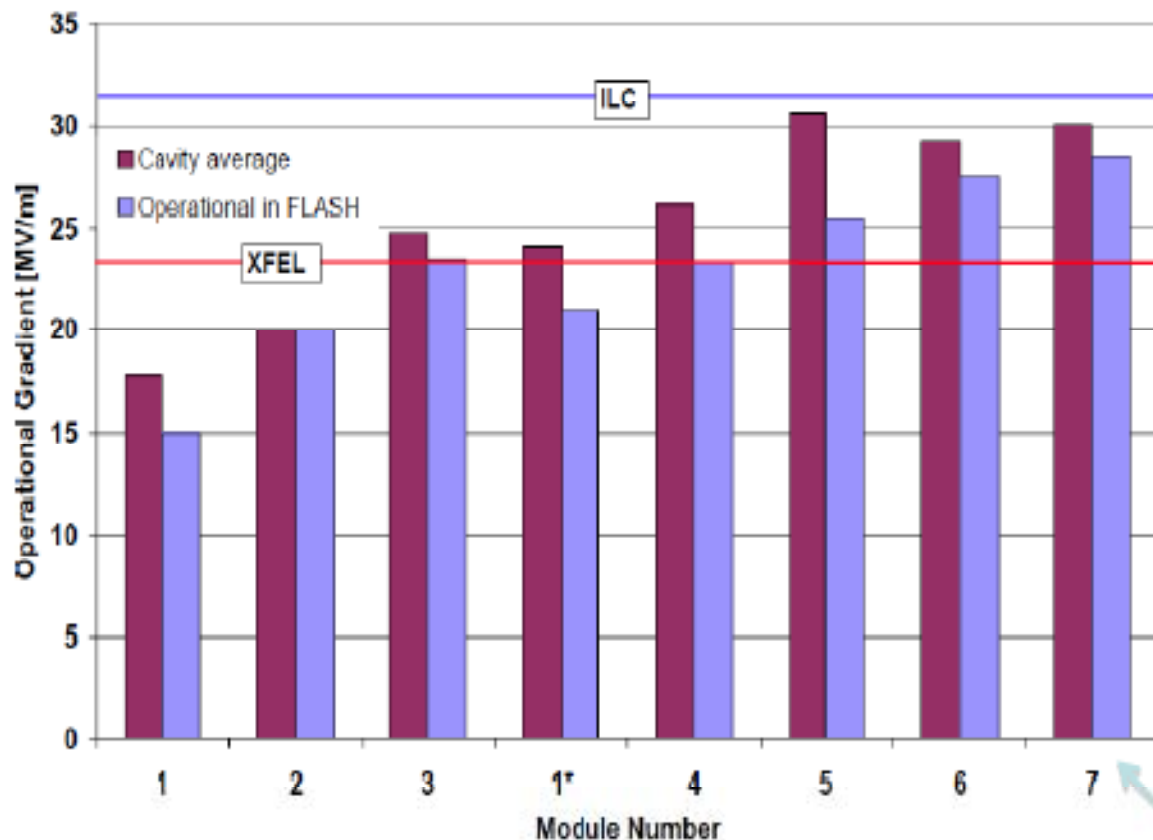


Cryomodule Assembly

Plug Compatible Approach

<u>Cryomodule costs (RDR)</u>	<u>fraction</u>	<u>sum</u>
Cavity Fabrication	36%	36%
Power Couplers	10%	46%
Helium Vessel Fabrication	8%	54%
Magnetic Package (Quad)	7%	61%
Tuners	7%	68%
Assembly, Testing, Transport	5%	72%
 (Next 7 items – to 1% level (22%)– Vacuum vessel, shields, interconnect, processing, dressing, pipes, supports, instrumentation)		

Cryomodule Gradient Progress



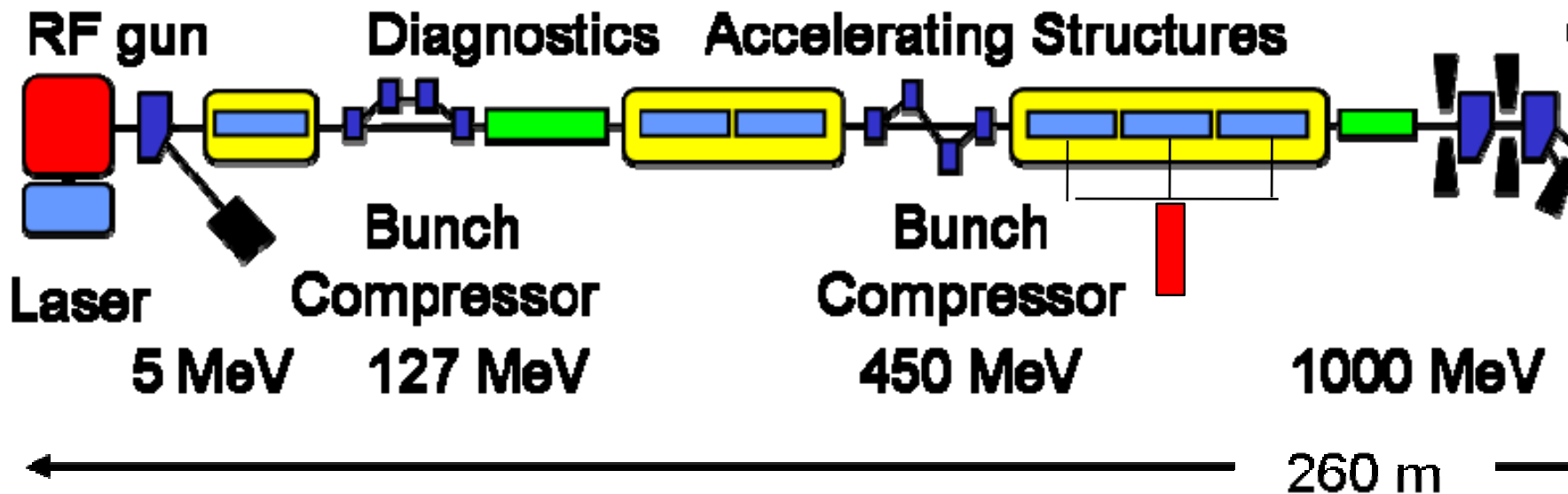
ILC operation :



- $\langle 31.5 \rangle$ MV/m spec
- (27 MV/m achieved at DESY/FLASH)
- (29 MV/m achieved DESY test stand)

• 20 % improvement required for ILC



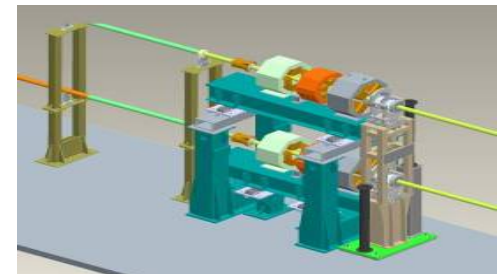
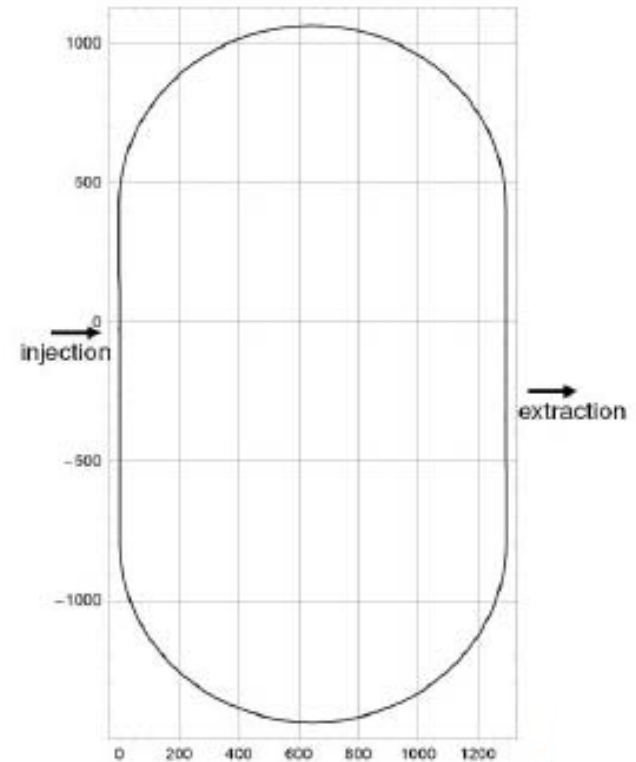
Beam Tests



				FLASH design	FLASH experiment
Bunch charge	nC	1	3.2	1	3
# bunches		3250*	2625	7200*	2400
Pulse length	μ s	650	970	800	800
Current	mA	5	9	9	9

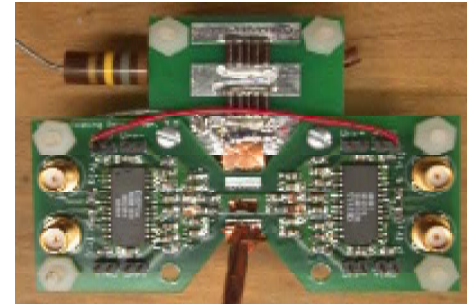
Damping Ring R&D

- DR has a flexible race track design
 - 6.4 km Circumference with >1 km straights, which contain, RF, Wigglers, Chicanes, Injection/ Extraction Systems
- There are two critical components which require a successful demonstration in TDP1
 - Fast Inj/Ext Kickers
 - Suppression of e- Cloud in the e+ ring

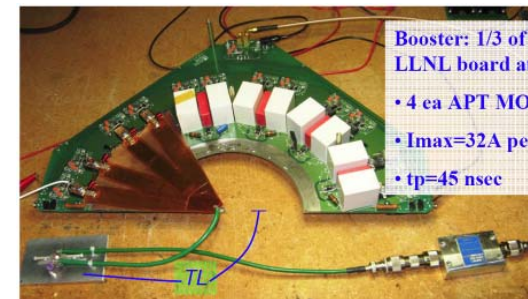


Fast Kicker R&D Program

- There are presently four strands to the R&D program:
 - **SLAC/LLNL**: Development of fast high-power pulsers based on MOSFET technology.
 - **SLAC/DTI**: Development of fast high-power pulsers based on DSRD (drift step recovery diode) technology.
 - **INFN-LNF**: Tests of fast kickers in DAΦNE.
 - **KEK**: Tests of fast kickers in the ATF.



Tests of MOSFET-based pulser show promising performance.

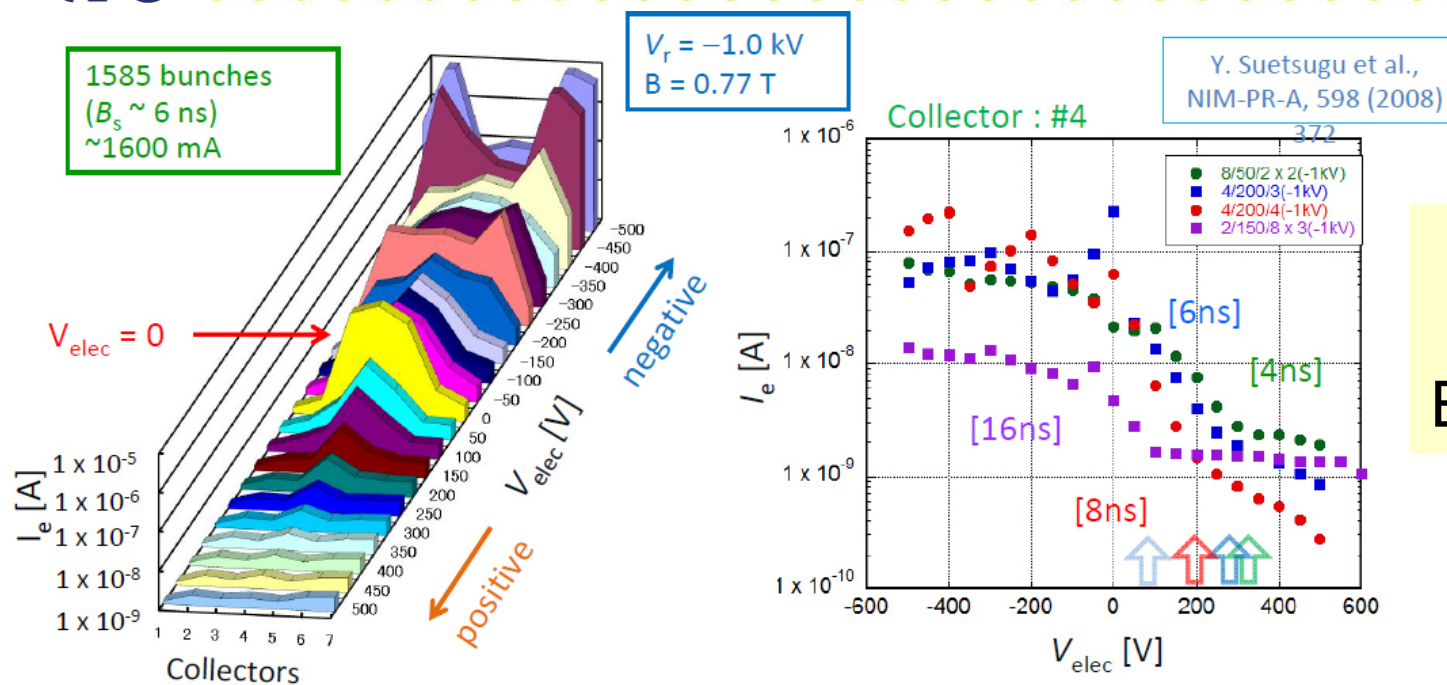


Booster: 1/3 of modified LLNL board at 300V
 • 4 ea APT MOSFETs
 • $I_{max}=32A$ per MOSFET
 • $t_p=45$ nsec

Tests of DSRD-based pulser using board based on LLNL design (for MOSFET inductive adder). Performance is limited by board design and components.

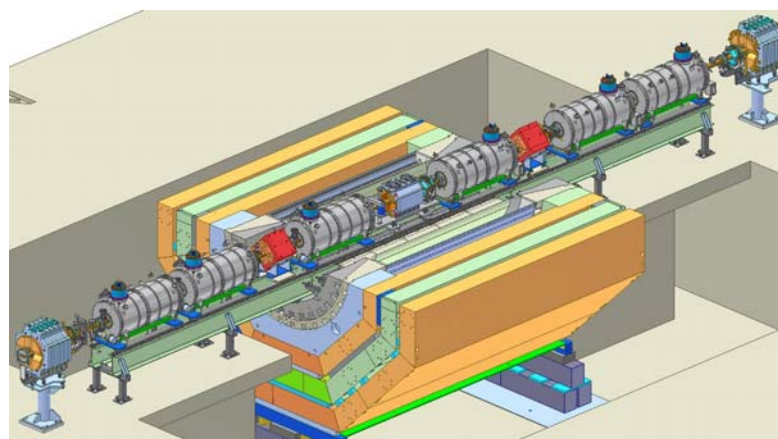
Electron cloud – Goal

- In electron or proton storage rings, low energy electrons are accelerated by the high energy beam into the wall of the vacuum chamber where more electrons are emitted leading to the formation of an electron cloud.
- For ILC damping ring, need to ensure the e- cloud won't blow up the e+ beam emittance.
 - **Studied through simulations**
 - **Test vacuum pipe coatings, grooved chambers, and clearing electrodes effect on e- cloud buildup**
 - **Do above in ILC style wigglers with low emittance beam to minimize the extrapolation to the ILC.**
 - **Test program is underway at CESR Cornell (CesrTA)**



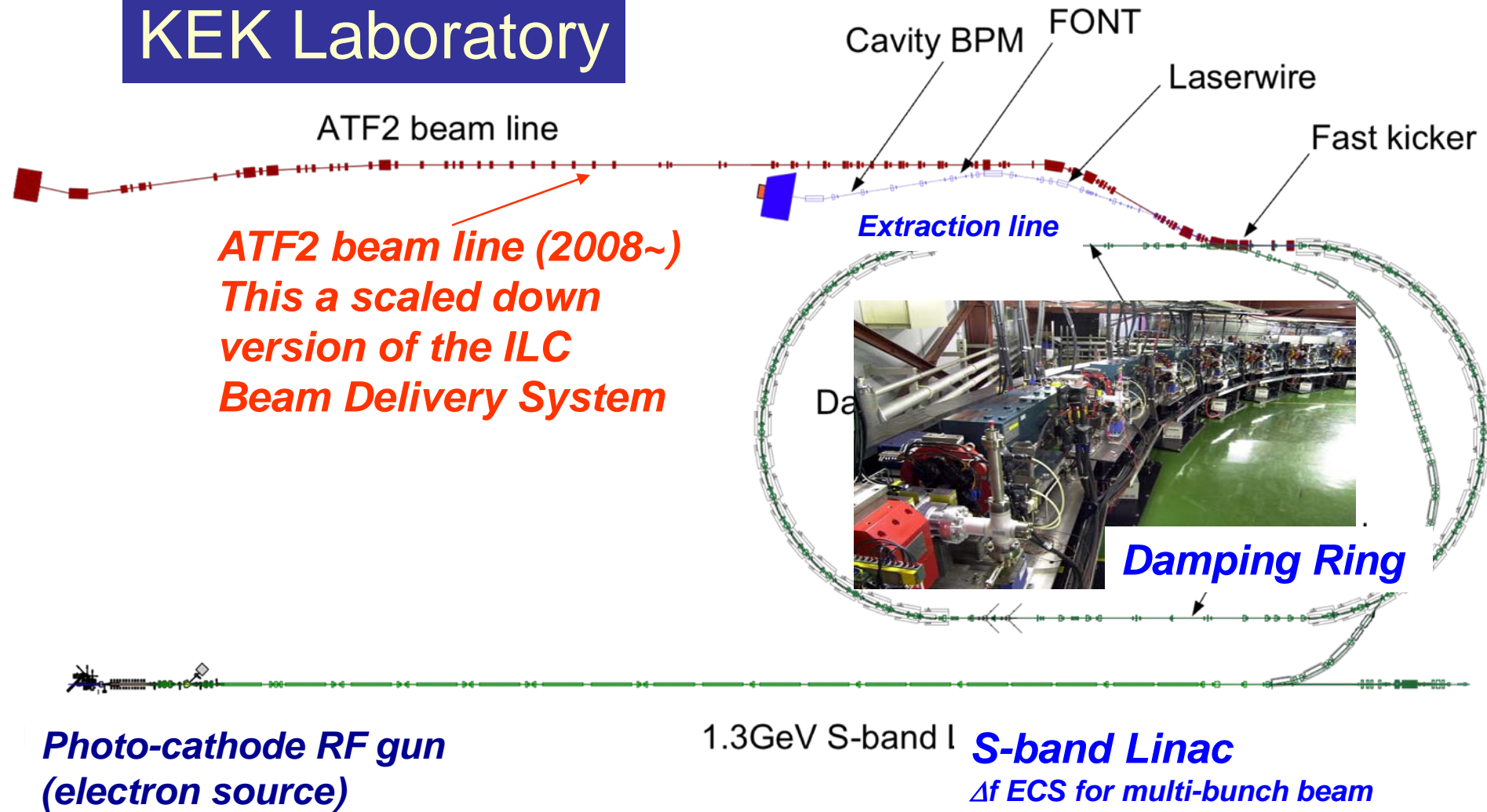
KEK-B
Clearing
Electrodes

CESR reconfigured to have 12 damping wigglers located in zero dispersion regions for ultra low emittance operation.



Accelerator Test Facility – ATF/ATF2

KEK Laboratory



ATF / ATF2 R&D Program and Goals

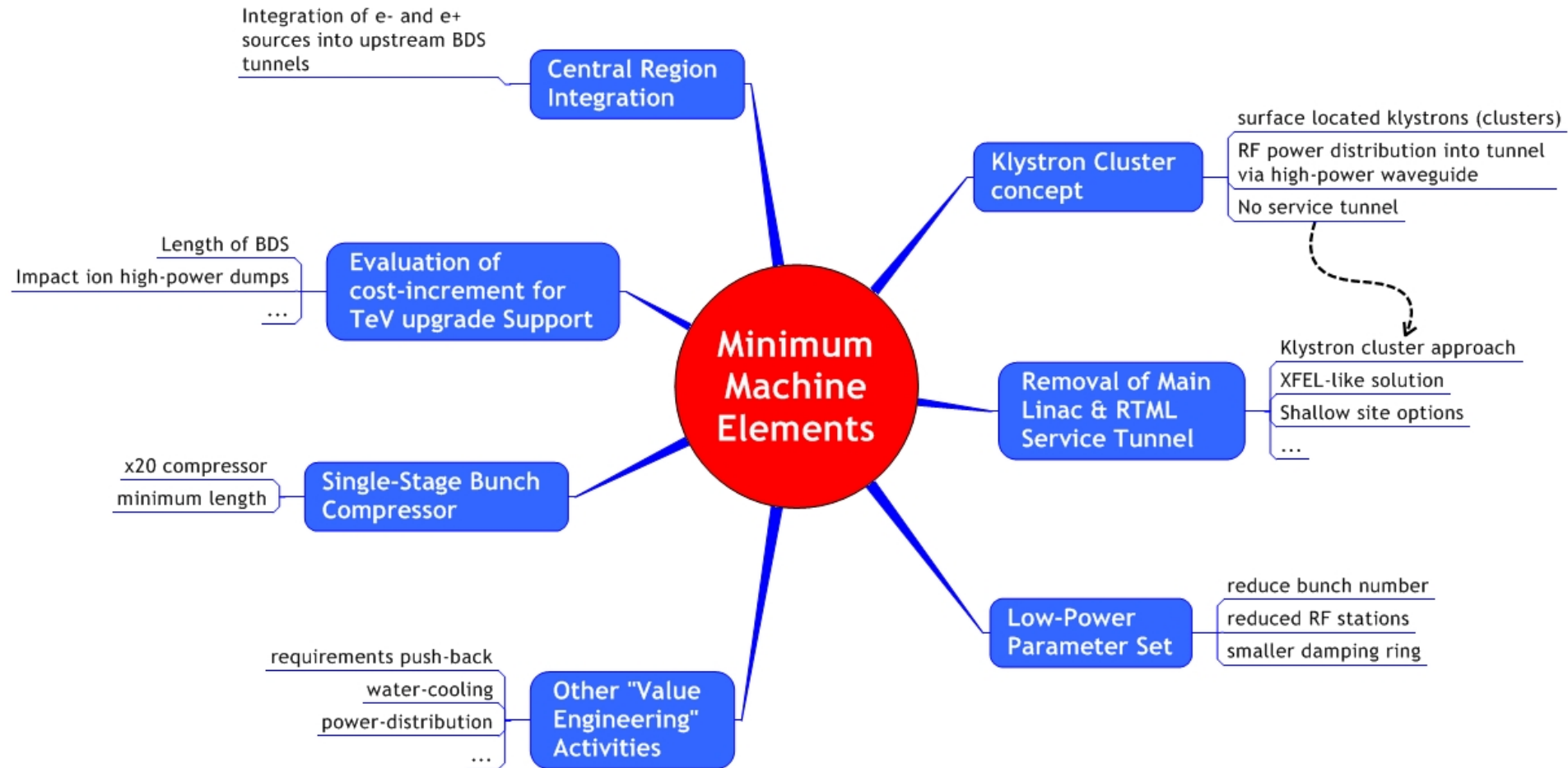
- Beam delivery system studies
 - Demonstrate ~ 50 nm beam spot by 2010
 - Stabilize final focus by 2012
- Broad international collaboration (mini-ILC) for equipment, commissioning and R&D program



ATF2 Beam Line vacuum pipe connected in October

Commissioning underway

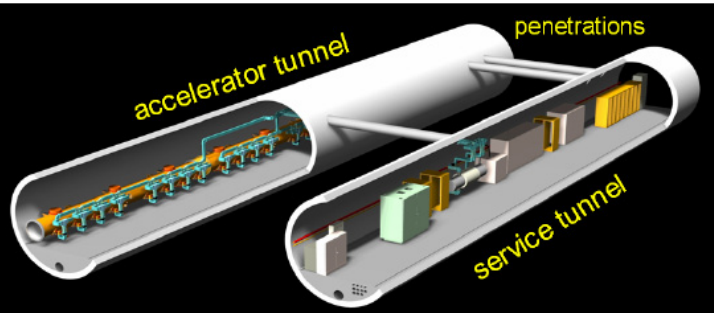
Identified Minimum Machine Elements



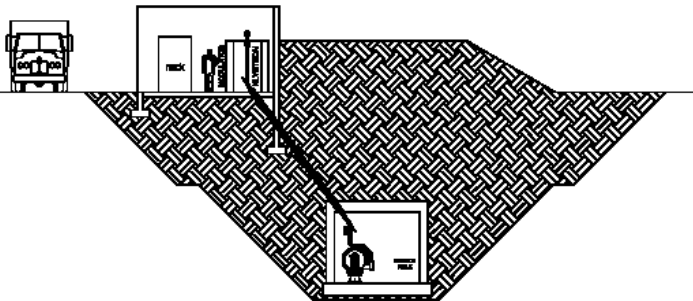
Towards a New Baseline

- “Minimum Machine” refers to a set of identified options (*elements*) which may simplify the design and be cost-effective
 1. **Klystron Cluster concept**
 2. **Central region integration**
 3. **Low beam power option**
 4. **Single-stage compressor**
 5. **Quantify cost of TeV upgrade support**
 6. **“Value engineering”**
 7. **Single-tunnel solution(s)**

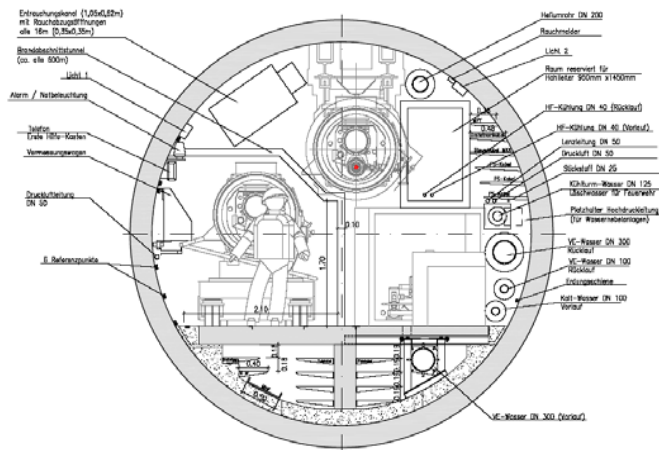
Main Linac & Support Tunnel



- RDR (two-tunnel)
 - Access to equipment during ops
 - Reliability/availability



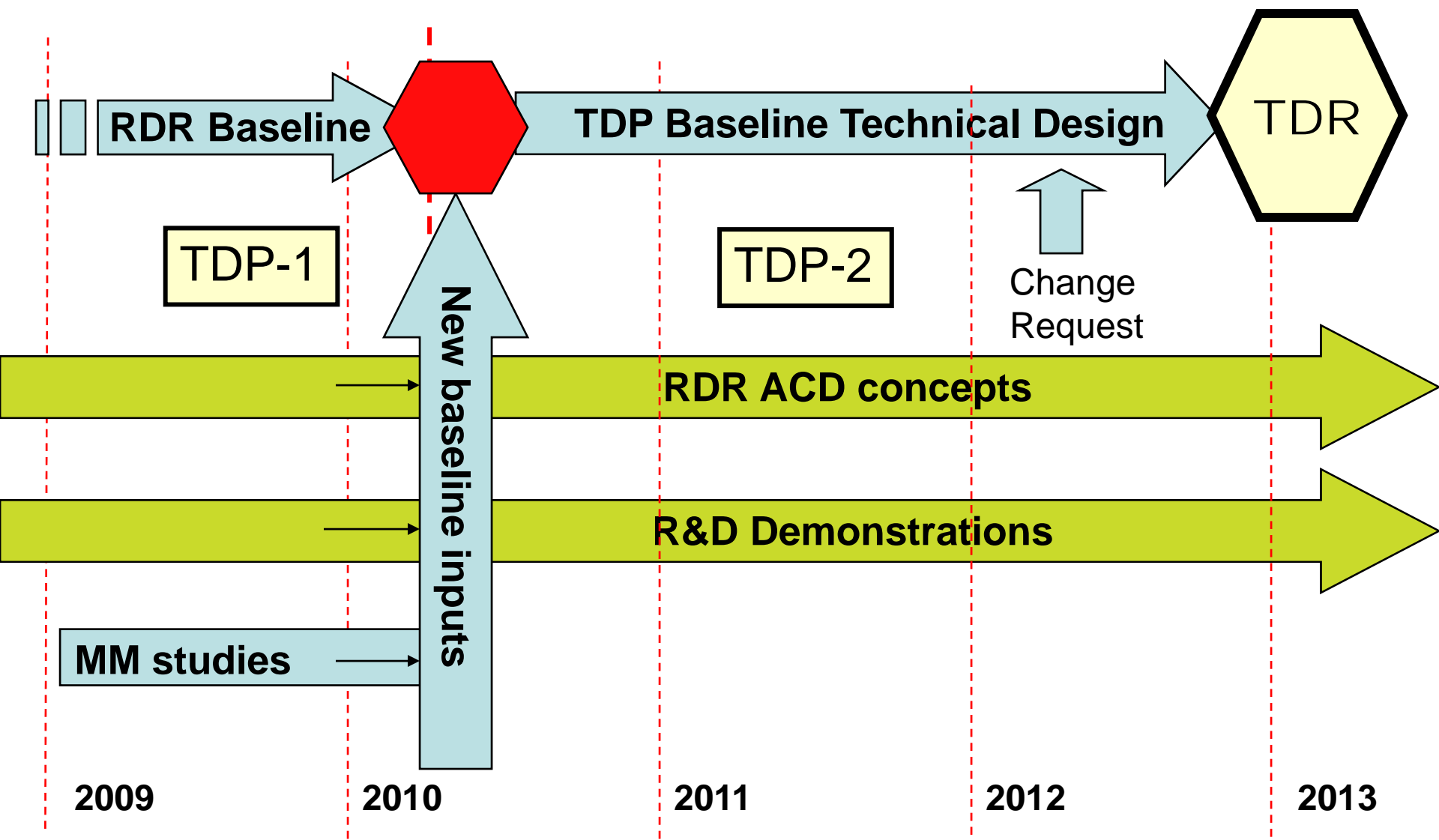
- Shallow sites
 - Cut and cover like solutions
 - “service tunnel” on the surface



- Single tunnel
 - European XFEL-like solution
 - availability / reliability



Technical Design Phase and Beyond





Technical Design Report

- What will it be?
 - **Cost performance optimized technical design**
 - **It will include new “value” estimate**
 - **It will include a project implementation plan**
- Who will it be for?
 - **It will be a detailed design and project plan ready for serious consideration by potential collaborating governments.**
- What it will not be?
 - **It will not be a complete engineering design with drawings, etc.**

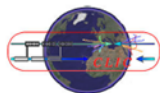
Technical Design Report

- What about LHC results?
 - The LHC will need to be a success technically (energy, luminosity and duty cycle)
 - LHC science will need to ‘validate’ the science case.
- Who about CLIC?
 - Joint work with CLIC will help make technical, cost and readiness comparisons possible, if needed.
 - Will LHC points to a ~ 1 TeV machine?
- Will our job be done?
 - Continuing R&D demonstrations, ADC and industrialization
 - Funding could become more difficult, without a potential project is in sight.



Technical Reviews

- Accelerator Advisory Panel (Willis & Elsen)
 - On-going reviews by assigned AAP members to particular systems (attend meetings, etc)
Example result: Questions regarding plug compatibility have resulted in studies, report
 - Technical Review – first one 3.5 days at TILC09 in April. Internal + 4-5 external reviewers. Yearly through TDP phase with continuity. First review: Overall coverage + focus areas
- ILCSC PAC Review:
 - 1.5 days (1 day GDE); higher level review and will use AAP review as input.



CLIC / ILC Joint Statements
27 October 2008



Purpose of these statements:

The CLIC and ILC Collaborations agree to work together, within the framework of the CLIC / ILC Collaboration, to outline comparative statements to be used in presenting their respective projects. The Collaboration members agree to limit statements made about each other's projects to specifically agreed upon statements such as those listed below:

• **Project design**

The CLIC and ILC projects both plan to release design documents in the coming years. The CLIC Conceptual Design Report is to be published in 2010. If the CLIC technology is demonstrated to be feasible, a CLIC Technical Design will then be launched for publication in a CLIC TDR by 2015. The ILC TDR will be published in 2012. The design reports are intended to summarize the R&D and project planning at that time and will serve as indicators of project readiness. Both TDRs are intended to be submitted to governments and associated funding agencies in order to seek project approval.

• **Test facilities and system tests**

The CLIC and ILC projects both have test facilities either in operation or under construction for the purpose of demonstrating the performance of key technical components or to allow system engineering and industrialization. For each project, R&D priorities and schedules have been defined and it is anticipated that milestones and progress will be reviewed and reported on by members of the community. The XFEL project, with the same technical basis as the ILC, although at a lower accelerating gradient, and 7% of the energy of one of the ILC linacs, is a large-scale system test and demonstration of the industrialization of the ILC linac technology. The CERN-based CTF3 project is a demonstration of the CLIC two beam technology, although at a lower beam power.

• **Technology maturity and risk**

The collaborations agree that the ILC technology is presently more mature and less risky than that of CLIC. There are plans to demonstrate, by 2010, the feasibility of CLIC technology and to reduce the associated risk in the future. The ILC collaboration will focus on consolidation of the technology for global mass-production. Both collaborations consider it essential to continue to develop both technologies for the foreseeable future.

• **Costing**

Project planners from the CLIC and ILC projects are developing common methodologies and tools with the intention of enabling the development of similarly-structured project planning and costing documents for each of the two projects. The two collaborations agree to make no public statements about the comparative cost numbers of the two machines until these project planning and costing documents are complete.

Barry C. Barish

Barry C. Barish
ILC-GDE Director

J-P. Delahaye

J-P. Delahaye
CLIC Study Leader

CLIC / ILC Collaboration

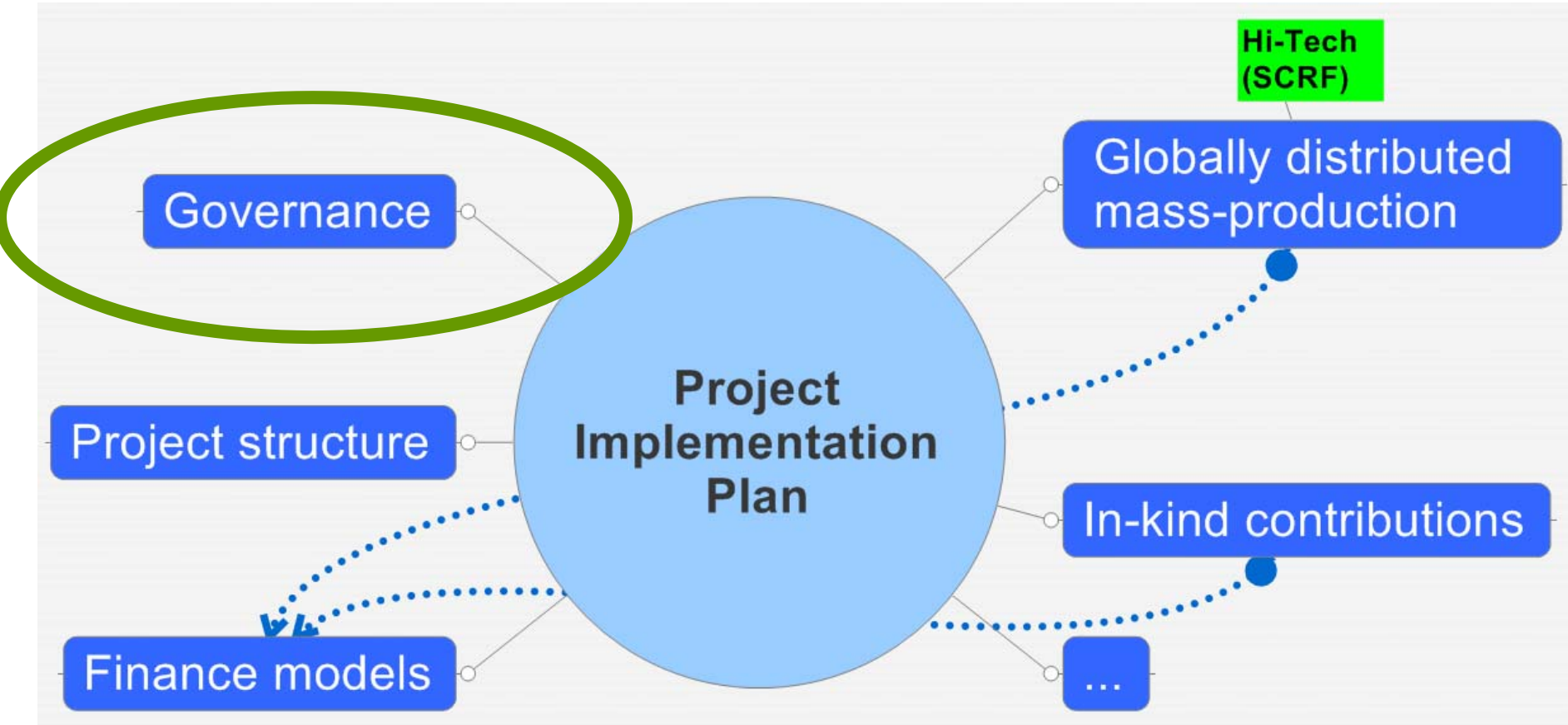
- Working Groups with joint leadership
- Accelerator Tech Areas
- Physics / Detectors
- Costing
- First progress reported last fall

**LOI Follow-on: Study
extrapolation to multi-TeV**

Collaboration Working Groups

	CLIC	ILC
Physics & Detectors	L.Linssen, D.Schlatter	F.Richard, S.Yamada
Beam Delivery System (BDS) & Machine Detector Interface (MDI)	D.Schulte, R.Tomas Garcia E.Tsesmelis	B.Parker, A.Seryi
Civil Engineering & Conventional Facilities	C.Hauviller, J.Osborne.	J.Osborne, V.Kuchler
Positron Generation (new 11/08)	L.Rinolfi	J.Clarke
Damping Rings (new 11/08)	Y.Papaphilipou	M.Palmer
Beam Dynamics	D.Schulte	A.Latina, K.Kubo, N.Walker
Cost & Schedule	H.Braun, K.Foraz, P. LeBrun	J.Carwardine, P.Garbincius, T.Shidara

Project Implementation Plan



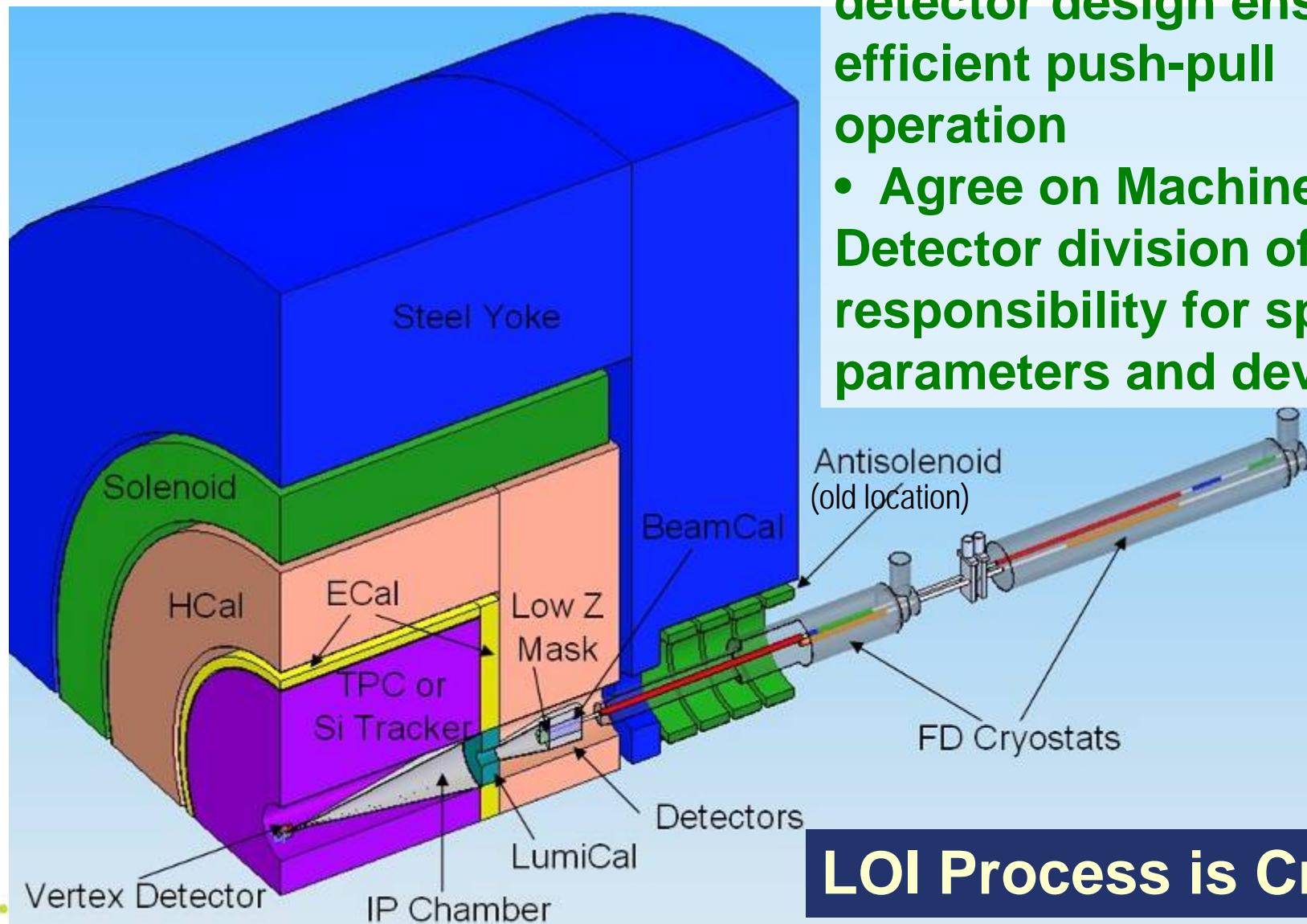
- Brian Foster leads the GDE governance group
 - Examining the main recent projects approved/in preparation: ALMA, FAIR, ITER, SKA, XFEL...
 - Contact made with key individuals in projects. Information gathered and presented.
 - E.g. on funding - 2 main models for funding: Host (~50%) + regional contributions (2 x~25%) or Host (~50%)+member states (n x~10%) (ITER). Balance of in-kind/cash?



IR Integration

CHALLENGES:

- Optimize IR and detector design ensuring efficient push-pull operation
- Agree on Machine-Detector division of responsibility for space, parameters and devices



LOI Process is Crucial

Final Remarks

- We are on track to be able to propose the ILC on a time scale of ~2012 (or before!)
 - GDE R&D demonstrations
 - Cost/risk/performance optimized technical design
 - Project Implementation Plan
 - Detector LOIs → Technical designs
 - LHC results
 - Outreach to generate support from science community, funding agencies, etc
- **Welcome! This meeting should be particularly interesting: first following LOIs; and first AAP review (or experiment)**