

Scintillator HCAL technological prototype: analysis software and results

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on behalf of the CALICE Collaboration
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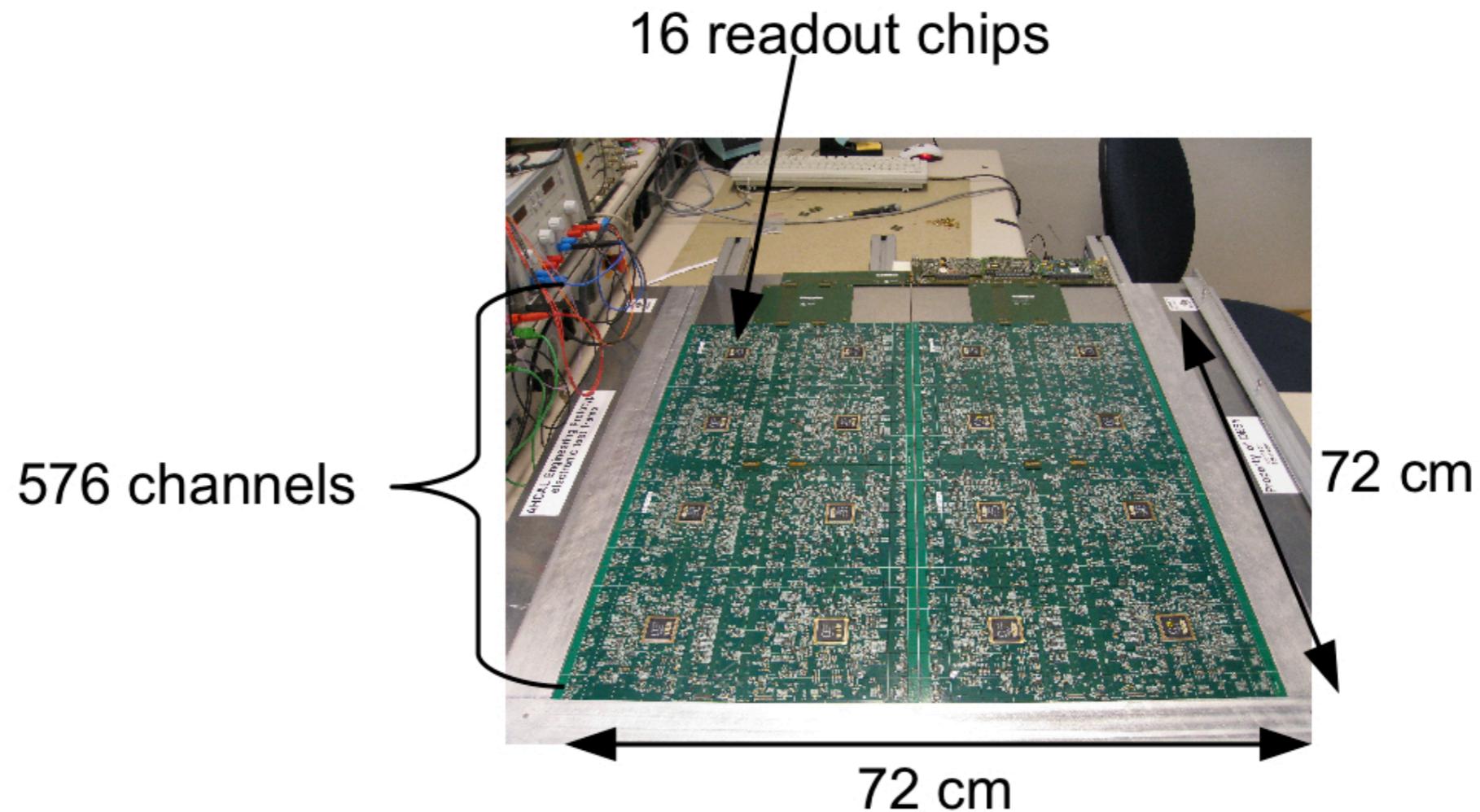


Outline

- The scintillator HCAL technological prototype
- CERN test beam setup
- DAQ and event building
- Test beam data offline analysis software
- Preliminary results from MC and TB
- Summary

The scintillator HCAL technological prototype

- CERN test beam hardware: one layer with 4 **HBU**

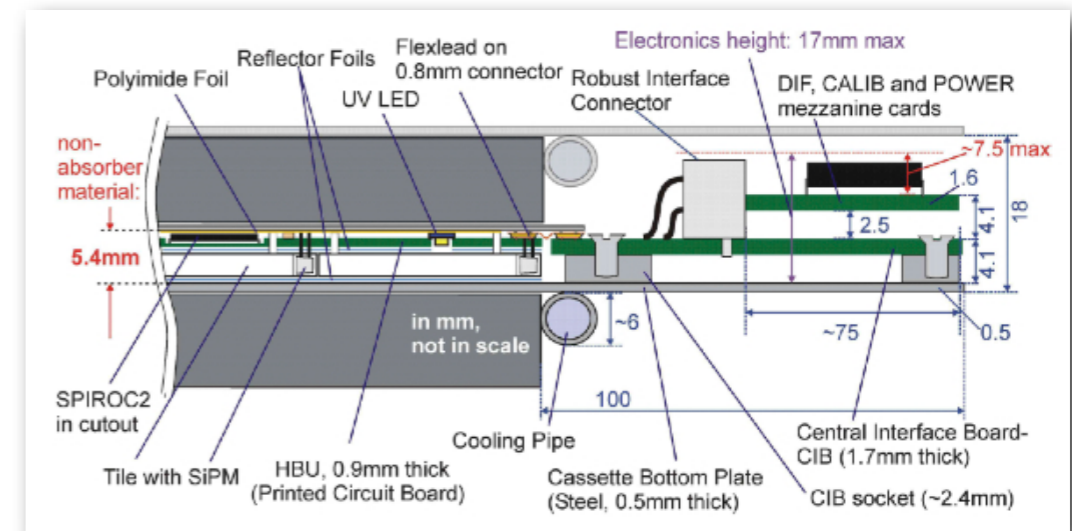


- **H**CAL **B**ase **U**nit: 36*36 cm², 144 tiles, 4 readout ASICs
- All 36 channels on one chip can be operated with a single trigger threshold
- SiPMs from different batches (different gains, different bias voltages) can be operated within one layer

The scintillator HCAL technological prototype

Compact detector:

- front-end electronics integrated in active layers
- thin layers → ASICs embedded in PCB, only 5.4 mm thickness including 3 mm tiles
- power pulsing → no active cooling needed

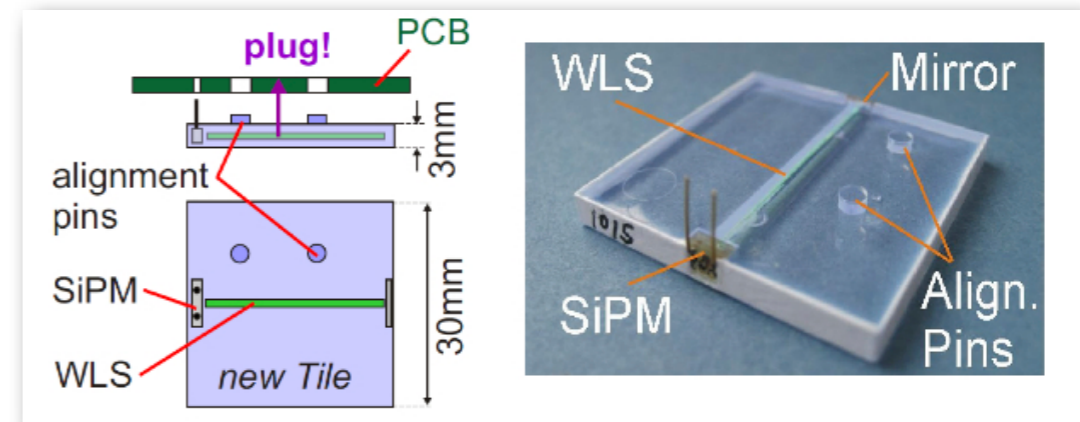


Active material:

- 3*3*0.3 cm³ scintillator tiles with WLS fibers

Detector: SiPM

- pixelated Geiger-mode avalanche photodiodes
- insensitive to magnetic fields
- high gain, low operating voltage, very low power consumption



SPIROC: highly integrated specific chip for SiPM readout (system on chip)

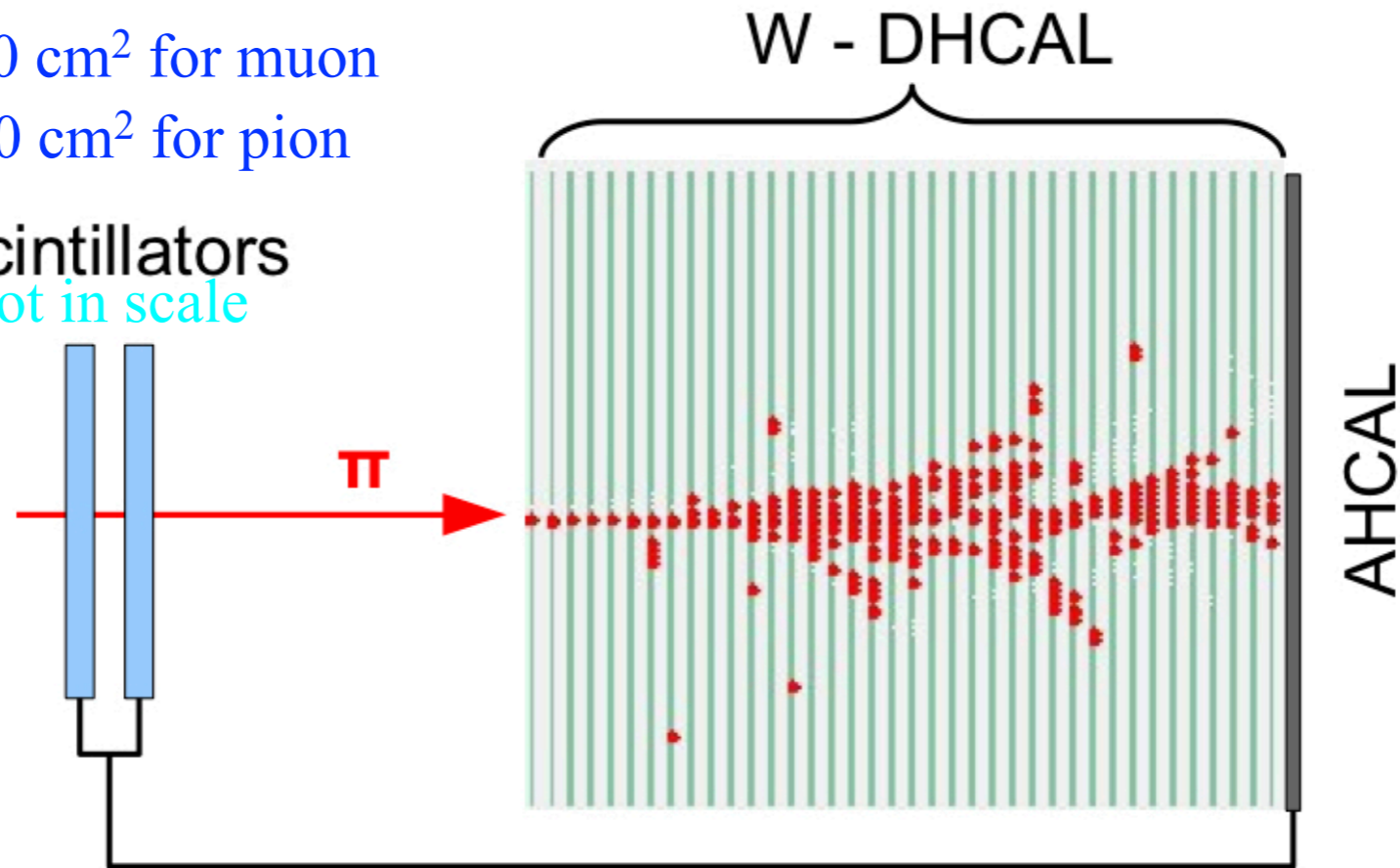
- channel-wise bias adjustment
- channel-wise adjustable gain
- dual gain setup per channel, high gain/low gain ~10
- designed for ILC operation:
 - power pulsing → 25 μ W/ch
 - auto-trigger mode, channel-wise fine adjustment of threshold
- has also special testbeam mode



CERN test beam

30*30 cm² for muon
10*10 cm² for pion

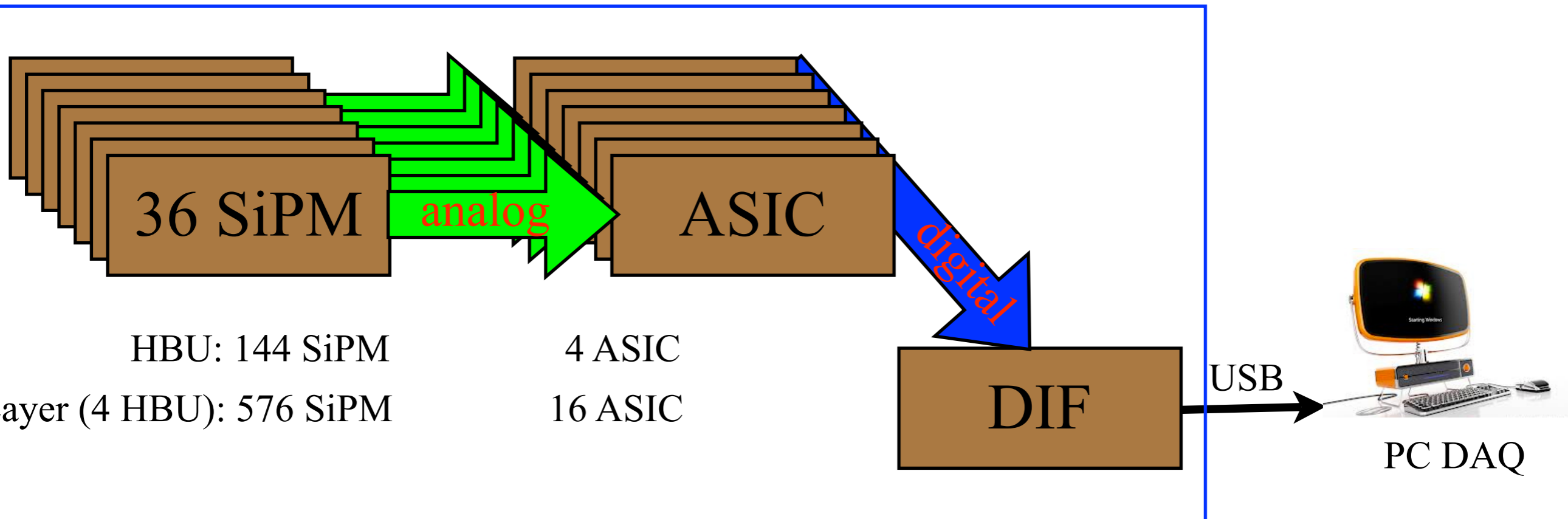
scintillators
not in scale



- Detectors set in conditioned tent:
 - stable temperature conditions
- Trigger scintillators in coincidence
 - online validation signal
 - into two AHCAL channels for absolute time reference
- Muon runs
 - for further MIP calibration
- Pion runs
 - at 50GeV and 180GeV

DAQ Hardware

- readout by Labview DAQ



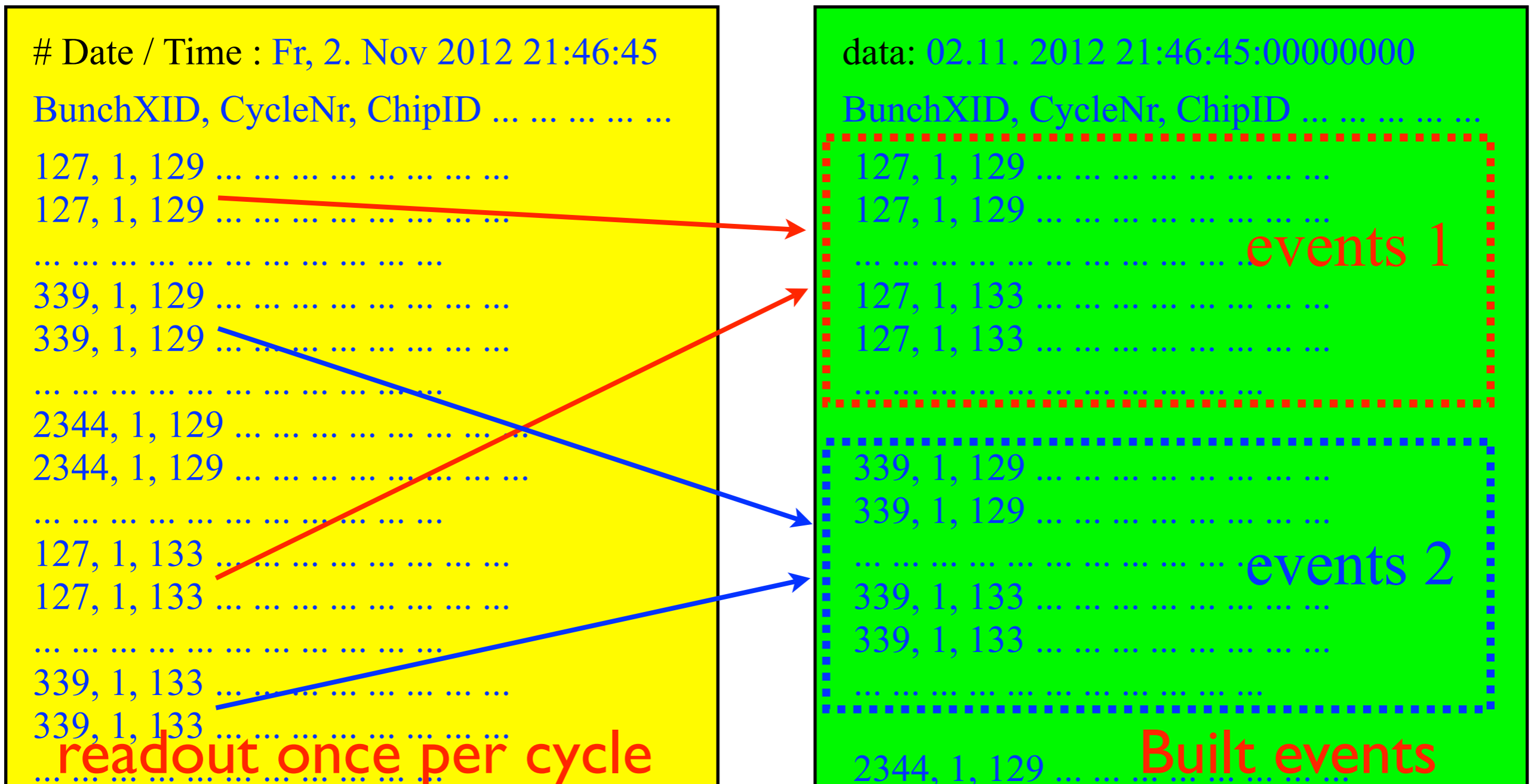
CERN test beam:

- beam cycle: 10s on, 35s off
- during 10s beam:
 - 12-16 readout cycles with up to 16 events per cycle

Event building

Labview DAQ data structure (CERN test beam): Ascii file

- 12 integers: BunchXID, CycleNr, ChipID, ..., Channel, TDC, ADC, ... HitBit, GainBit
- event building: $\text{BunchXID} + \text{CycleNr} == \text{Event}$
- Database timestamp: # Date / Time : Fr, 2. Nov 2012 21:46:45



LCIO conversion

Marlin Processor: LabviewConverter

- events building
- data format conversion
- database run timestamp creation
- LCIO conversion is still needed
- to be done online in the future

```
=====
Event : 1 - run: 10005 - timestamp 1351889236000000000 - weight 1
=====
date:      02.11.2012  20:47:16.000000000
detector : unknown
event parameters:

collection name : LabviewData
parameters:

----- print out of LCGenericObject collection -----

flag: 0x80000000
parameter DataDescription [string]: i:BunchXID; i:CycleNr; i:ChipID; i:ASICNr; i:EvtNr; i:Channel; i:TDC; i:ADC; i:XPos; i:YPos; i:HitBit; i:GainBit,
parameter TypeName [string]: LabviewBlock,

[ id ] i:BunchXID; i:CycleNr; i:ChipID; i:ASICNr; i:EvtNr; i:Channel; i:TDC; i:ADC; i:XPos; i:YPos; i:HitBit; i:GainBit - isFixedSize: true
-----
[00000004] i:19; i:1; i:129; i:0; i:0; i:0; i:989; i:227; i:6; i:7; i:0; i:1; -----
[00000005] i:19; i:1; i:129; i:0; i:0; i:1; i:982; i:240; i:5; i:7; i:0; i:1; -----
[00000006] i:19; i:1; i:129; i:0; i:0; i:2; i:997; i:232; i:4; i:7; i:0; i:1; -----
[00000007] i:19; i:1; i:129; i:0; i:0; i:3; i:988; i:223; i:3; i:7; i:0; i:1; -----
[00000008] i:19; i:1; i:129; i:0; i:0; i:4; i:984; i:251; i:2; i:7; i:0; i:1; -----
[00000009] i:19; i:1; i:129; i:0; i:0; i:5; i:991; i:254; i:1; i:7; i:0; i:1; -----
[0000000a] i:19; i:1; i:129; i:0; i:0; i:6; i:1001; i:233; i:6; i:8; i:0; i:1; -----
[0000000b] i:19; i:1; i:129; i:0; i:0; i:7; i:984; i:256; i:5; i:8; i:0; i:1; -----
[0000000c] i:19; i:1; i:129; i:0; i:0; i:8; i:993; i:245; i:4; i:8; i:0; i:1; -----
[0000000d] i:19; i:1; i:129; i:0; i:0; i:9; i:997; i:238; i:3; i:8; i:0; i:1; -----
[0000000e] i:19; i:1; i:129; i:0; i:0; i:10; i:993; i:230; i:2; i:8; i:0; i:1; -----
[0000000f] i:19; i:1; i:129; i:0; i:0; i:11; i:992; i:226; i:1; i:8; i:0; i:1; -----
[00000010] i:19; i:1; i:129; i:0; i:0; i:12; i:974; i:230; i:6; i:9; i:0; i:1; -----
[00000011] i:19; i:1; i:129; i:0; i:0; i:13; i:990; i:241; i:5; i:9; i:0; i:1; -----
[00000012] i:19; i:1; i:129; i:0; i:0; i:14; i:982; i:236; i:4; i:9; i:0; i:1; -----
[00000013] i:19; i:1; i:129; i:0; i:0; i:15; i:986; i:244; i:3; i:9; i:0; i:1; -----
[00000014] i:19; i:1; i:129; i:0; i:0; i:16; i:985; i:230; i:2; i:9; i:0; i:1; -----
[00000015] i:19; i:1; i:129; i:0; i:0; i:17; i:980; i:237; i:1; i:9; i:0; i:1; -----
[00000016] i:19; i:1; i:129; i:0; i:0; i:18; i:984; i:244; i:6; i:9; i:0; i:1; -----
```

LCIO format

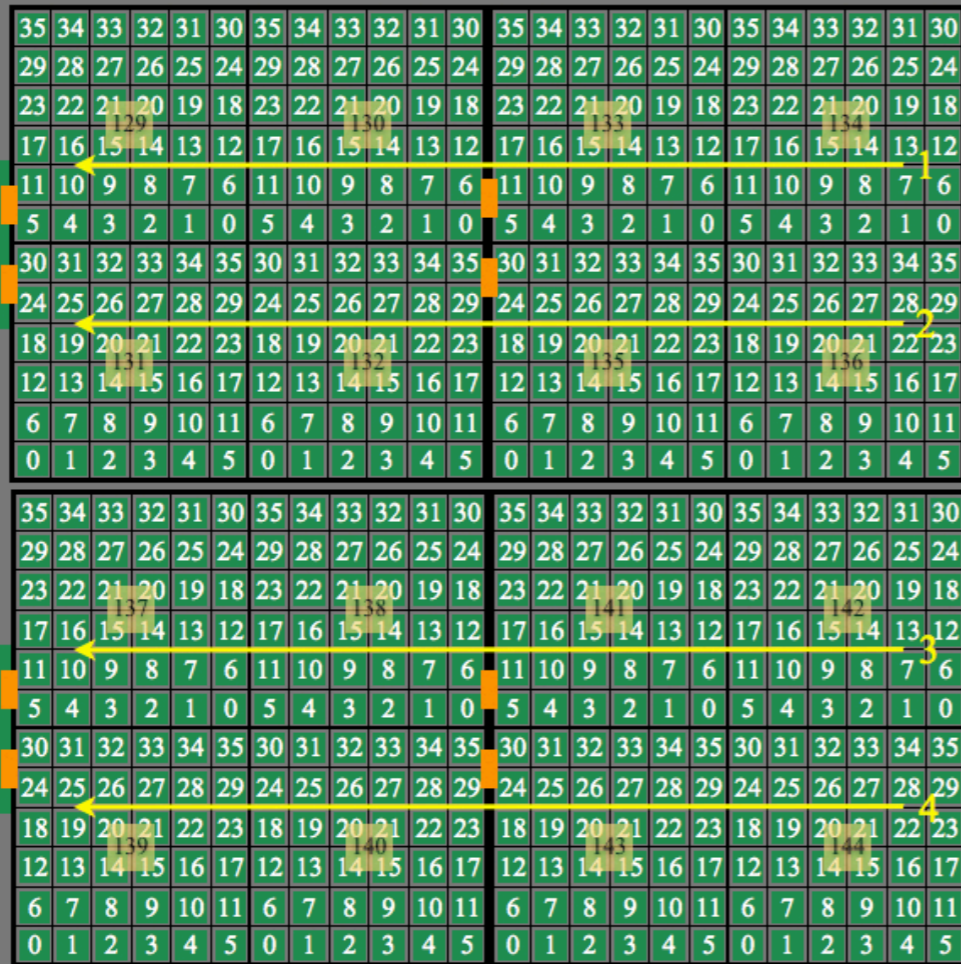
Test beam data offline analysis software

The scintillator HCAL technological prototype:

- Analysis software for CERN test beam
 - test beam data have been converted to slcio format
 - the analysis process continue to stick to the ILCsoft LCIO/MARLIN framework
 - mapping from electronics to geometry
 - the understanding from the physics prototype will be transferred
 - and expand to the understand of the 4th dimension - time
 - analysis the data and write into root for final plots

Electronics geometry mapping

Absorber Plate (steel or tungsten)



1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24

x/I

1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24
y/J

1/17	2/17	3/17	4/17	5/17	6/17	7/17	8/17	9/17	10/17	11/17	12/17	13/17	14/17
1/16	2/16	3/16	4/16	5/16	6/16	7/16	8/16	9/16	10/16	11/16	12/16	13/16	14/16
1/15	2/15	3/15	4/15	5/15	6/15	7/15	8/15	9/15	10/15	11/15	12/15	13/15	14/15
1/14	2/14	3/14	4/14	5/14	6/14	7/14	8/14	9/14	10/14	11/14	12/14	13/14	14/14
1/13	2/13	3/13	4/13	5/13	6/13	7/13	8/13	9/13	10/13	11/13	12/13	13/13	14/13
1/12	2/12	3/12	4/12	5/12	6/12	7/12	8/12	9/12	10/12	11/12	12/12	13/12	14/12
1/11	2/11	3/11	4/11	5/11	6/11	7/11	8/11	9/11	10/11	11/11	12/11	13/11	14/11
1/10	2/10	3/10	4/10	5/10	6/10	7/10	8/10	9/10	10/10	11/10	12/10	13/10	14/10
1/9	2/9	3/9	4/9	5/9	6/9	7/9	8/9	9/9	10/9	11/9	12/9	13/9	14/9
1/8	2/8	3/8	4/8	5/8	6/8	7/8	8/8	9/8	10/8	11/8	12/8	13/8	14/8
1/7	2/7	3/7	4/7	5/7	6/7	7/7	8/7	9/7	10/7	11/7	12/7	13/7	14/7
1/6	2/6	3/6	4/6	5/6	6/6	7/6	8/6	9/6	10/6	11/6	12/6	13/6	14/6
1/5	2/5	3/5	4/5	5/5	6/5	7/5	8/5	9/5	10/5	11/5	12/5	13/5	14/5
1/4	2/4	3/4	4/4	5/4	6/4	7/4	8/4	9/4	10/4	11/4	12/4	13/4	14/4
1/3	2/3	3/3	4/3	5/3	6/3	7/3	8/3	9/3	10/3	11/3	12/3	13/3	14/3
1/2	2/2	3/2	4/2	5/2	6/2	7/2	8/2	9/2	10/2	11/2	12/2	13/2	14/2
1/1	2/1	3/1	4/1	5/1	6/1	7/1	8/1	9/1	10/1	11/1	12/1	13/1	14/1

y/J
x/I
z/K

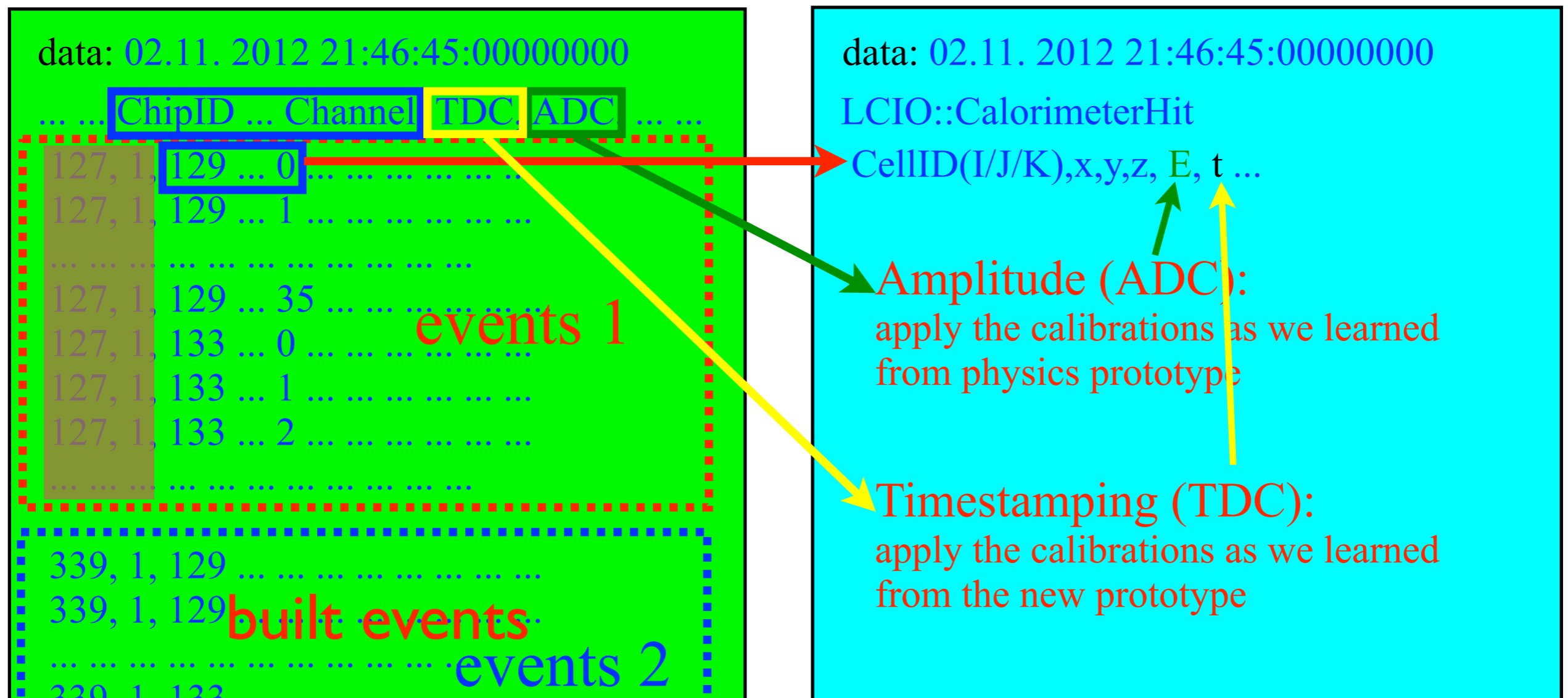
Note: z/K is the beam direction

- Mapping of CERN test beam hardware setup
 - right hand coordinate
 - coordinate definition: z/K is the beam direction, y/J direction is up.
 - Mapping between model/chip/channel(HW), I/J/K and x/y/z (SW Reconstruction).

Reconstruction overview

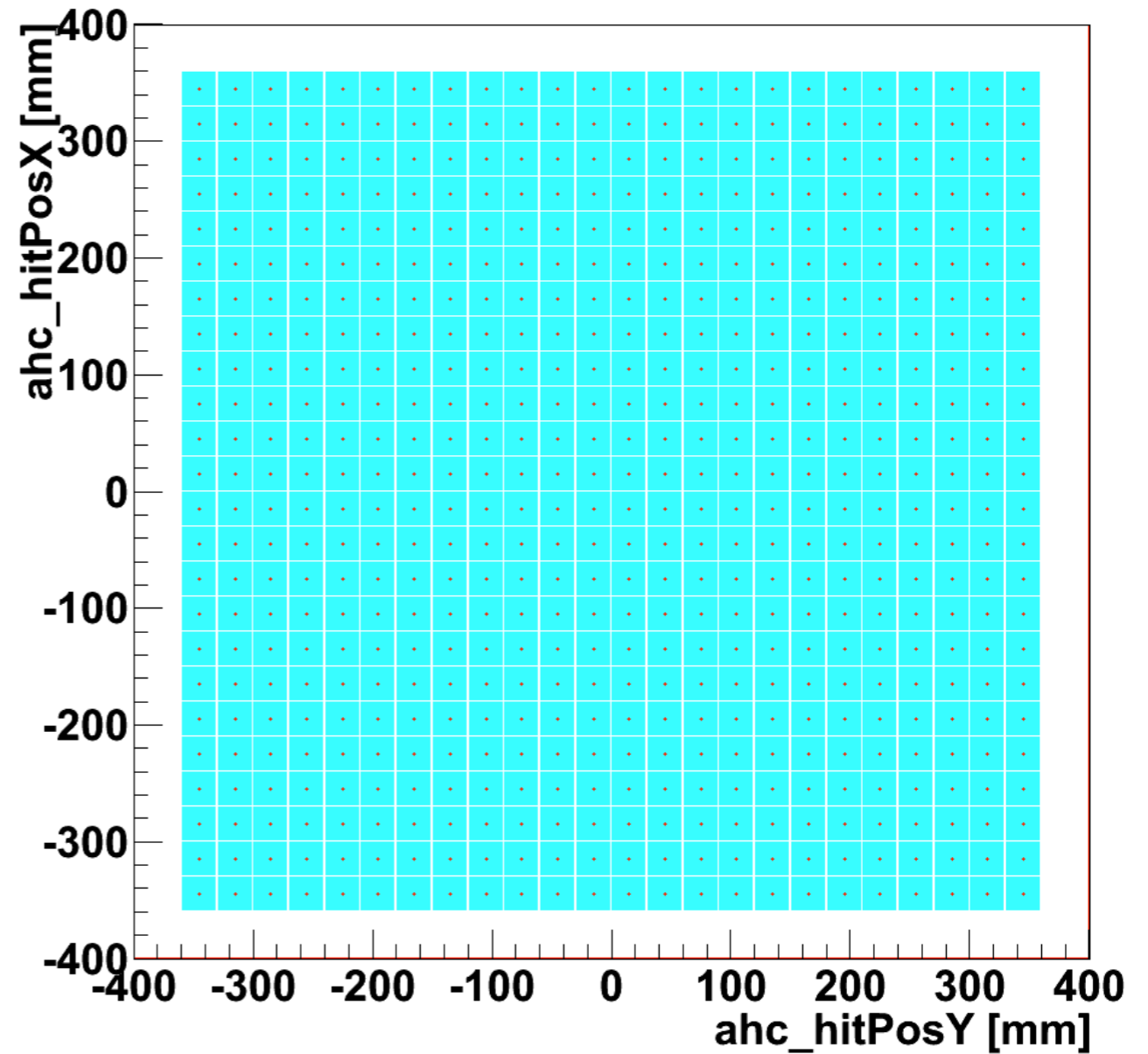
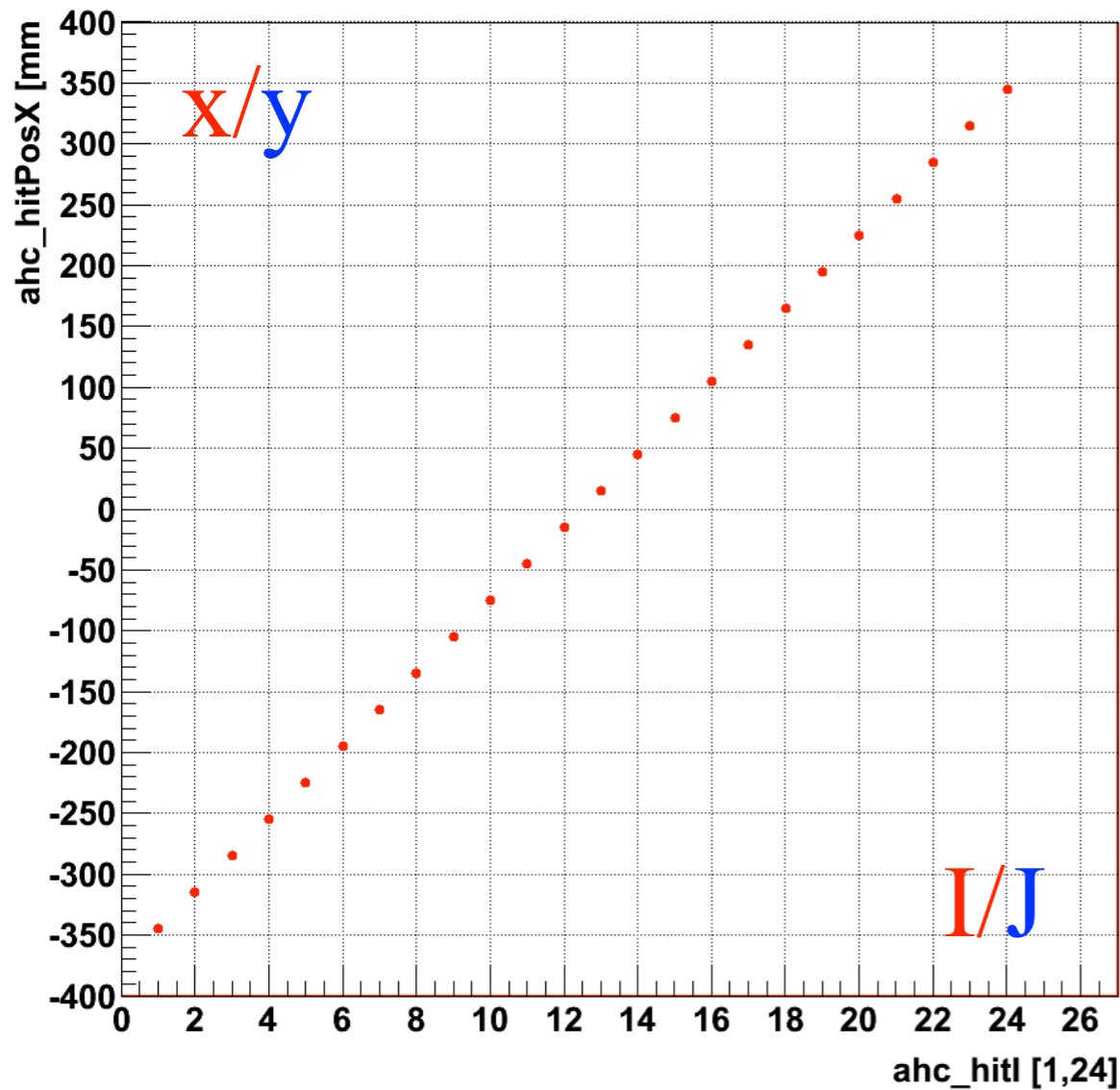
USE ILCsoftware framework:

- using MySQL database to handle the geometry mapping and condition/calibration constants
- using Marlin Processors for algorithm: Amplitude calibration, time stamping calibration ...
- write out as CalorimeterHit in LCIO format
 - reconstructed CellID,x,y,z,E,t ...



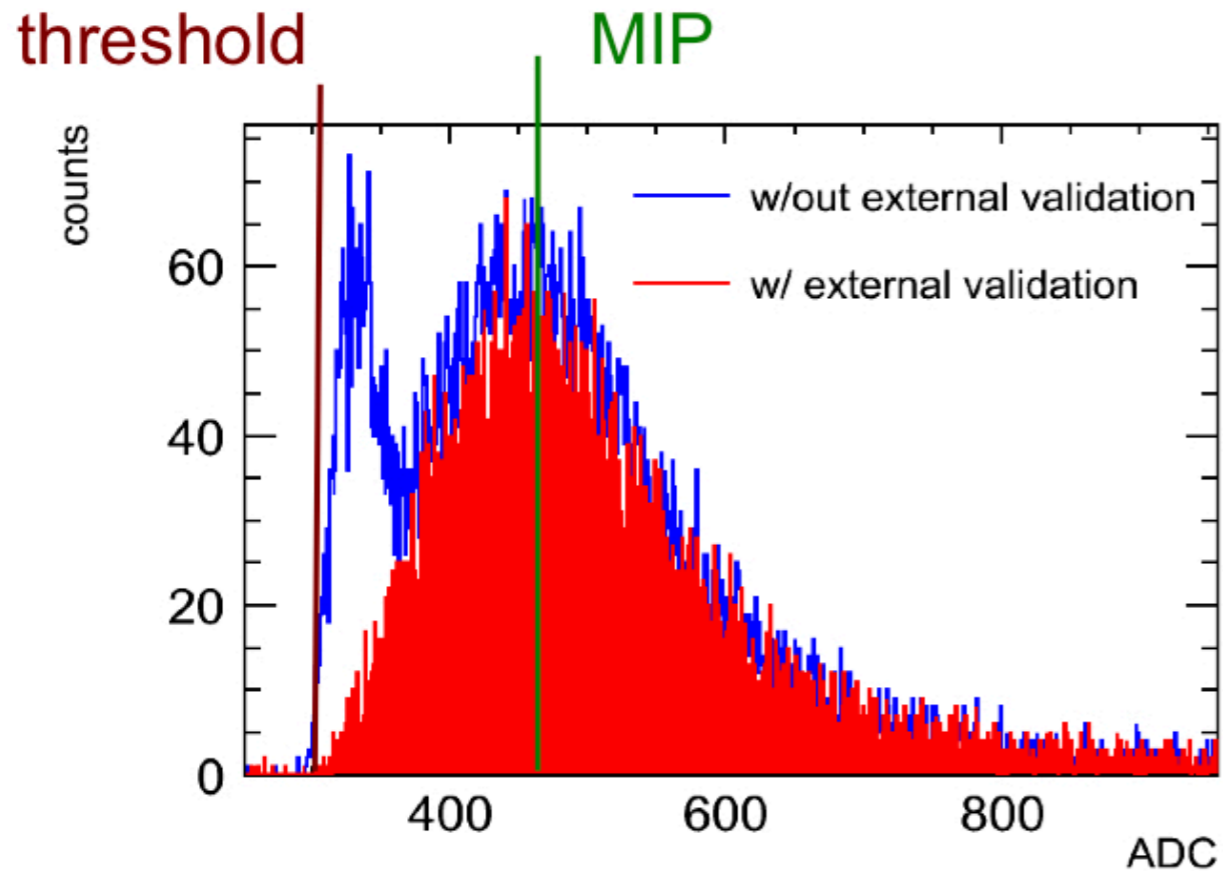
Reconstructed geometry

- Geometry reconstructed for 180GeV Muon



- Blue square shows the tiles and the dot in the centre shows the position
- After reconstruction, the correlation of the coordination for the hits

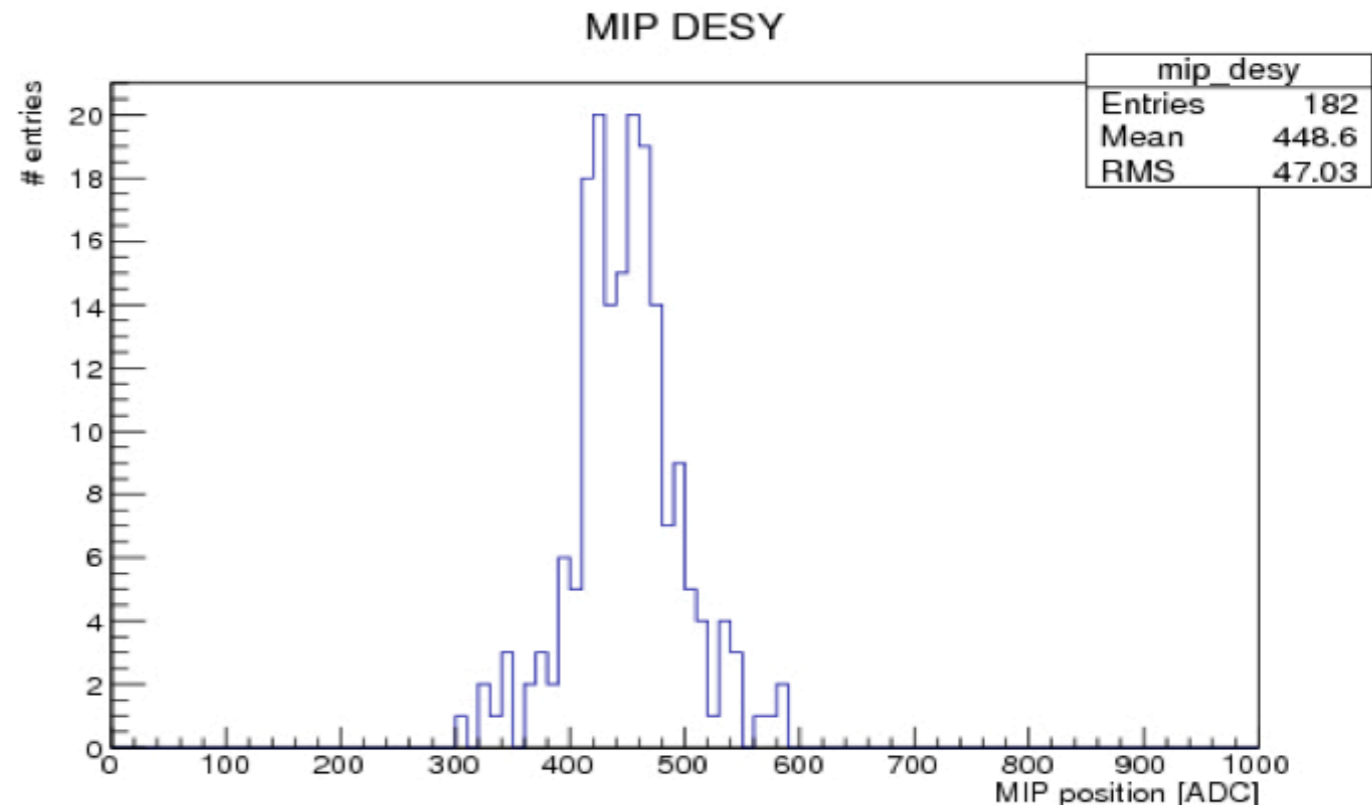
Calibration: MIP



Performed at DESY test beam

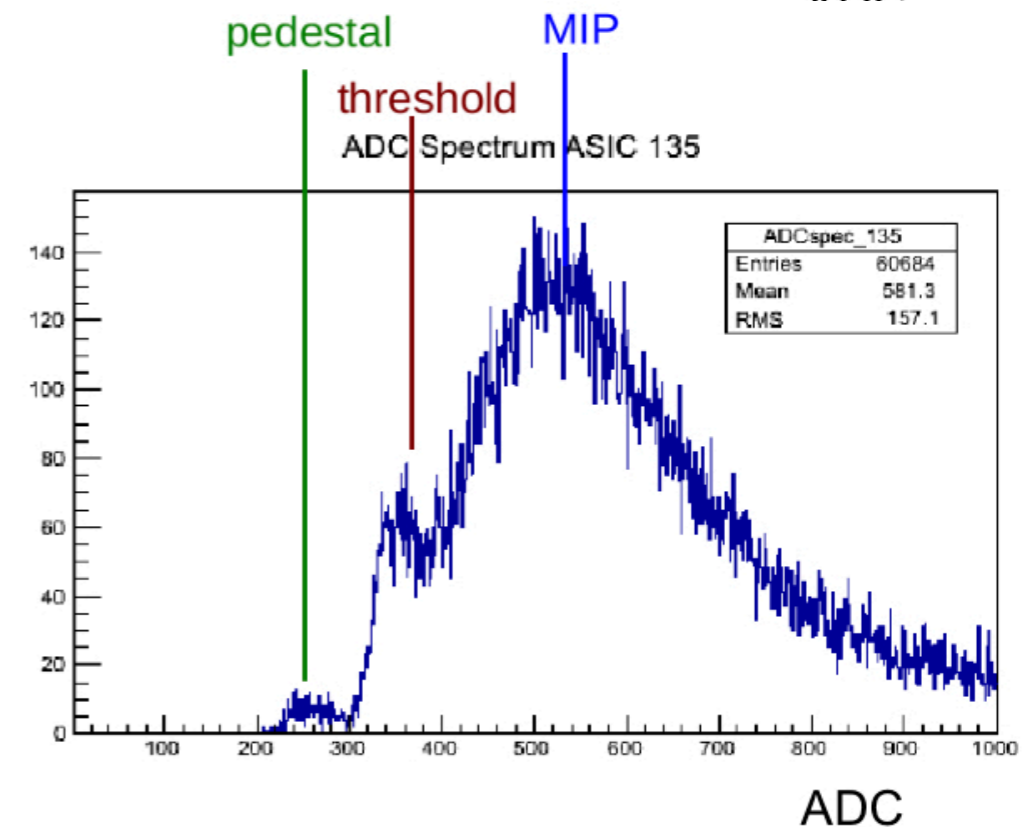
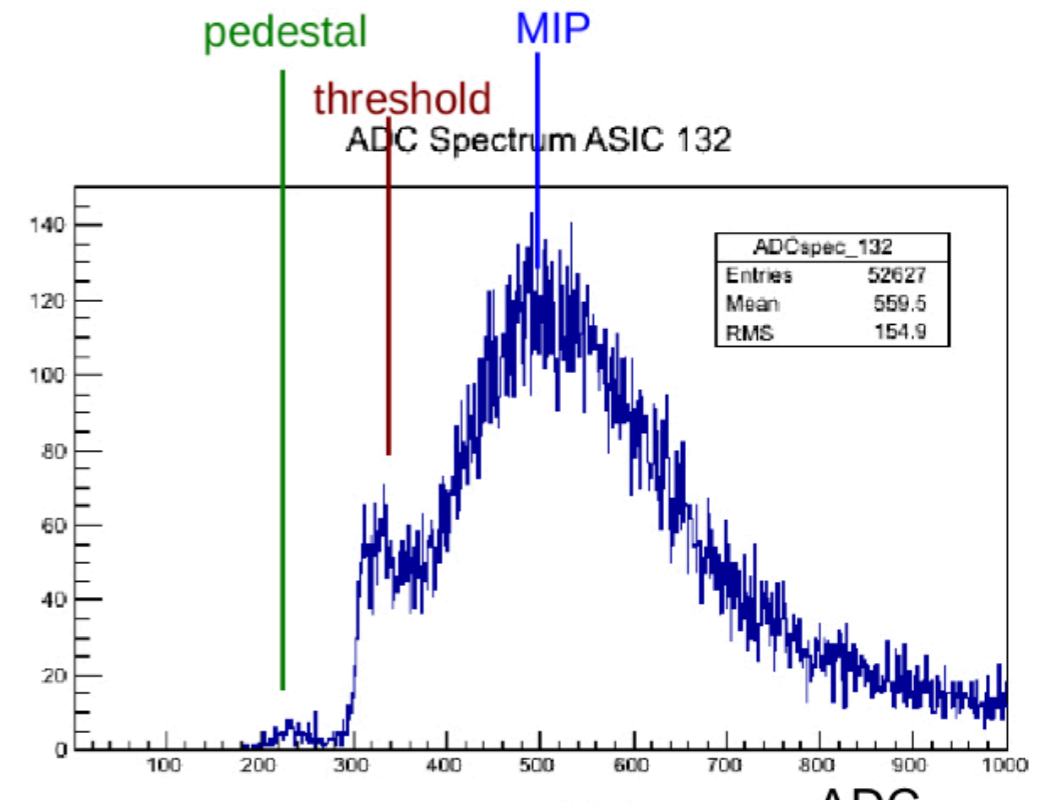
- 3 GeV electron
- on individual tiles
- individual threshold adjustment
- test for the event validation

- Spread of the MIP most probable value $\sim 10\%$
- This value includes:
 - individual SiPM bias adjustment
 - SiPM gain equalization via preamplifiers



CERN test beam: Muons

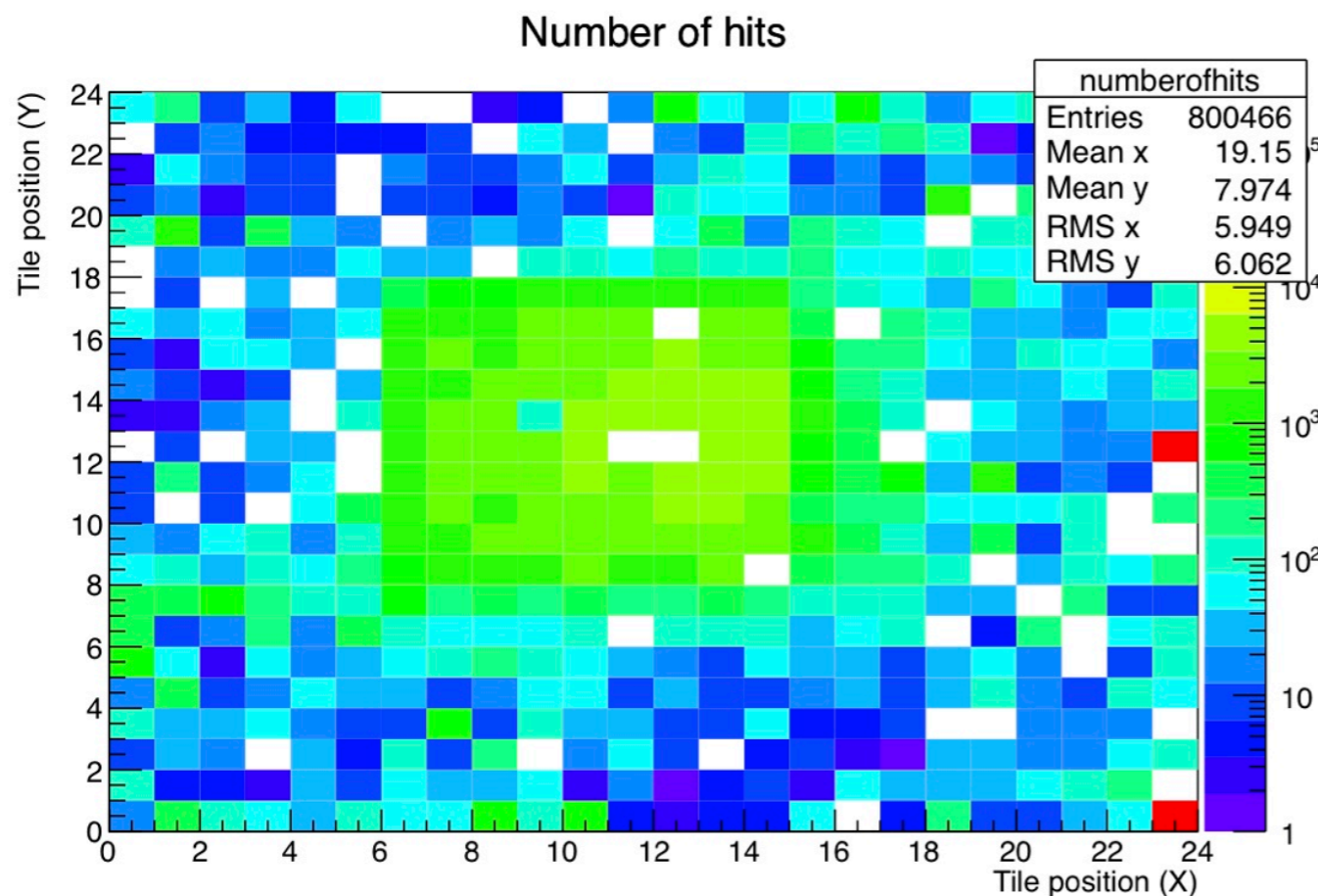
- Cross-check of threshold calibration
 - flexible electronics allows equalization of detector response
 - homogeneity of MIP positions
- Successful test for external validation:
 - Threshold < 0.5 MIP
 - Noise < 50 Hz/HBU (up to 700 Hz/HBU w/ out validation)
 - Beam rate 1 – 50 Hz
- 36 channels superimposed
- Noise peak due to known small inefficiency of external validation



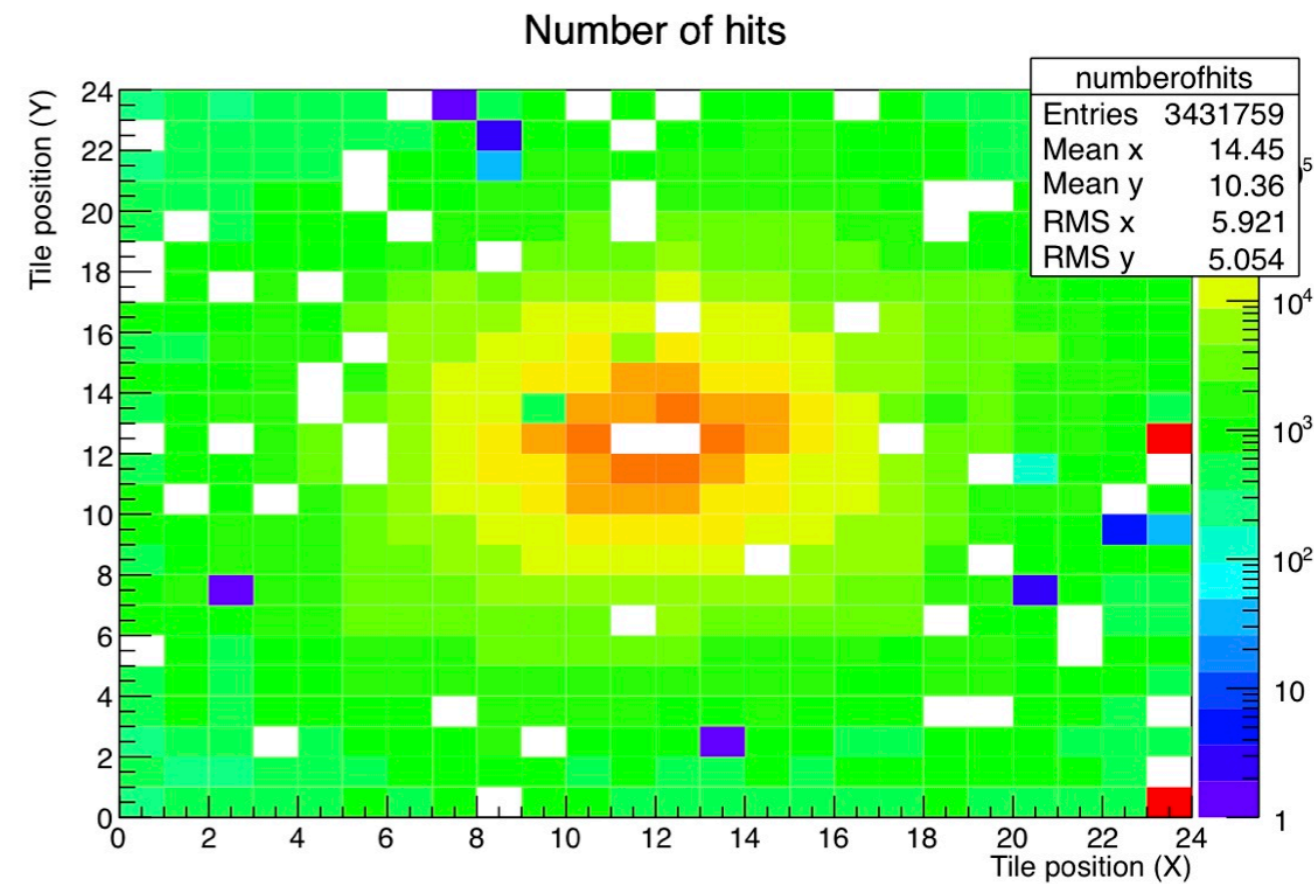
CERN test beam: Muons and Pions

The events collected at CERN:

- more than 400k muons events reconstructed
- ~ 420k pion events reconstructed at 180 GeV
- ~ 86k pion events reconstructed at 50 GeV



180 GeV muons



180 GeV pions

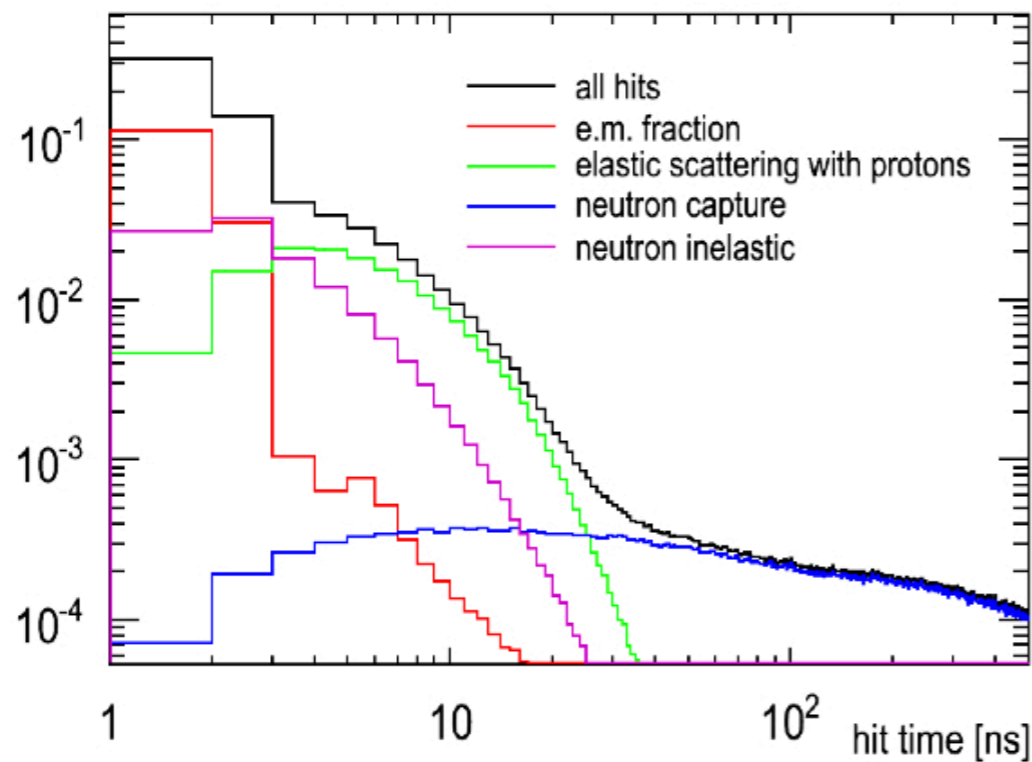
The two white color channels in the center are dead channels
dead channels due to SiPM, ASIC, connection

The two red color channels are the “T0” reference channels

- input from the coincidence triggers

Hadronic shower timing

180 GeV π^- QGSP_BERT_HP



see Frank Simon's talk

First timing experiment in CALICE (T3B)

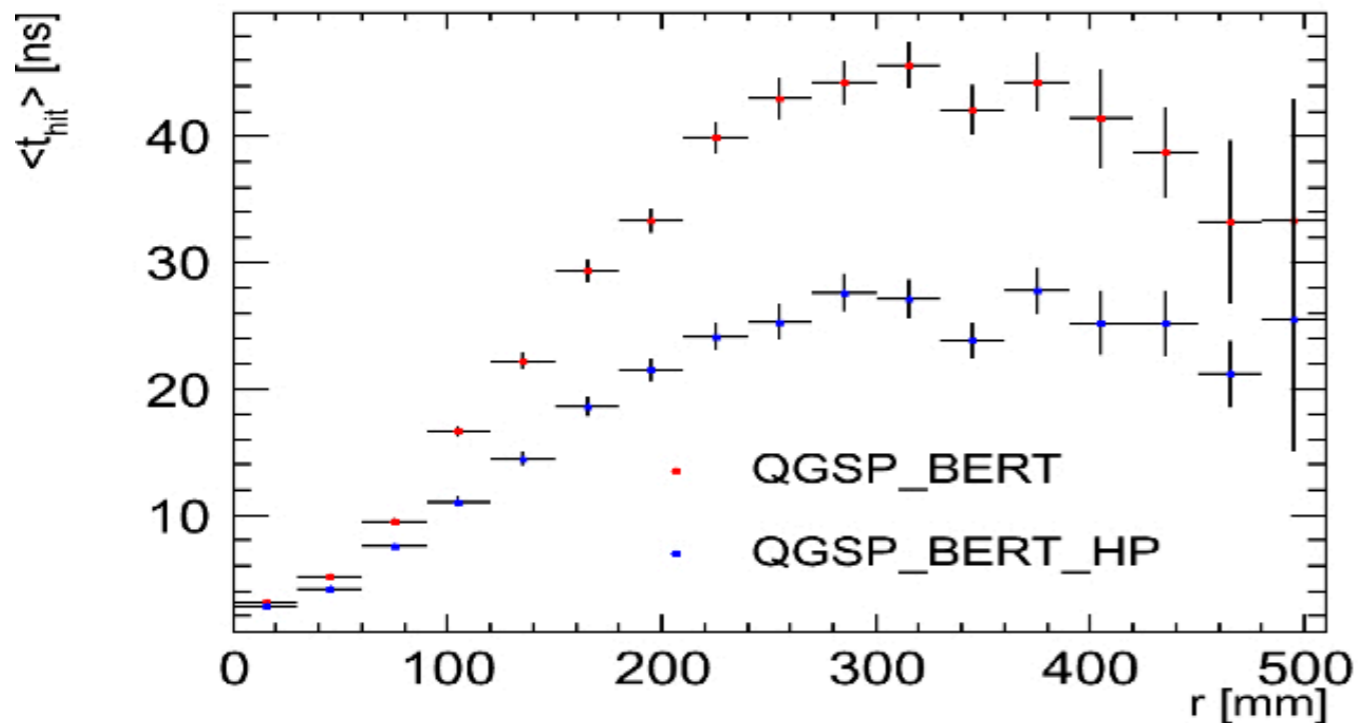
- One dimensional: row of 15 channels
- Picosecond resolution (not scalable)

- Late neutron component in hadronic showers: Impact on Particle Flow Algorithm
- Improve shower reconstruction with time cuts
- Estimate effect of pile-up (CLIC)

Hadronic shower timing

Do we have the sensitivity?

180 GeV π^-



The scintillator HCAL technological prototype:

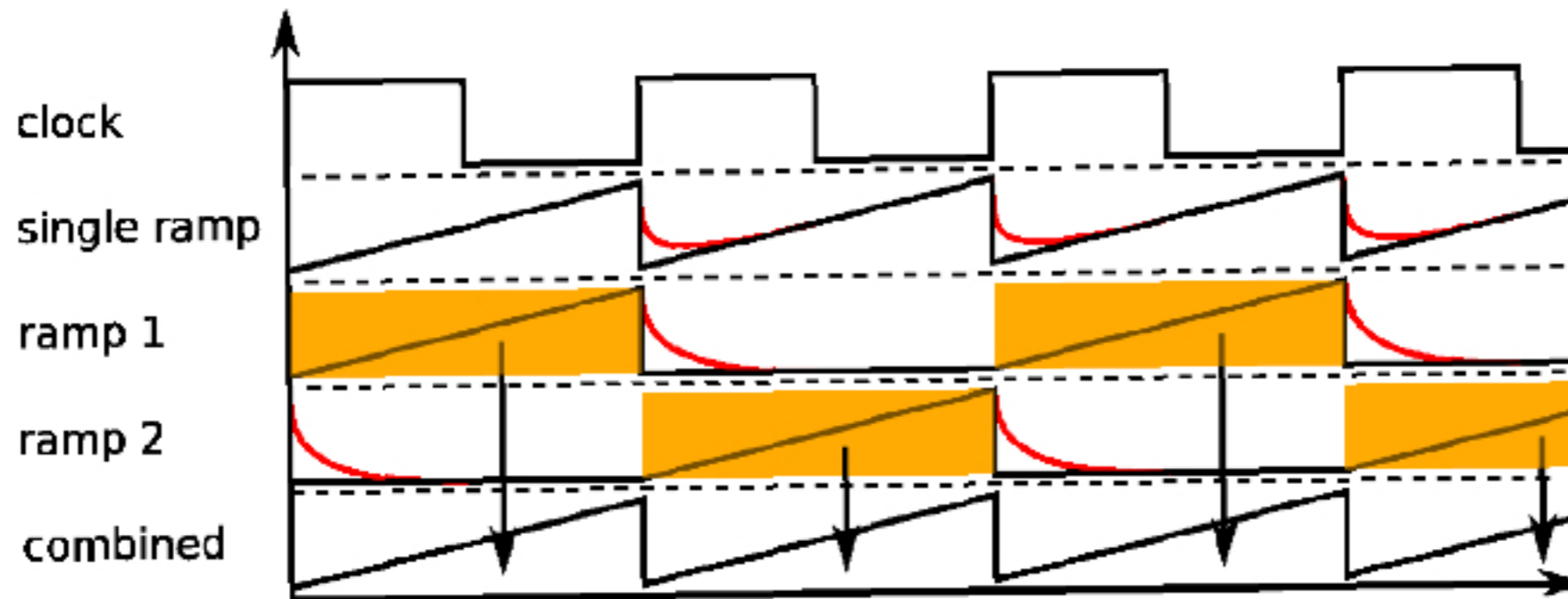
- nanosecond resolution scalable technology
- one layer (4 HBU): tested at CERN

simulation for the CERN AHCAL setup

- 500k MC events

AHCAL time stamping

Time stamping creation:

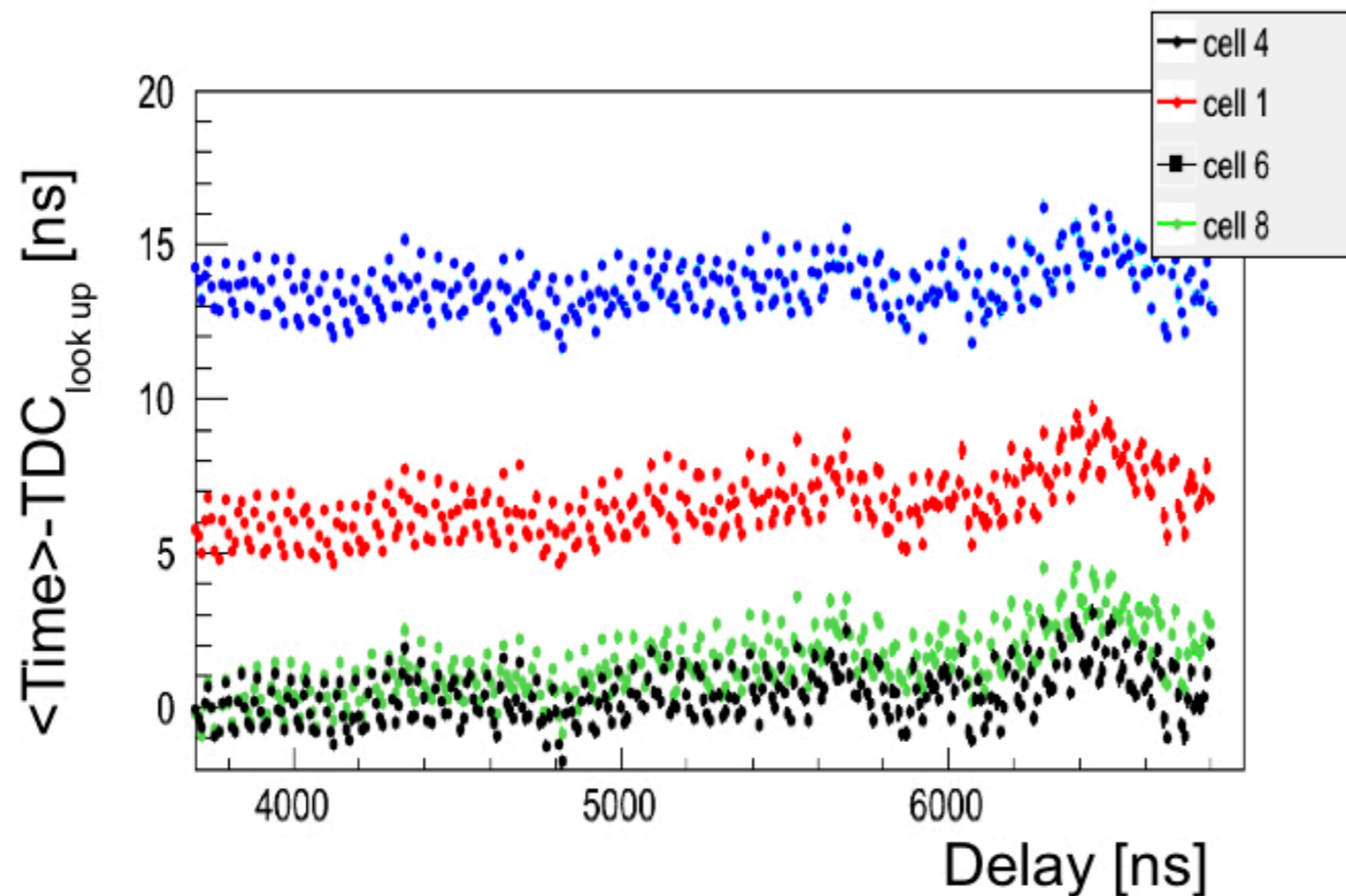


- 2 multiplexed voltage ramps per chip (adj. length):
 - 5 μs long
 - voltage value at hit is stored in memory cell:
 - $\sim 1.2 \text{ ns/bin}$ ($5 \mu\text{s}/4096 \text{ bins}$)

Calibration: Time stamping

Ramp calibration performed at DESY

- charge injection with a pulser
- $\text{time [ns]} = f(\text{TDC}) + \text{offset}$



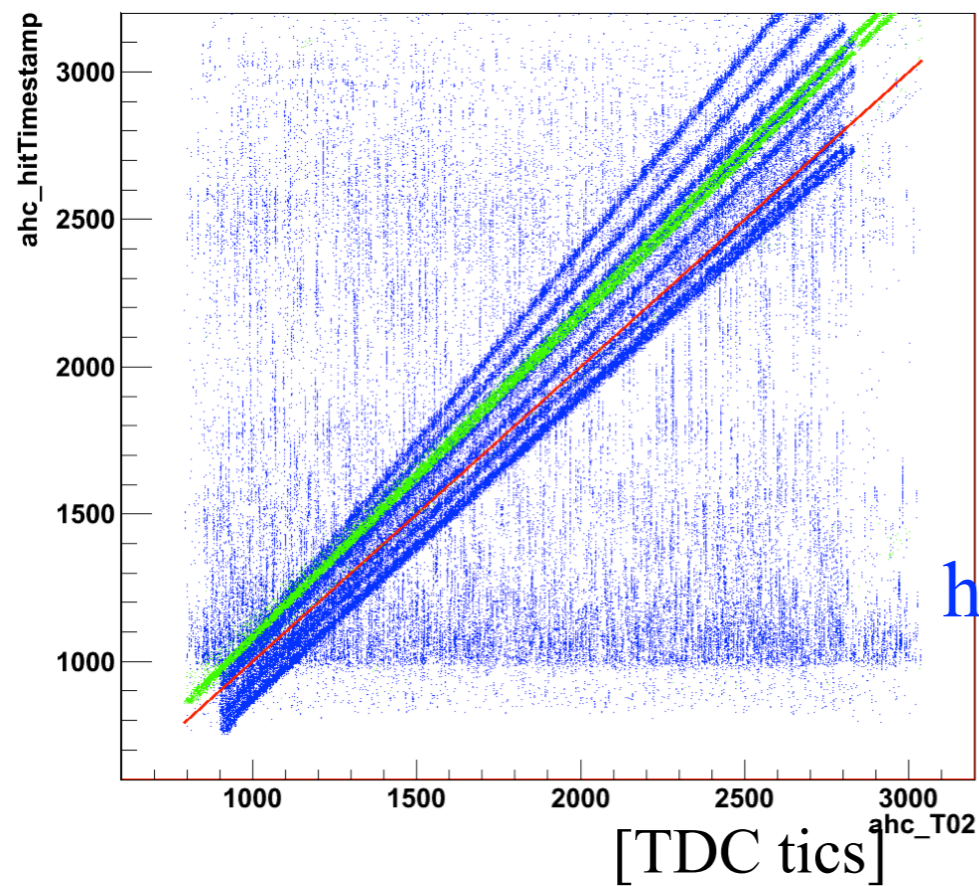
- different time slice / buffer number
- same channel / chip

- each memory cell has different offset
- Single channel spread ~ 3 ns

CERN test beam: Time stamping

- Time stamping: TDC

180GeV Pions

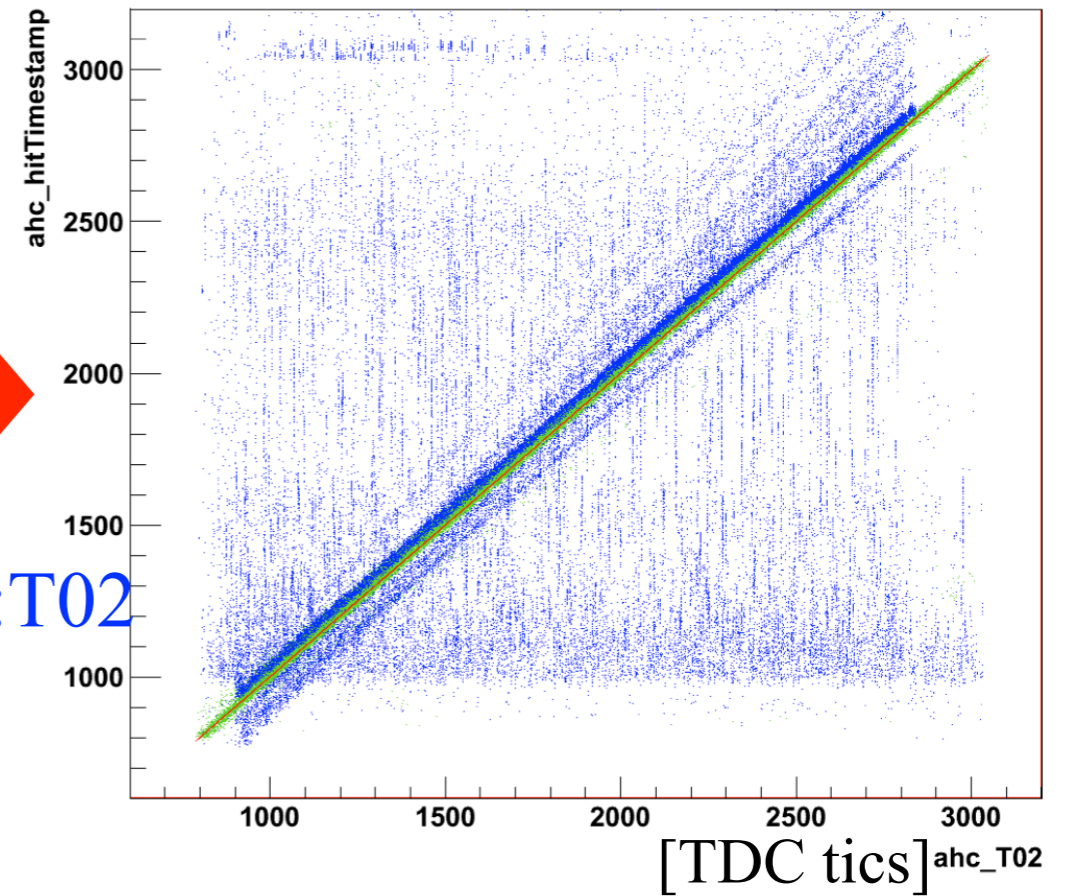


T01:T02

T02:T02

reco

hitTimestamp:T02



w/o TDC offset calibration

after TDC offset calibration

- Very good calibrations for the central 4 chips
- high statistic muons selected by the online validation

Calibrations are ongoing

Summary

- For the scintillator HCAL technological prototype, the analysis software framework has been setup.
- The **Converter** has been done to move “train” back onto the track.
- The reconstruction software development continue to stick to the ILC software framework.
- **Reconstruction** process has been built.
- To finalize the **calibration** constants, and continue to improve the calibrations.