

Simulation of Gate GEM

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reported by A.Sugiyama

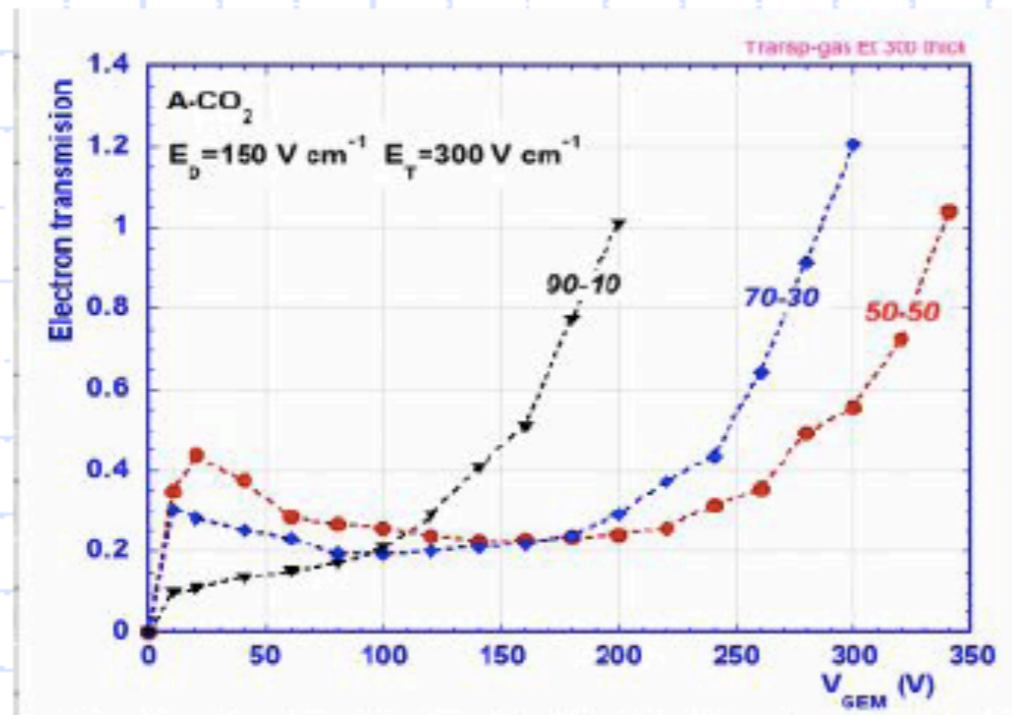
Why GEM for Gate ?
What has been done?
How much GEM can do ?
What will we do?

Original motivation was given by Sauli

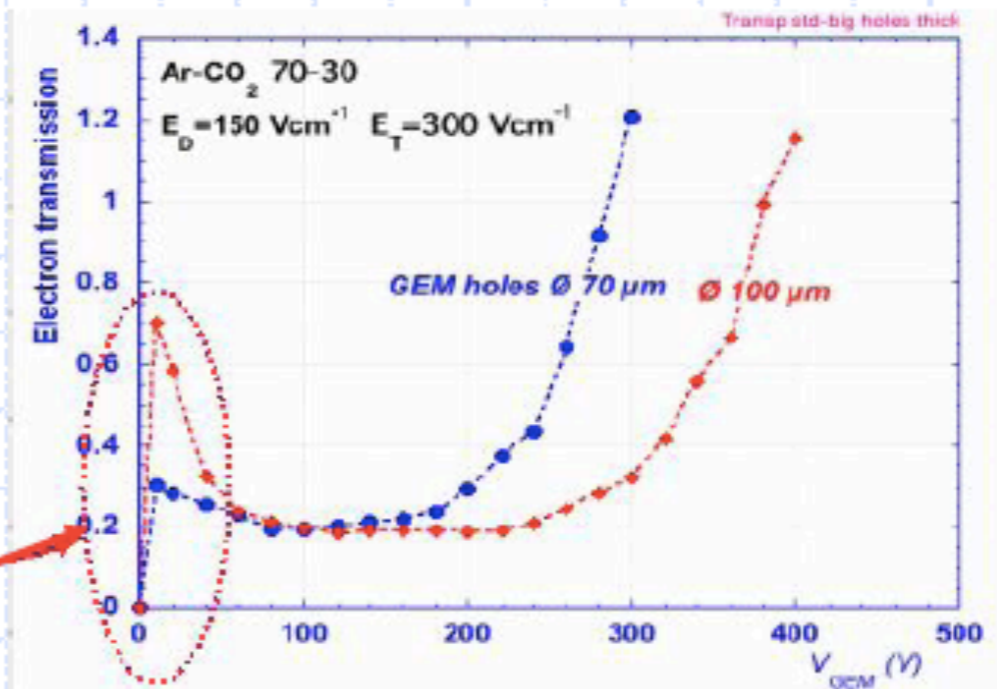
F.Sauli had proposed GEM gating @LBLTPC WS' 06

These figures are picked up from his slides.

GAS EFFECT



HOLE DIAMETER EFFECT



10 V/50 μ m ~ 2 kV/cm!

Why low voltage operation give us higher transmission ?

Is this true for LC TPC gas ?

How much electrons will be lost ? \rightarrow resolution

This affects to Gas choice of LC-TPC

We have tried to understand behavior at low V_{GEM} using Maxwell3D + Garfield

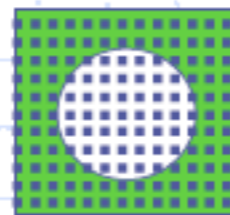
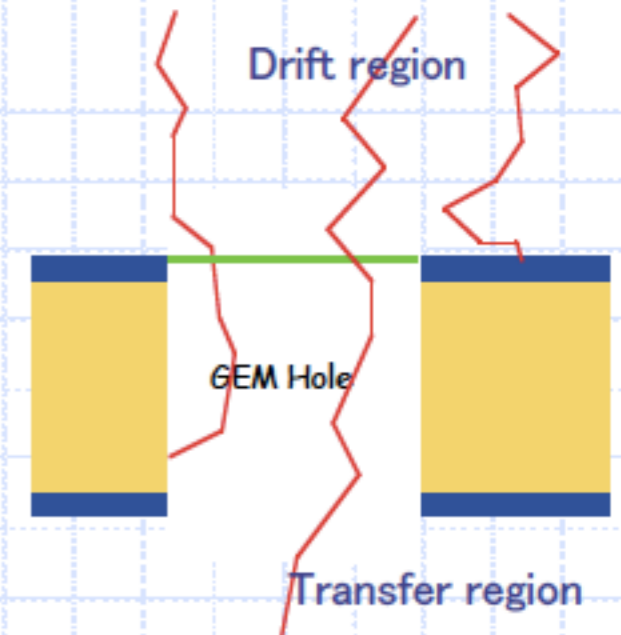
How do we understand his data ?

Garfield help us to understand it !!

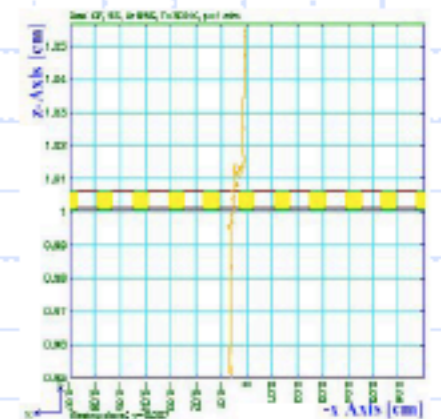
Transmission = Collection eff. x Extraction eff.

Collection eff. = $\frac{\text{\#electrons arrived at Hole entrance}}{\text{\# produced electron}}$

Extraction eff. = $\frac{\text{\#electrons coming out from GEM hole}}{\text{\#electrons arrived at Hole surface}}$



In the simulation electrons are generated 500um above the GEM surface, at 20x20 different positions covering the hole part of GEM.

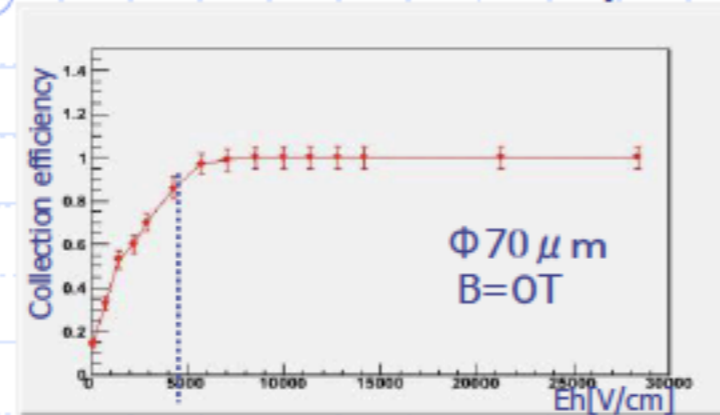


Simulation results are compared to Sauli's measurement

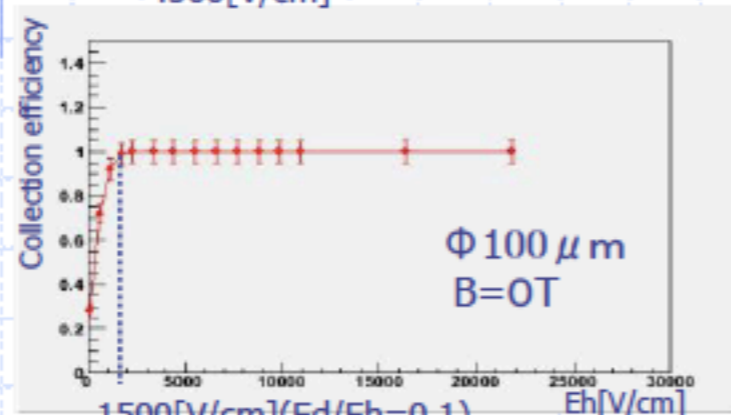
*remarks: you have to choose proper "step size"
we finally chose 2um as step length

Simulation could reproduce data (partially) and taught us a lot !

collection efficiency



4500[V/cm]



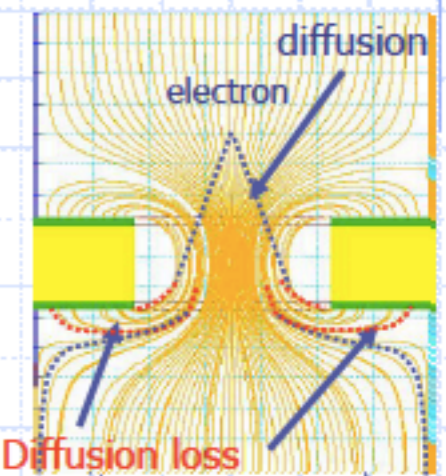
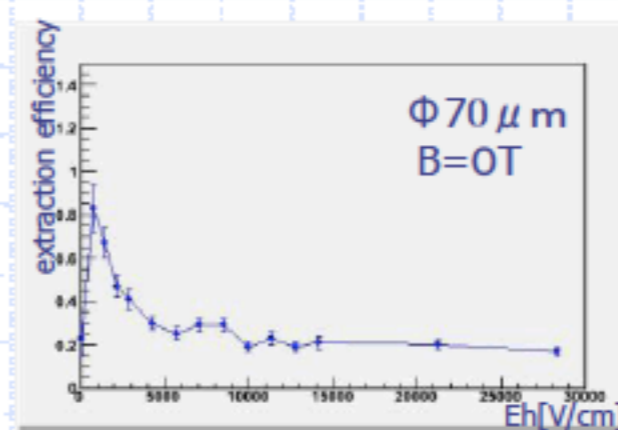
1500[V/cm]($E_d/E_h=0.1$)

Collection efficiency has been studied by many groups and is known to be 1 at $E_d/E_h < \sim 0.03$

Which almost corresponds to 4500V for 70um hole.

In 100um phi case, collection efficiency reach to 1 at $E_d/E_h < 0.1$.

extraction efficiency



In the case of extraction efficiency, the area of pass-through field lines from drift region would shrink as Electric field in the hole.

And Diffusion under high electric field becomes larger and electrons may escape into return line.

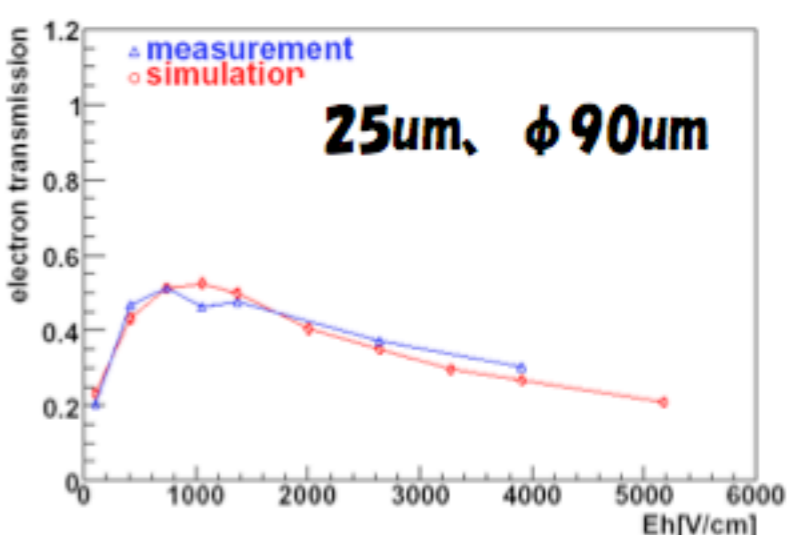
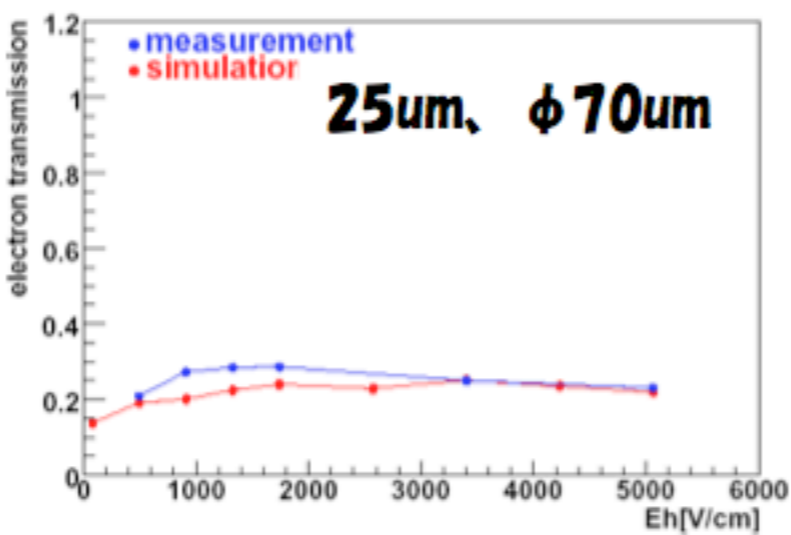
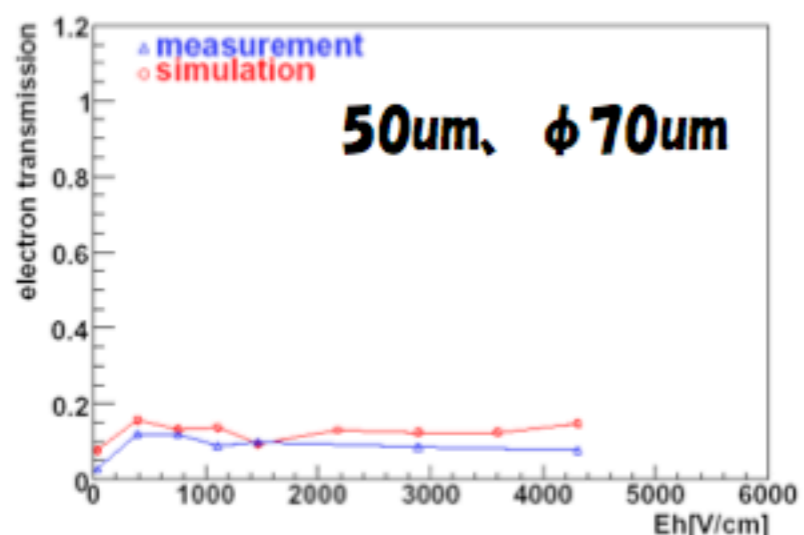
Effects of shrinking electric field lines

and diffusion may determine this behavior.

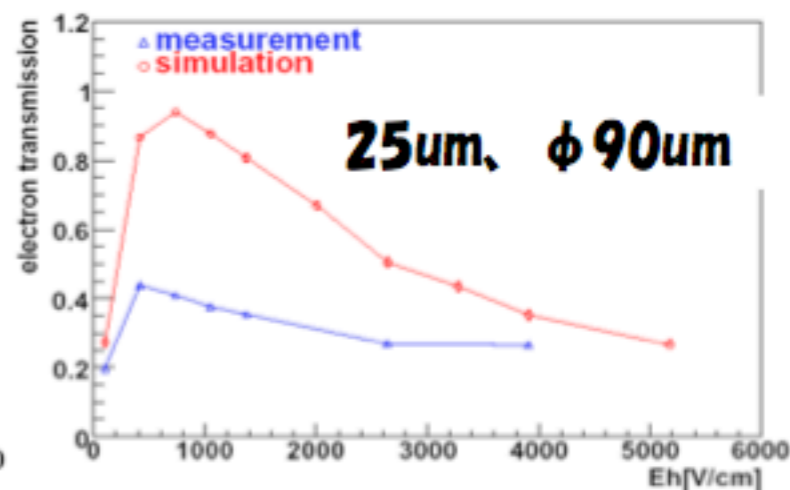
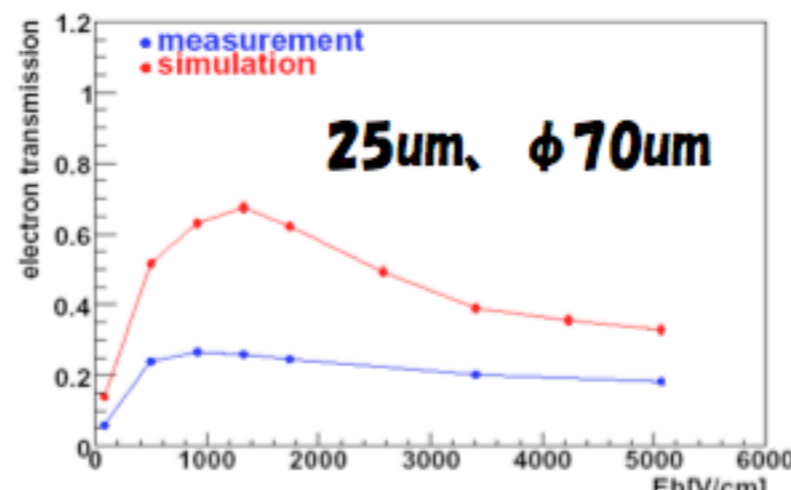
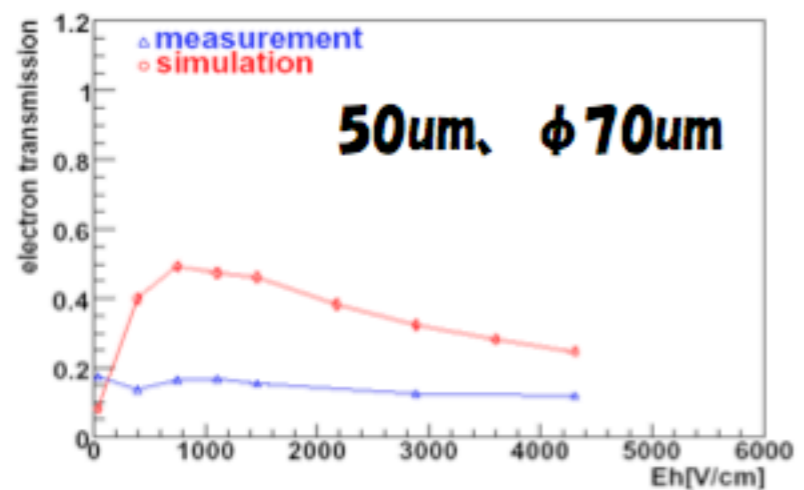
However it could not reproduce all of data
due to artificial parameter “step size”

Compare with simulation

1T



0T



Experimental result is not consistent with simulation without magnetic field.

Tokubetsu suisin allow us to use

ANSYS

which can fit to garfield++

which has no “step size”

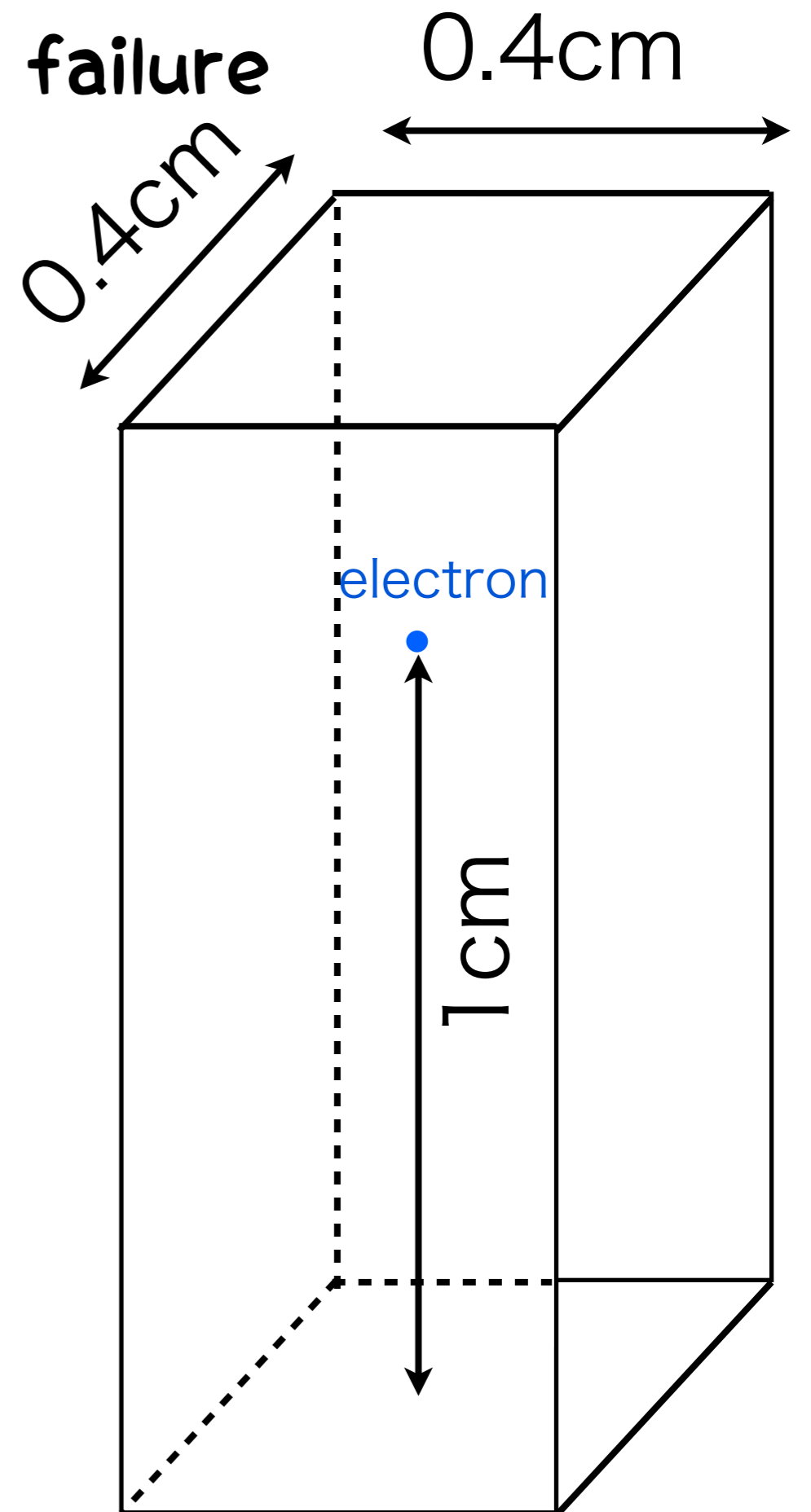
We have started to simulate old data again

In order not to go into the same failure

Validity check is important.

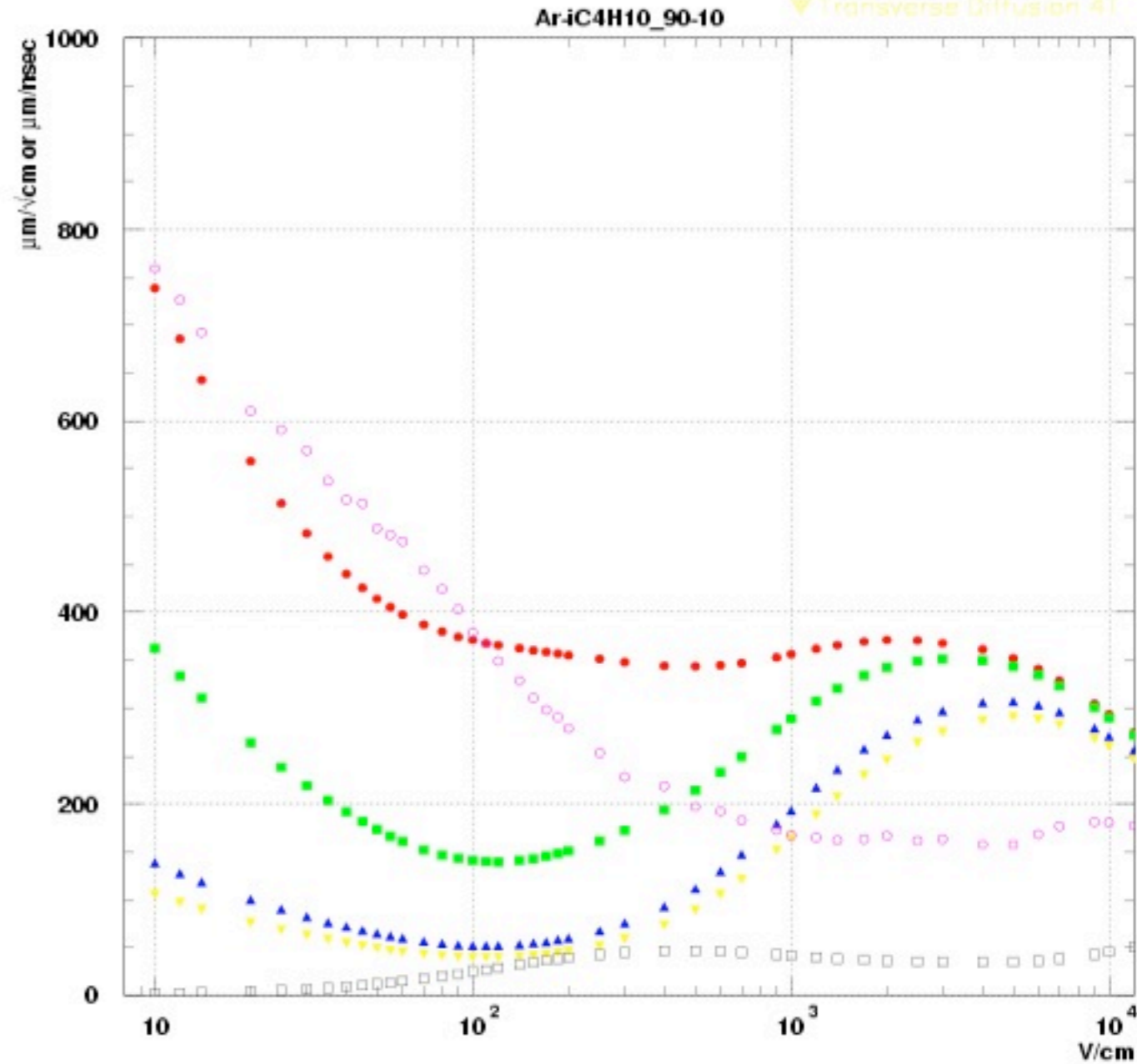
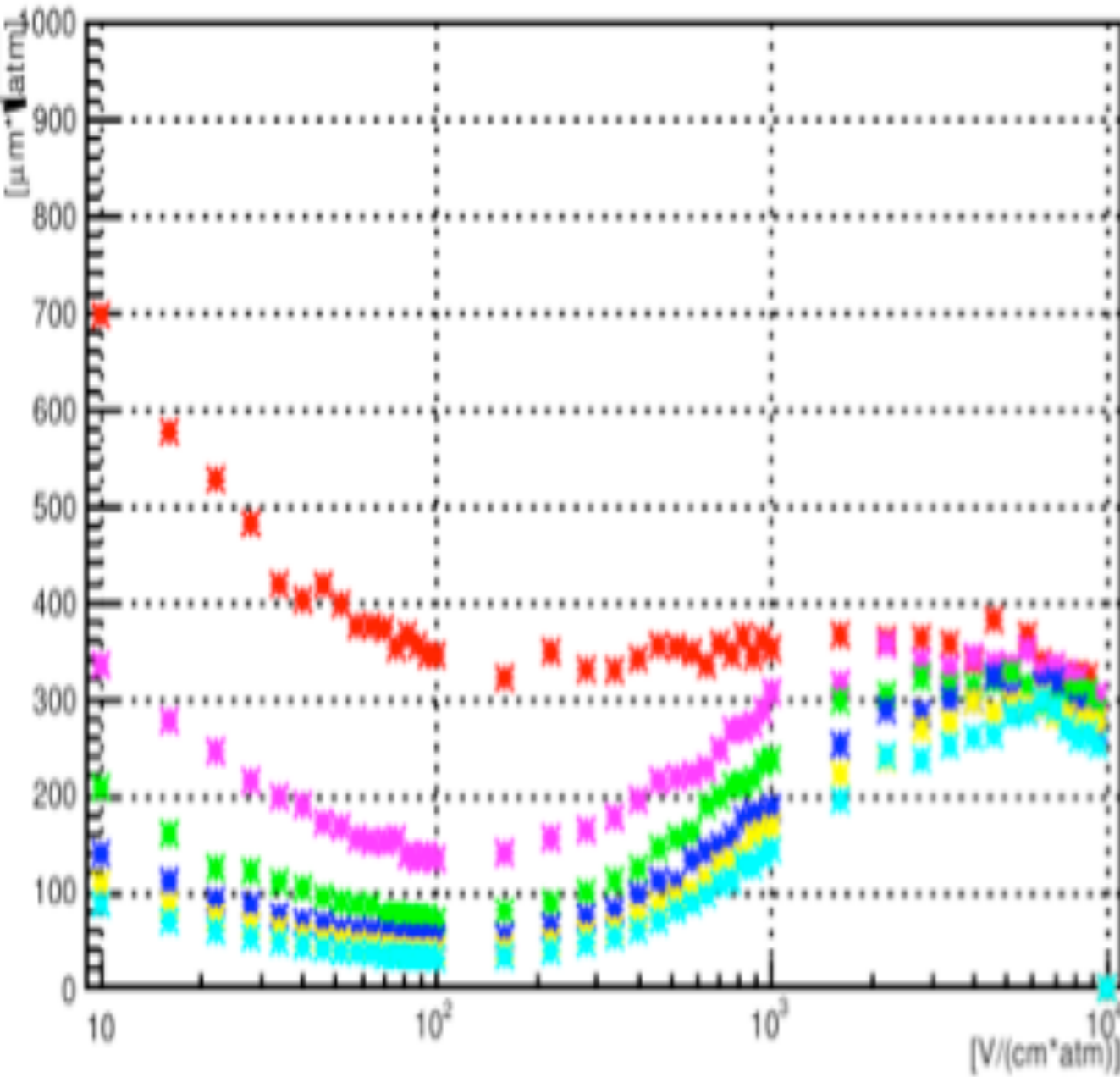
simple test of gas property
by garfield++ simulation

simulate diffusion under
various E/B fields



Garfield使ったsimulation

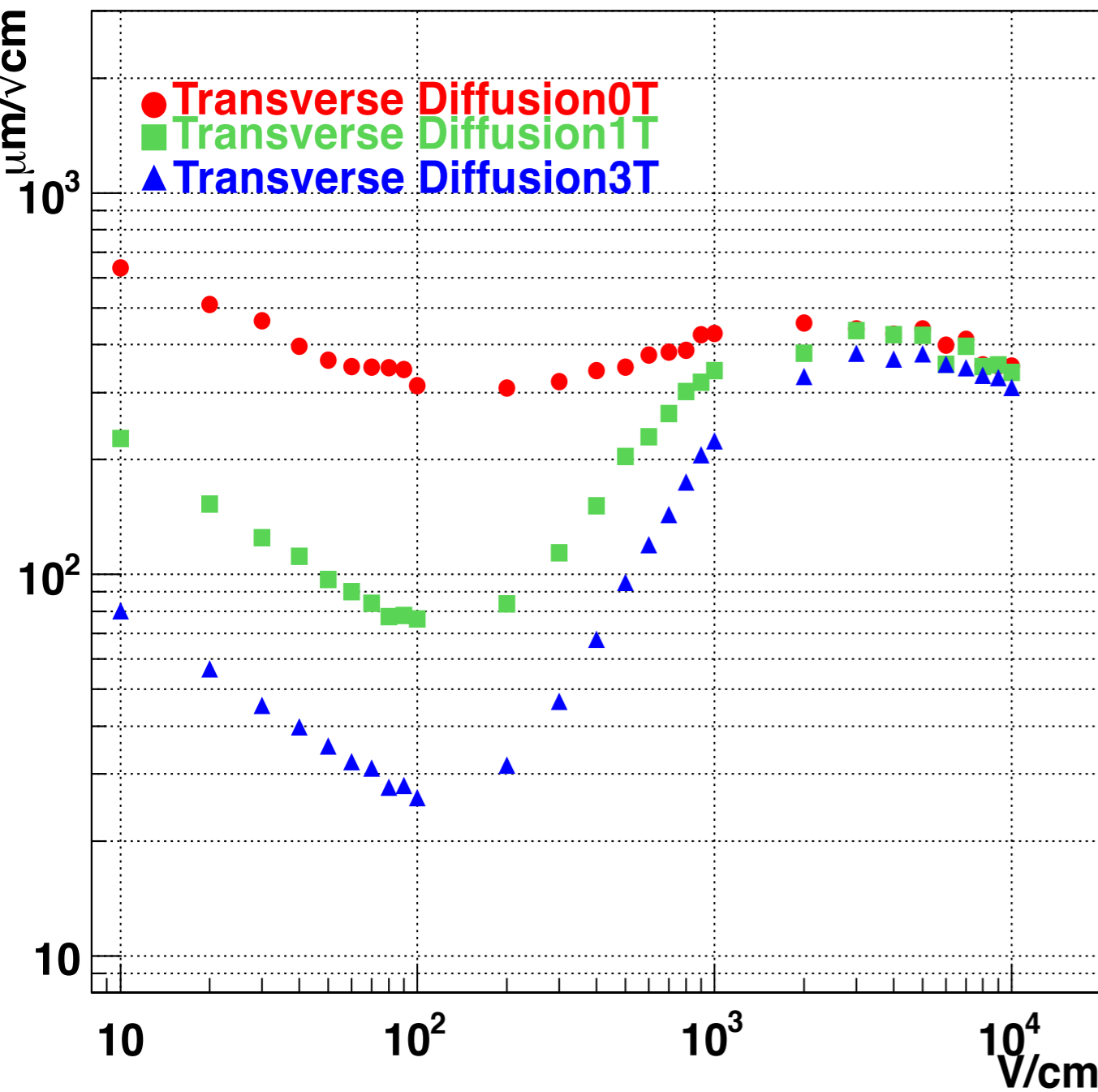
Garfield++使ったsimulation



ILC TPC Wiki -Gas Study with Magboltz-
<http://www-hep.phys.saga-u.ac.jp/ILC-TPC/gas/index.html>

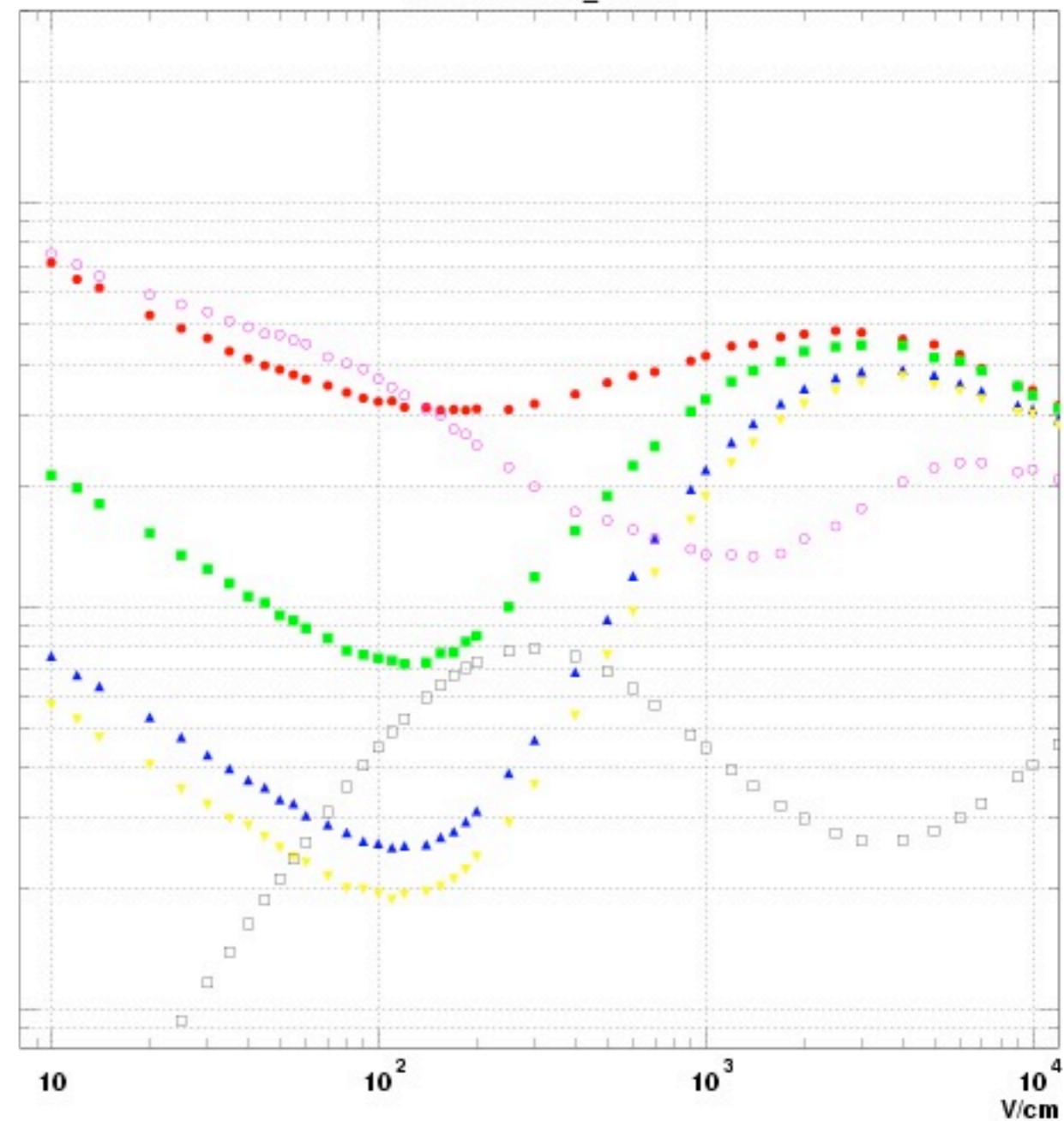
Diffusion

Garfield++使ったsimulation
Ar:CF₄:iC₄H₁₀=95:3:2



Garfield使ったsimulation

○ Longitudinal Diffusion
□ Drift Velocity
● Transverse Diffusion 0T
■ Transverse Diffusion 1T
▲ Transverse Diffusion 3T
▼ Transverse Diffusion 4T



ILC TPC Wiki -Gas Study with Magboltz-
<http://www-hep.phys.saga-u.ac.jp/ILC-TPC/gas/index.html>

garfield++を使ってgasの拡散の振る舞いを再現できた

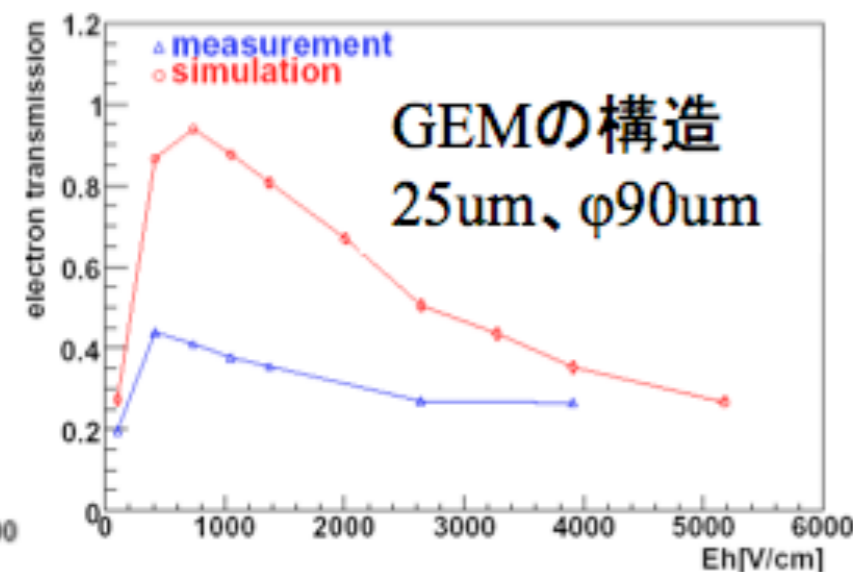
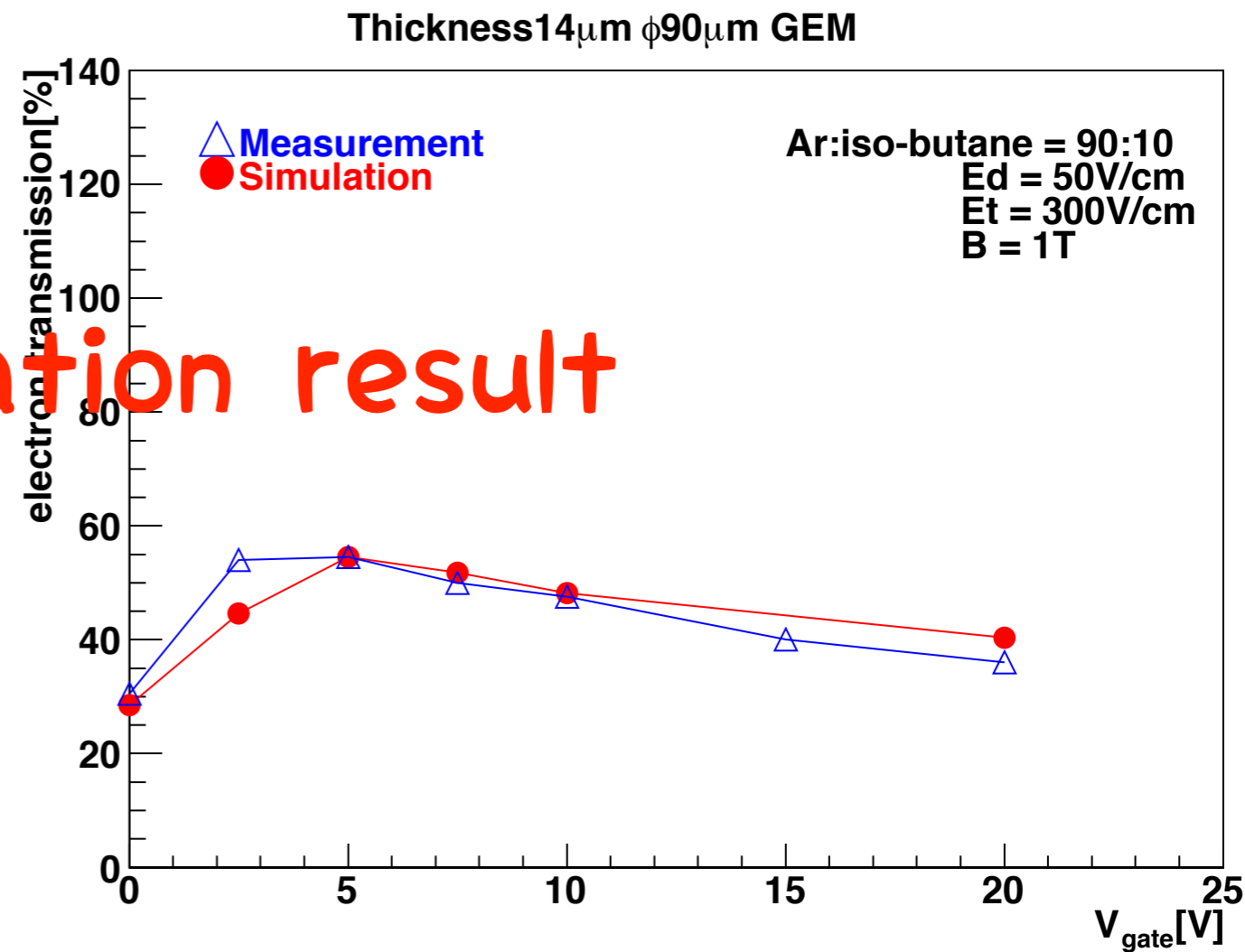
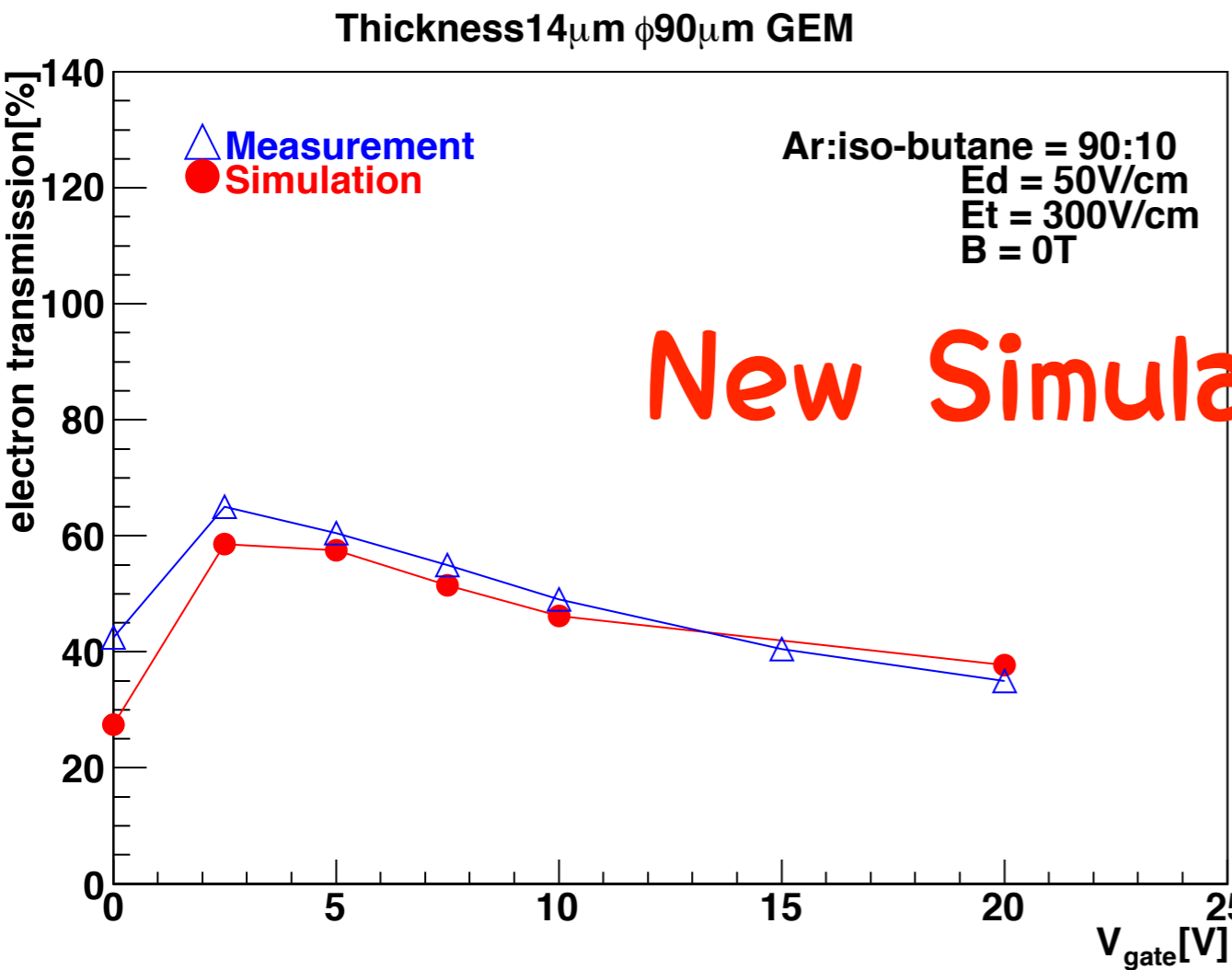
↓
Garfield++で電子の透過率をsimulationしてみる

Simulate Old data

with Garfield++ w/ ANSYS

Result

garfield++を使ったsimulationの結果



← garfieldを使ったsimulation (磁場なし) の結果

Old Simulation

→ 磁場なしでのsimulationの結果は測定結果の近くなっている。

Garfield++ w/ ANSYS

does work properly

We can use this for feasibility study of Gate GEM

though we've already expected

Gate must have large aperture as big as transmission efficiency
under high B field operation

it was just a guess but it became

ILCに使えるためのGate GEMの最適化

ILCで使用する予定のゲートは、3.5Tの磁場中にT2K gasでの中で動作させる。

またドリフト領域の電場は230V/cmとする

セットアップ条件

ドリフト領域の電場 230V/cm
ガス T2k(Ar:CF4:iC4H10=95:3:2)

← 他の要求からきまり自由に變更できない

トランスファー領域の電場 ←

ゲート～増幅GEM間の電場は調整可能だが、
高電場をかけると増幅GEMへの収集効率の低下を招く

銅部分5 μm → 1 μm

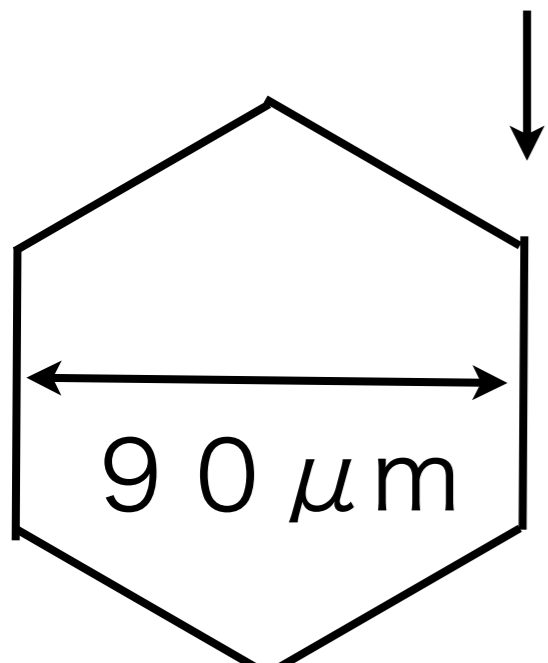
穴の径 = 90 μm

Thickness(絶縁体) = 10 μm ←

ゲートの厚さは調整可能なパラメーター
であり、薄ければ薄いほど製作が困難になる

Gate GEMの構造 Honeycombタイプ

↓
開口率を80%程度にした蜂の巣構造でsimulationしてみる



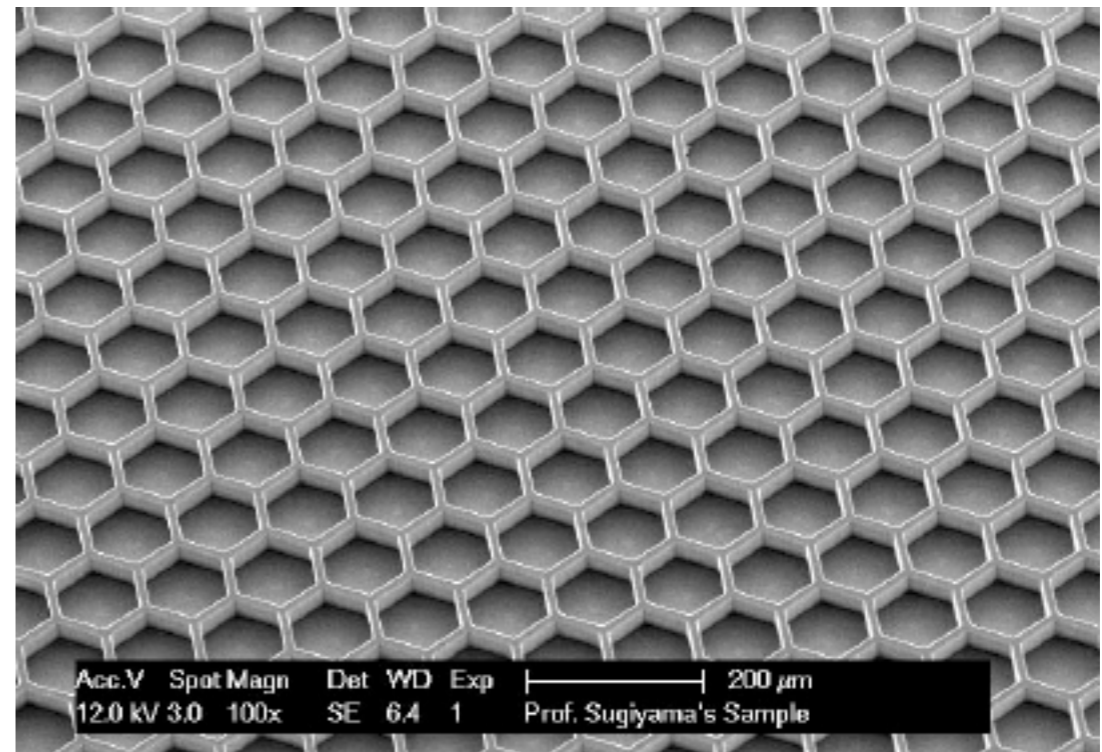
まずは、ansysで電場の計算をします

in order to achieve high aperture

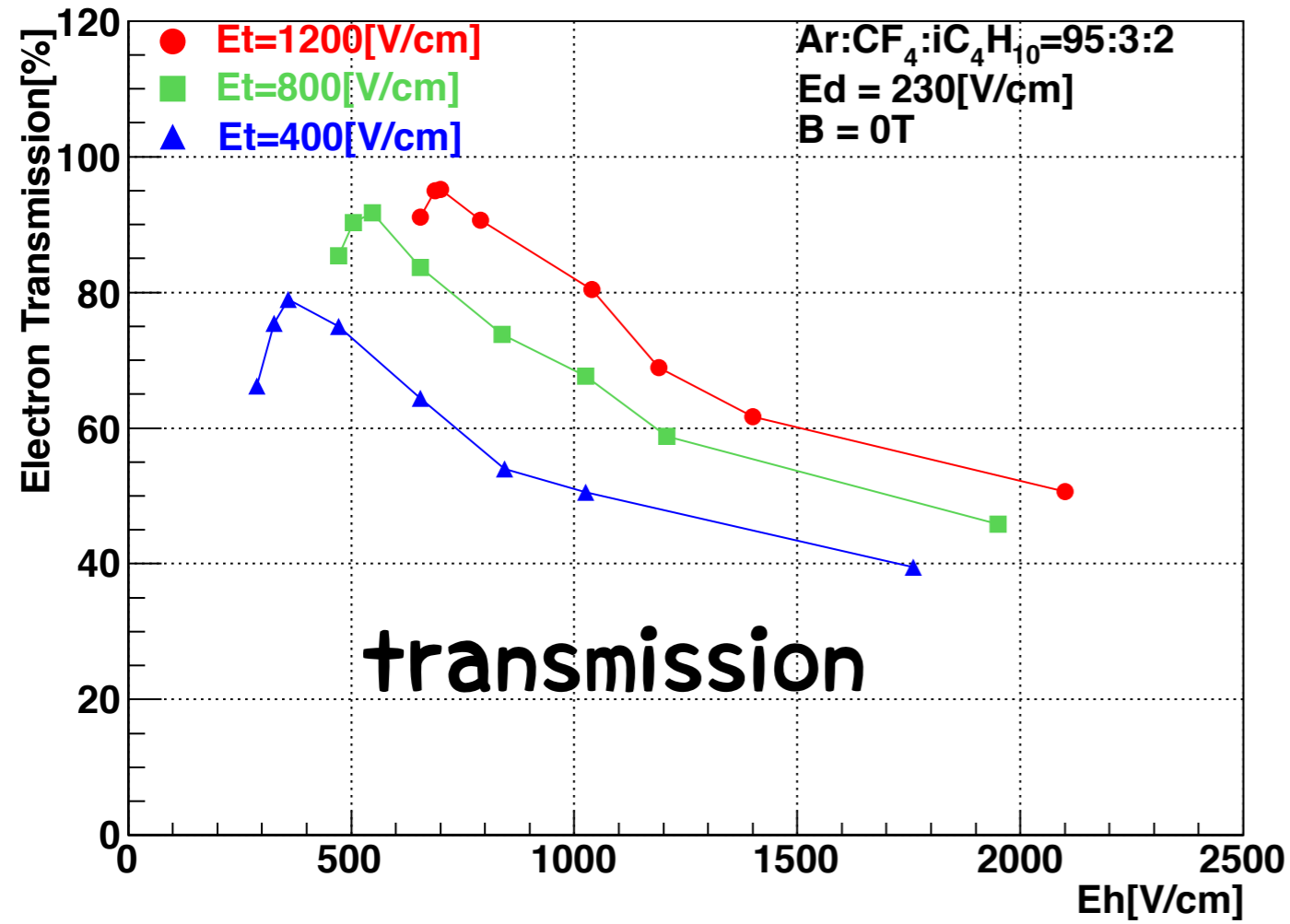
Honeycomb shape was chosen

to get 80% aperture

width of lib is 10 μm in 100 μm pitch



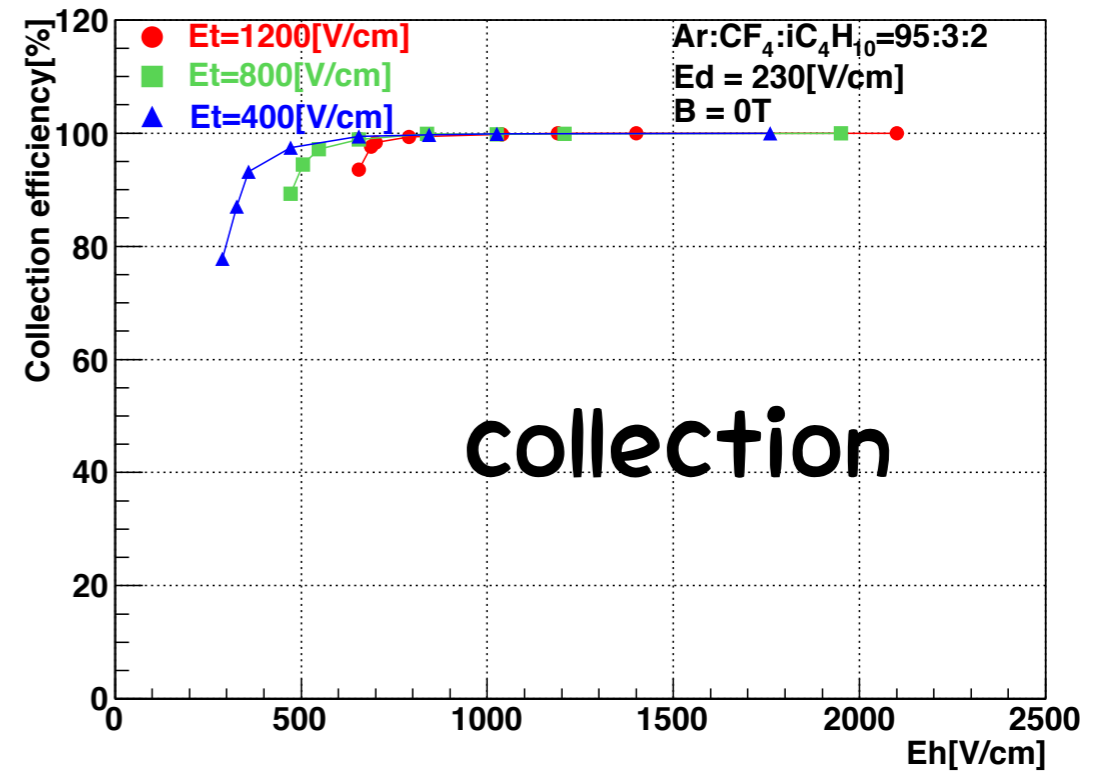
HoneycombThickness10 μ m



transmission

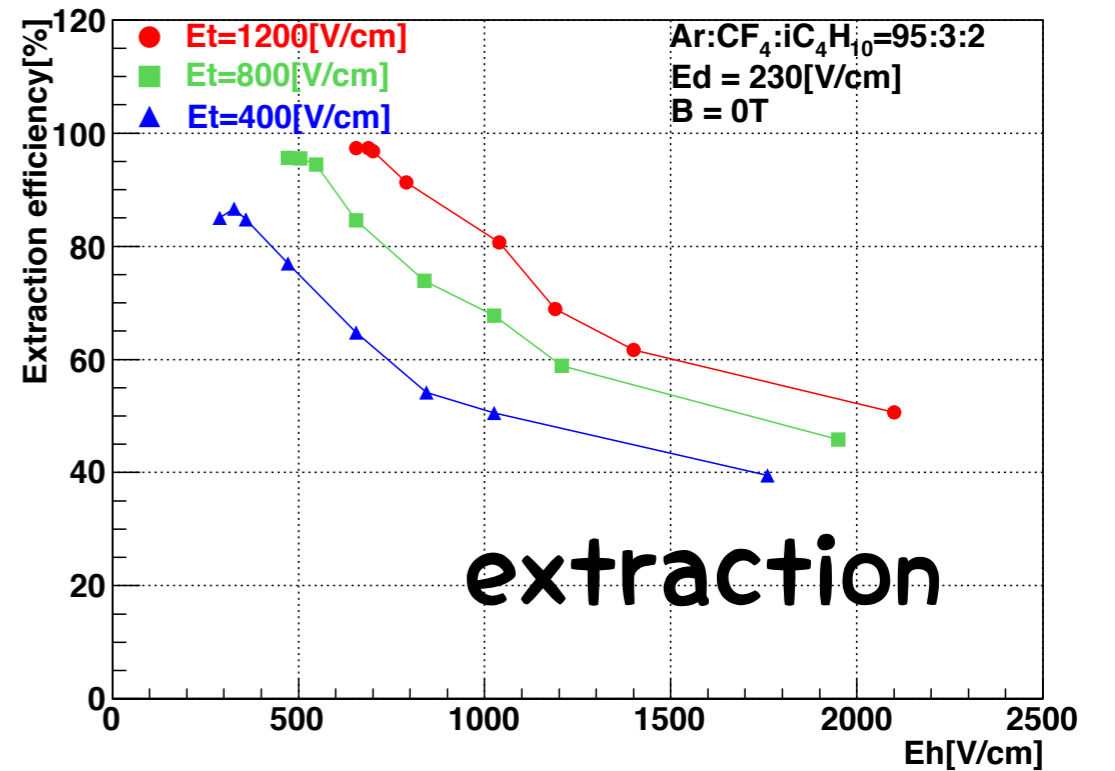
B=0T

HoneycombThickness10 μ m



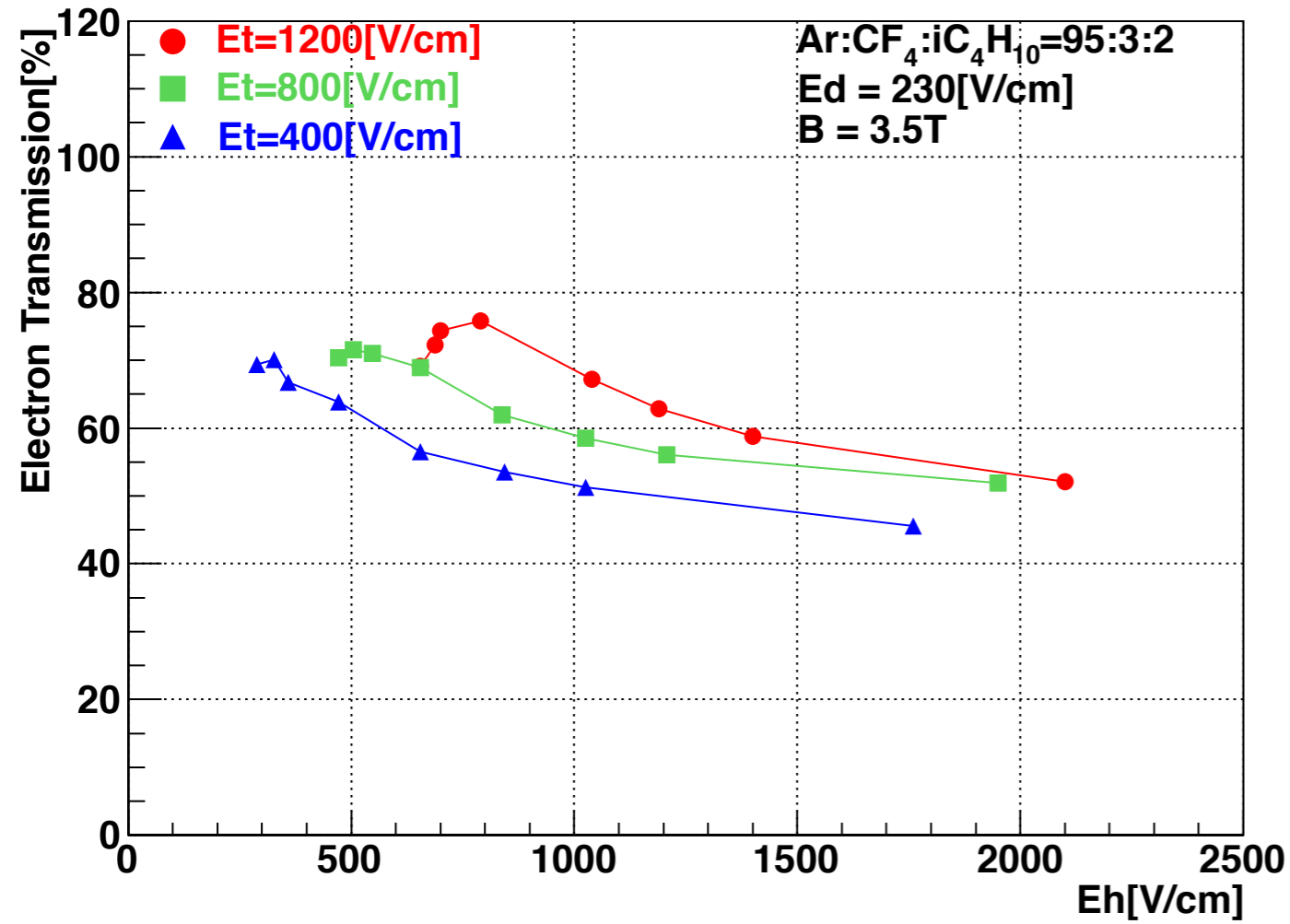
collection

HoneycombThickness10 μ m

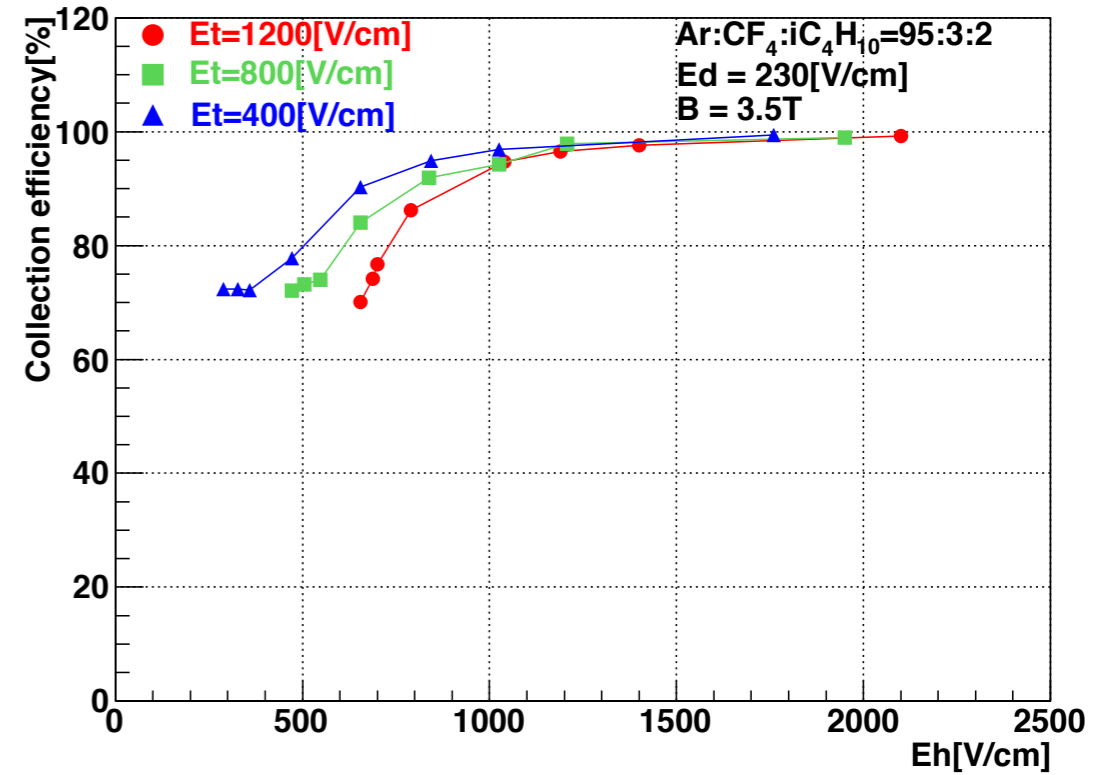


extraction

HoneycombThickness10 μ m

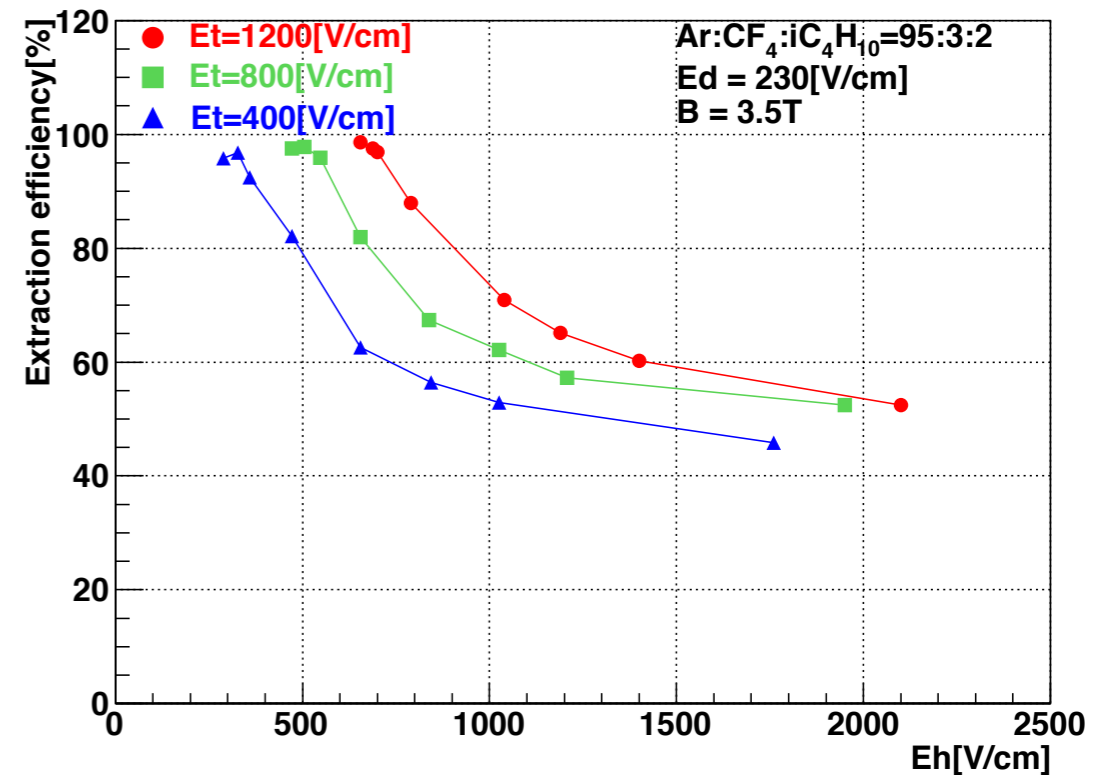


HoneycombThickness10 μ m

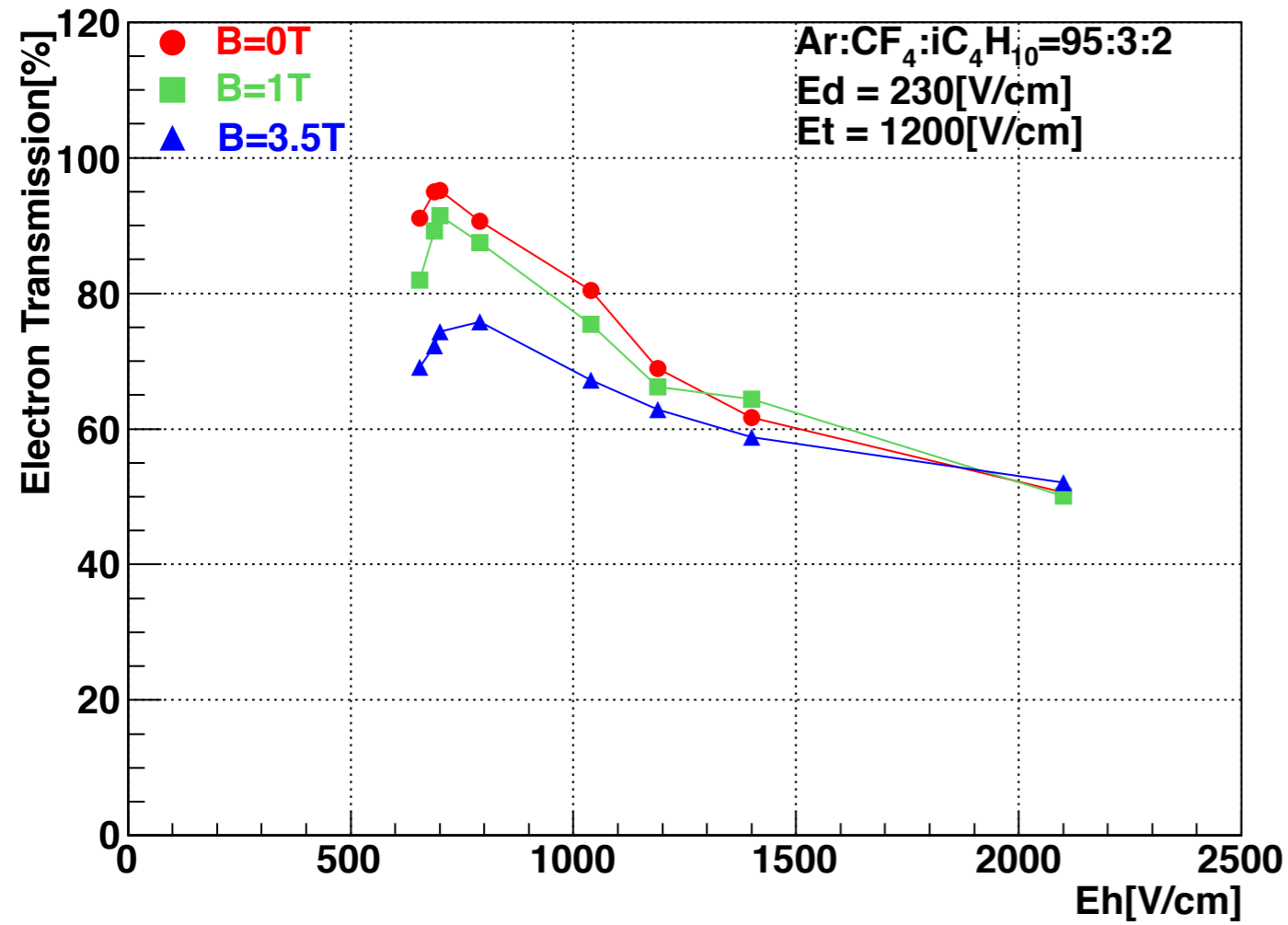


B=3.5 T

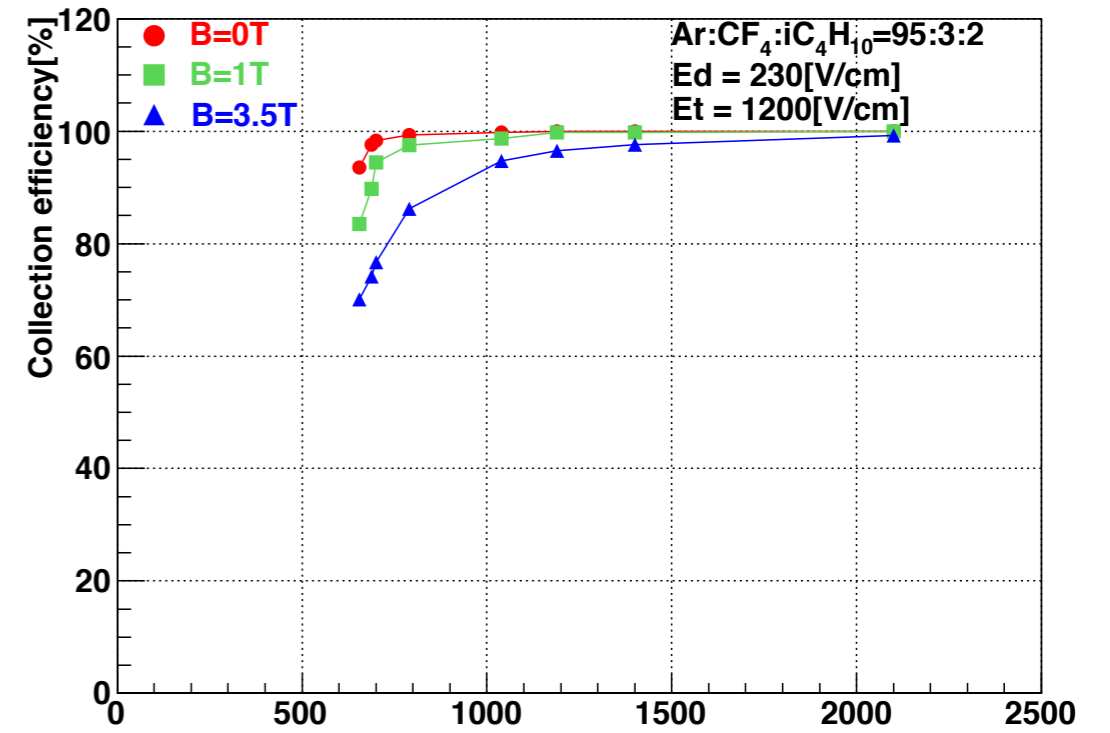
HoneycombThickness10 μ m



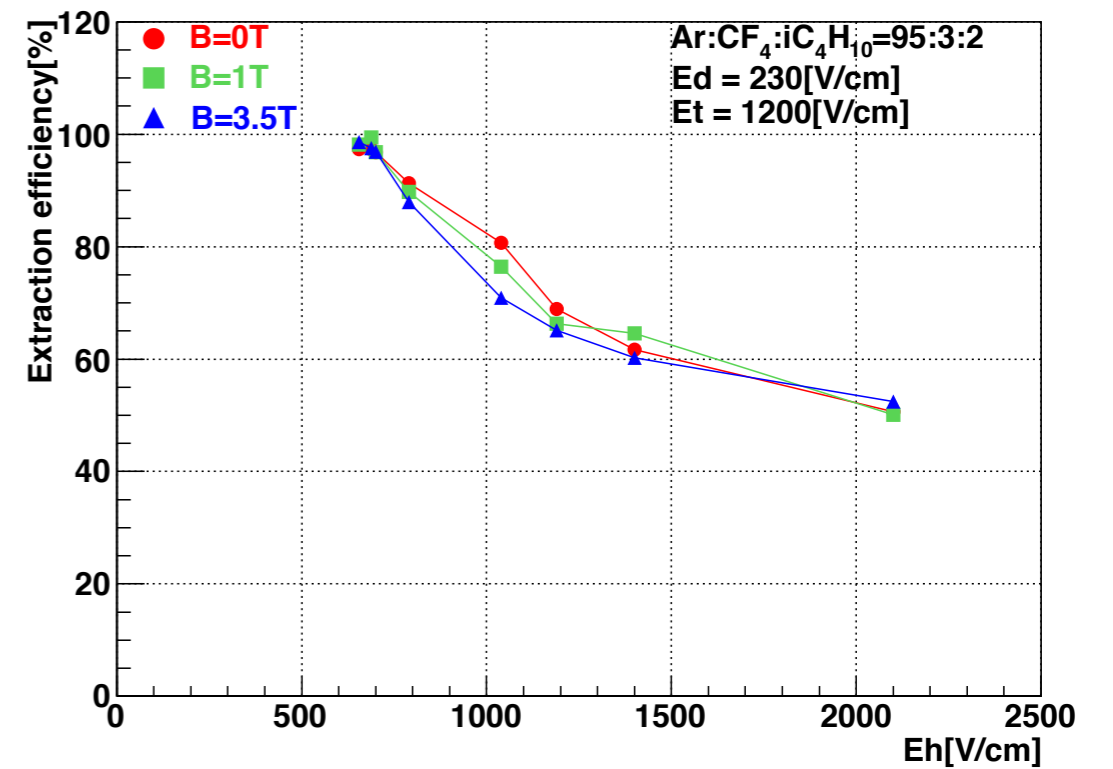
HoneycombThickness10 μ m



HoneycombThickness10 μ m



HoneycombThickness10 μ m



summary

Tools are prepared

Next thing to do is

find optimum condition
to provide

Higher transmission
without deteriorating resolution
so much

and find a method to produce this GEM