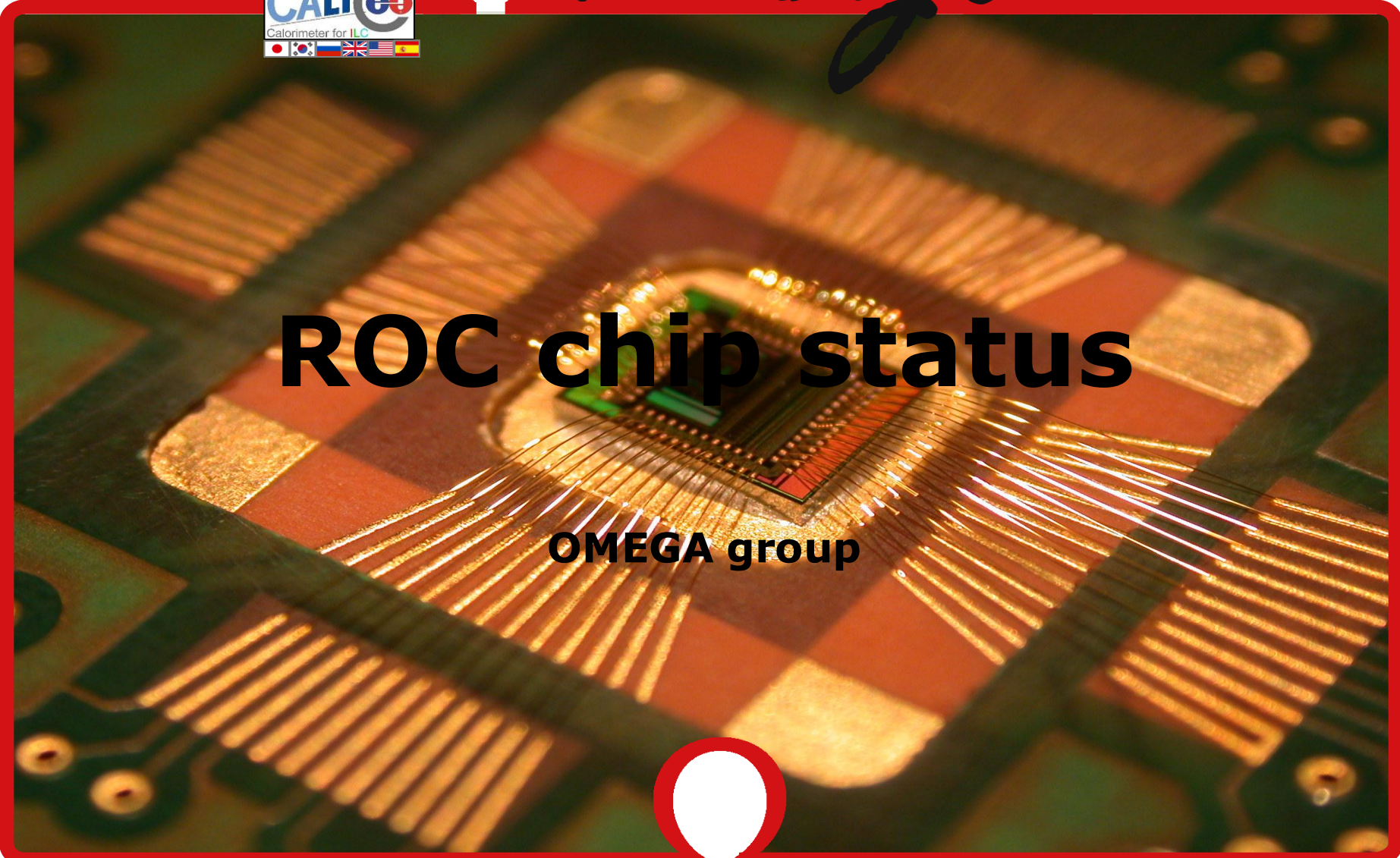
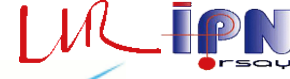




**Omega**



# ROC chip status

OMEGA group

*Orsay MicroElectronics Group Associated*

- OMEGA group (9 designers) is currently being reorganized in order to serve as a design center for IN2P3
  - ✓ OMEGA left LAL Orsay at the beginning of January and is hosted by LLR/Ecole Polytechnique (Palaiseau)
  - ✓ OMEGA will become an independent unity of IN2P3 in the coming months



- WeeROC: spin OFF from OMEGA/IN2P3 to design microelectronics for industrial applications (mainly spatial and medical applications). Created by Julien Fleury. Strong links between Weeroc and Omega



- Jean-Baptiste Cizel: PhD student (Weeroc/LLR), detailed measurements on Skiroc2 -> Skiroc3 design

- HARDROC3: was submitted at the end of Feb 2013 (SiGe 0.35 $\mu$ m), expected in June 2013
  - Die size  $\sim$ 30 mm<sup>2</sup>
  - 64 independent channels
  - I2C link (@IPNL)
  - PLL: integrated before in a building block, first measurements are very good
    - Input frequency 2.5 MHz =>output frequency: 10, 20, 40, and 80 MHz available
- Many measurements performed on testbench and at system level on SKIROC2 and on SPIROC2B and 2C to understand plane events, pedestal shifts, unexpected triggers

