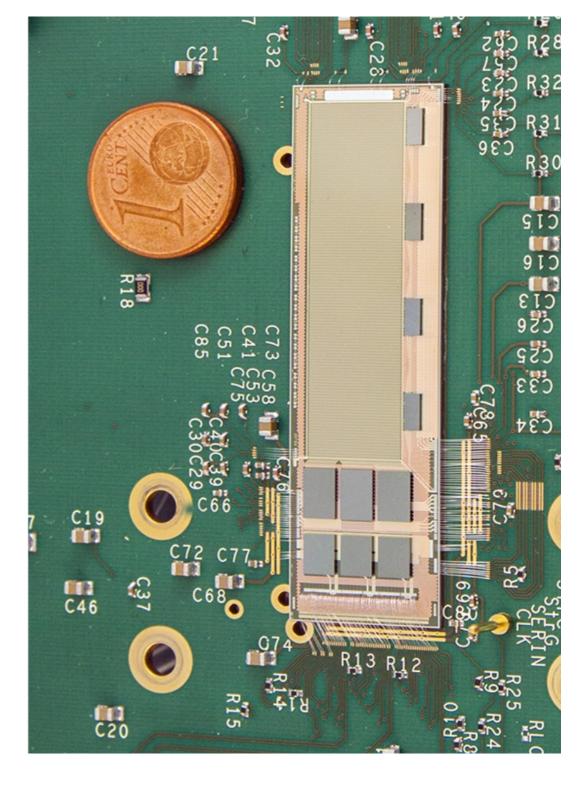
DEPFET vertex detector & Forward Tracking Disks

ILD meeting

Oshu, Japan, Sep. 2014

Marcel Vos (IFIC Valencia), for the DEPFET collaboration and the Spanish LC network

Thanks to F. Arteche, ITA, I. Garcia, IFIC, I. Vila, IFCA, M.A. Villarrejo, IFIC



FTD-pixels





FTD: detailed design

A detailed design for innermost Forward Tracking Disks (and VXD)

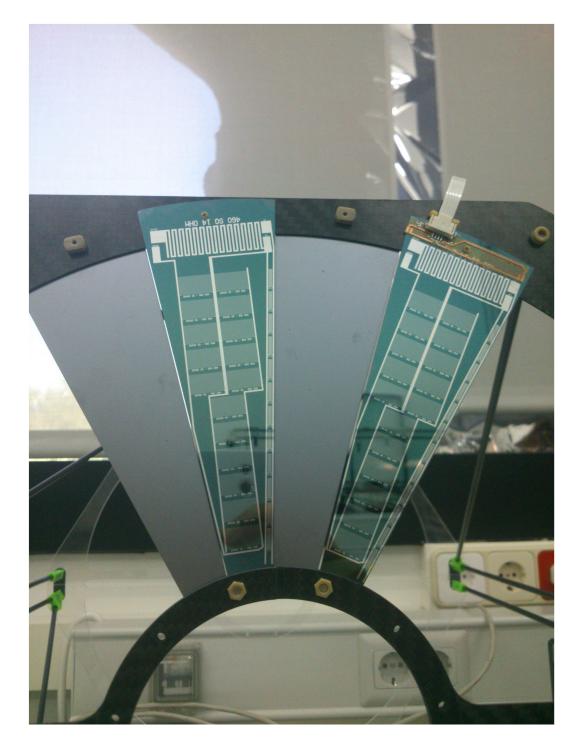
- \rightarrow end-of-ladder material (technology-specific)
- → cables & services (generic, inner-tracker level problem)
- \rightarrow feed back into simulation for increased realism



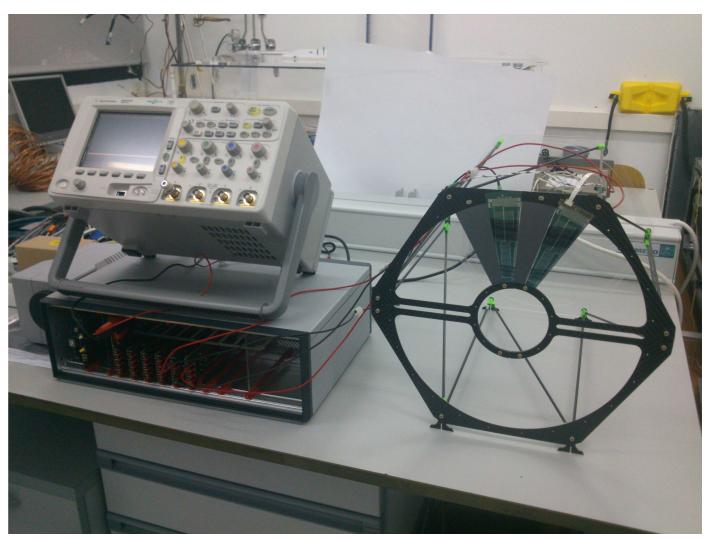
INSTITUTO DE FÍSICA CORPUSCULAR

M.A. Villarrejo (engineer at IFIC) needs ILD contact

FTD mock-up



Thermo-mechanical petals on FTD1 support structure



FTD1 and power-pulsing set-up

FTD mock-up

- disk 1 (pixels, IFIC Valencia)
- disk 3 (strips IFCA Santander)
- mechanical performance



INSTITUTO DE FÍSICA CORPUSCULAR

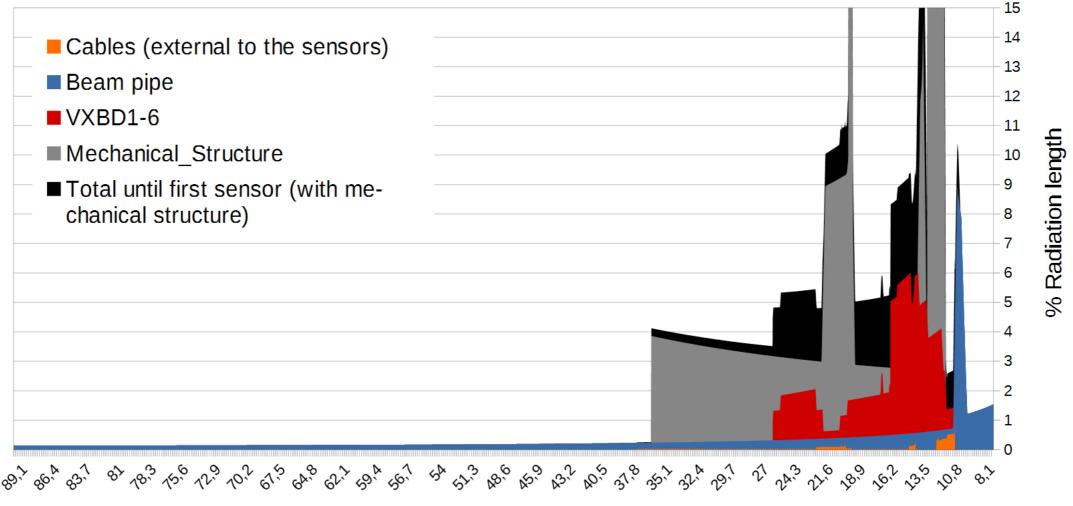


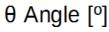
Detailed design



Where does the additional material go? How does it impact physics?

Radiation length contribution of the elements before the first sensitive layer





Design optimization

FTD-pixels has some long-standing design issues (see Marcel Demarteau's talk) Too few pixelated disks too far from the IP

Why haven't we sorted this out yet?

Any serious optimization needs realism \rightarrow inner-tracker coordination for supports and services Lack of pixel technology decision \rightarrow difficult to optimize "generic" design

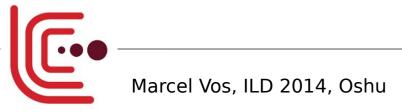
I propose to compare performance of alternative design:

- 5-6 pixel disk option as close as possible to VXD
(without cryostat, but with realistic constraints from end-of-ladder area, supports and cables)
- vary barrel length (current 25 cm and "shorter")

Can commit engineer to produce CATIA design and estimate material budget, need to interact with experts in GEANT4 implementation and analysis

FTD micro-strips

(7)



Micro-strips?

A proven technology for a difficult region

Solution for moderate-density forward region (|z| > 50 cm)

Single-BX read-out needed to provide a time-stamp for Silicon-only tracks

The idea of all-pixel tracking as suggested by M. Demarteau (and by Chris Damerell and myself years ago) seems more suited to central detector \rightarrow SIT in ILD (background free, backed by TPC)

Notoriously undermanned R&D effort in LC, but large community worldwide \rightarrow instrumentation of few m² is no problem

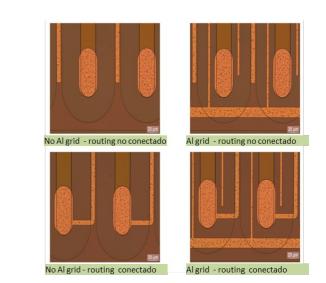


Micro-strip detector R&D

R&D focus on reducing material

Charge division read-out: 3D measurement from a single strip

 \rightarrow possible replacement of (false) double-sided microstrips. Second generation produced of charge-division microstrips sensors with integrated signal routing lines and signal isolation structures to avoid cross-talk \rightarrow good preliminary results



OLD IDEA (Radeka) NEW TECHNOLOGY (CNM in-house production of custom designs)

Further integration of components

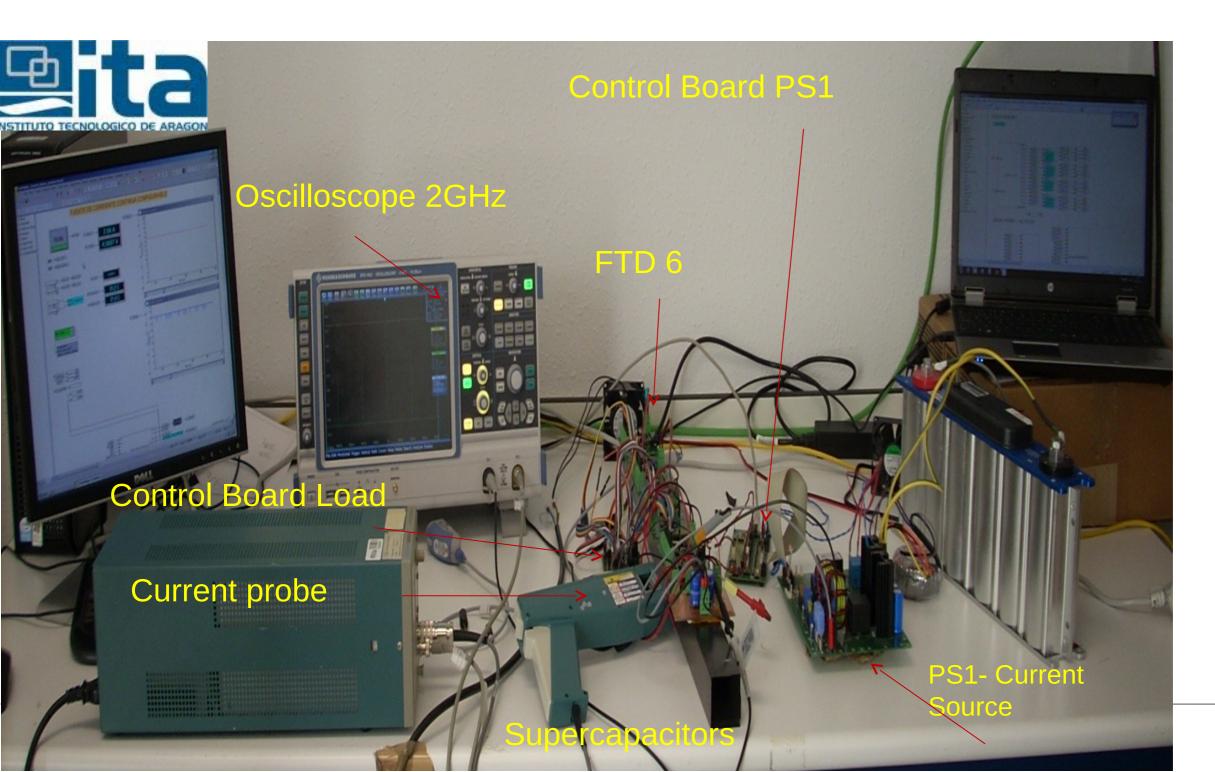
- \rightarrow integrated pitch adapter tested by HEPHY Vienna
- Active sensors
 - \rightarrow sensors with small gain investigated for HL-LHC (RD50, CNM/IFCA)



Power distribution

R&D on power distribution (power pulsing)

First power system demonstrator for a quarter of disk 6 (details in F. Arteche talk)

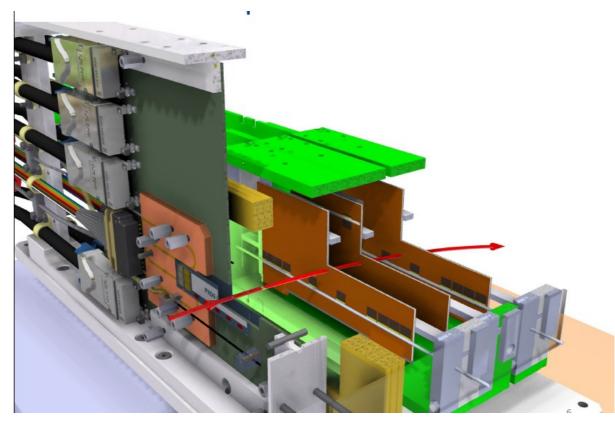


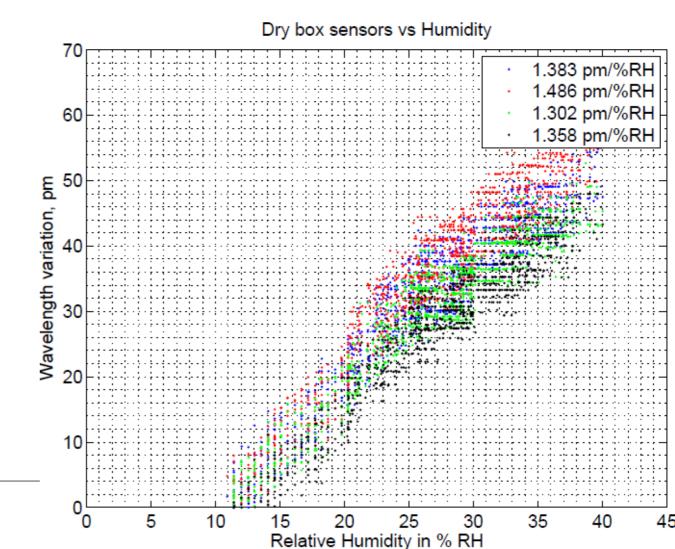


Bragg Fiber Environmental monitoring

Belle II PXD-SVD common test beam at DESY

- Two DEPFET pixel layers+ four SVD microstrip layers
- Nitrogen environment, CO2 cooling, magnetic field
- Little experiment, monitoring found to be reliable
- Establishing humidity monitoring
- capabilities of "naked" fibres
- Excellent linearity and sensibility after
- temperature compensation





Marcel Vos, ILD 2014, Oshu

Summary

FTD R&D

- thermo-mechanical studies of pixel petals, advances in micro-strips and monitoring

FTD detailed design

- go well beyond wireless detectors floating in mid-air (+ tech-specific petals)

FTD optimization

- strengthen pixelated disks (2 \rightarrow 5/6) and bring inward \rightarrow working to provide design

